



## Senior CAD Engineer Behavioral Modelling and Mixed-Signal Verification (f/m/div)\*

### Job description

Infineon's engineers develop state-of-the-art semiconductor solutions. This is only possible with a cutting-edge design flow and the best possible methodology as a basis. Do you want to lay the groundwork for how Infineon's products of the future are optimized for robustness, power efficiency and highest performance? In this position, you will be able to let your innovations run wild and develop and deliver the best tools for our executive design engineers. With this position, you will enter our "Technical Ladder" career path. The Technical Ladder is a special career path for those who share innovative ideas, demonstrate comprehensive technical knowledge, show thought leadership, possess problem solving abilities and are able to create business value.

In your new role you will:

- **Act as CAD and methodology engineer** for behavioral modelling and/or analog /mixed-signal verification
- **Work inside your CAD team on methodology or tool improvements** for verification of Infineon IC developments
- **Contribute to evaluation of tools und methodology on the market**, based on the aligned set of requirements, drive required improvements
- **Develop own solutions** (Software) together with the project team, in case EDA market can not cover the defined requirements
- **Integrate updates and new tools & methods** into the Infineon Designs System and Flow
- **Apply your tools and methods** inside Infineon circuit development projects to further improve the solution
- **Document, develop and hold trainings** for your customers
- **Support your customers** in case of issues and act as central contact for developed solutions inside Infineon

### Profile

You have a clear notion of how innovation contributes to the success of the company and you enjoy to push ideas to their full implementation and application. You are personally committed to the customer's concerns and requirements and award them a high priority.

You are best equipped for this task if you have:

- **A degree** in Electrical Engineering, Physics, Computer Science or similar

### At a glance

Location: **Munich (Germany)**  
Job ID: **346792**  
Start date: **as soon as possible**  
Entry level: **1-3 years**  
Type: **Full time**  
Contract: **Permanent**

Apply to this position online by following the URL and entering the Job ID in our job search:

Job ID: **346792**  
[www.infineon.com/jobs](http://www.infineon.com/jobs)

### Contact

**Daniel Lichtblau**  
Talent Attraction Manager



- **First experience** in the field of Mixed-Signal (analog + digital) Design Verification
- **Knowledge in Hardware Description Languages** and behavioral modelling (RNM, SystemVerilog, VHDL)
- **Software Development experiences**
- **Insights into the latest technical developments** as well as the Electronic Design Automation (EDA) tool landscape
- **Good English and German** language skills

## Benefits

- **Munich:** Coaching, mentoring networking possibilities; Wide range of training offers & planning of career development; International assignments; Different career paths: Project Management, Technical Ladder, Management & Individual Contributor; Flexible working conditions; Home office options; Part-time work possible (also during parental leave); Sabbatical; On-site creche and kindergarden with 120 spots, open until 6pm; Holiday child care; On-site social counselling and works doctor; Health promotion programs; On-site gym, jogging paths, beachvolleyball, tennis & soccer court; On-site canteen; Private insurance offers; Wage payment in case of sick leave; Corporate pension benefits; Flexible transition into retirement ; Performance bonus; Reduced price for public transport and very own S-Bahn station; Access for wheelchairs

## Why Us

**Part of your life. Part of tomorrow.**

Infineon is a world leader in semiconductor solutions that make life easier, safer, and greener. Our solutions for efficient energy management, smart mobility, and secure, seamless communications link the real and the digital world.

The central R&D organization „**Design Enabling and Services**“ (DES) provides the design environment to the different Infineon product development teams. With state-of-the-art design methods, building blocks and a wide range of product development services DES supports Infineon's advanced IC development from early high level system models to verified products ready for manufacturing.

*\* The term gender in the sense of the General Equal Treatment Act (GETA) or other national legislation refers to the biological assignment to a gender group. At Infineon we are proud to embrace (gender) diversity, including female, male and diverse.*

