Senior Staff Engineer - Digital IC Design

Job description

You will be responsible for delivering high-quality RTL coding, verification and synthesis. You will also be in charge of the concept, DFT and design of the IP assigned.

In your new role you will:

- Responsible to deliver **high-quality RTL coding, verification and synthesis on area of work.**
- Responsible for the **concept, DFT and design of the IP** assigned.
- Tightly working with **SOC Layouters and the analog/software team.**
- Perform full **pre and post-layout IP and Fullchip verifications.**
- Implement **digital design in FPGA** and **perform the FPGA debugging.**
- Exposure to **all stages of IC Design processes and flow.**
- Continuous improvement on **digital design methodologies and flow.**
- Define **silicon bring up plan** and **component validation of 1st silicon.**
- Define **IP /Chip micro-architecture** for design reviews in the project.

Profile

You are best equipped for this task if you have:

- Good knowledge in **digital design techniques** and the state-of-the-art design verification method.
- Solid fundamentals in IC design flow and verification concepts.
- Experience with **RTL2GDS, floorplanning, static timing analysis, timing closure and verification**
- Hands-on exposure on of **FPGA, formal verification and power analysis.**
- Knowledge in **ACDC SMPS IC design-related** will be an advantage.
- Good in scripting using **PERL/PYTHON**
- **Exposure to Cadence and Synopsys tools**
- Experienced in DFT scan tools and test mode concepts will be an advantage.
- Able to communicate well and good in interpersonal skills
- Strong analytical and problem-solving skills

Why Us

Part of your life. Part of tomorrow.

We make life easier, safer and greener – with technology that achieves more, consumes less and is accessible to everyone. Microelectronics from Infineon is the key to a better future. Efficient use of energy, environmentally-friendly mobility and security in a connected world – we solve some of the most critical challenges that our society faces while taking a conscientious approach to the use of natural resources.

In accordance with the requirements set by the Singaporean Government, Infineon Technologies Asia Pacific Pte Ltd ("Infineon") can only allow individuals who are (a) fully vaccinated, (b) certified to be medically ineligible for a vaccine or (c) have recovered from COVID-19 within a prescribed period, onto company premises. Therefore, Infineon requires all new employees, as well as contractors and business partners, to be fully vaccinated against COVID-19. "Fully vaccinated" means individuals have completed the full regime of an approved COVID-19 Vaccine as listed under the World Health Organization (WHO) Emergency Use Listing (EUL) including the respective post-vaccination period to ensure the vaccine has become full effective. Anyone who is unable to be vaccinated due to an approved and/or recognised exemption condition may apply for special consideration.