



Senior Staff Engineer - Analog IC Design

Job description

We are looking for a Senior Staff Engineer, Analog IC Design to perform the role of Technical Lead for new complex IC design projects with our Power & Sensor Systems division.

In your new role you will:

- Perform the role of **Technical Lead** for new complex IC design projects.
- Take charge of key Analog IP developments such as **high voltage startup circuit with JFET or Depl-MOSFET, sigma-delta ADC, class AB driver, pulse/coreless transformer transceiver and DCDC converter (Boost, Buck, Buck-Boost, Charge-pump and etc) with emphasis on high voltage AC/DC applications.**
- Engage with System Application Team to **establish design requirements and IP specifications**, experienced in defining IC architecture, IC concepts and able to develop IC Design internal target specifications.
- **Perform comprehensive system-Level analysis and evaluation, detailed competitor analysis at IC design level, full-chip architecture design and modelling.** Responsible for all activities ranging from architecture and design to silicon bring up and characterization.
- **Expert and hands on in Analog Mixed Signal IC Design**, good knowledge in **high voltage and high driving current design, ESD & Latch-up** will be an added advantage.
- **Good knowledge in power semiconductors (SMPS) systems, technologies, topologies and applications.**
- Demonstrated leadership in IC design and ability to guide junior designers.
- Able to **provide effective technical SMPS IC support** and advice across development teams and business partners.
- Responsible in advising **layouts on chip /IP layout** for optimal performance and able to develop re-usable Analog IP.
- Drive and deliver **IC design-related documentations** and design reviews.

Profile

You are best equipped for this task if you have:

- Excellent Analog Mixed Signal IC Design knowledge **min 10 years experience.**
- **Proven expertise in SMPS IC design and in SMPS system topologies (Flyback, CCM, DCM, PFC, LLC)**
- Advantage if candidate has past technical patents and publications in related area (Analog Design / ACDC SMPS IC/Systems)

At a glance

Location: **(Singapore)**
Job ID: **346744**
Start date: **as soon as possible**
Entry level: **5+ years**
Type: **Full time**
Contract: **Permanent**

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- In-depth knowledge of ADCs, charge pumps, LDOs, drivers, HV-technology experience, oscillators.
- Solid understanding in state of the art IC development flow/methodologies/tools .
- Experienced in leading technical negotiations with external customers/internal experts.
- Strong hands-on troubleshooting skills and debugging experience.
- Strong teamplayer, detail, good communication and presentation skills at all levels, results-oriented, innovative, effective technical thought leadership

Why Us

Part of your life. Part of tomorrow.

We make life easier, safer and greener – with technology that achieves more, consumes less and is accessible to everyone. Microelectronics from Infineon is the key to a better future. Efficient use of energy, environmentally-friendly mobility and security in a connected world – we solve some of the most critical challenges that our society faces while taking a conscientious approach to the use of natural resources.

In accordance with the requirements set by the Singaporean Government, Infineon Technologies Asia Pacific Pte Ltd ("Infineon") can only allow individuals who are (a) fully vaccinated, (b) certified to be medically ineligible for a vaccine or (c) have recovered from COVID-19 within a prescribed period, onto company premises. Therefore, Infineon requires all new employees, as well as contractors and business partners, to be fully vaccinated against COVID-19. "Fully vaccinated" means individuals have completed the full regime of an approved COVID-19 Vaccine as listed under the World Health Organization (WHO) Emergency Use Listing (EUL) including the respective post-vaccination period to ensure the vaccine has become full effective. Anyone who is unable to be vaccinated due to an approved and/or recognised exemption condition may apply for special consideration.

