



Senior Engineer Mask Design

Job description

Job Description :

- 1) Participate in block/IP floor planning from scratch, performing routing/layout verification.
- 2) Conduct thorough layout design reviews internally as well as external review with circuit designers.
- 3) Extensive use of CAD Tools (Cadence virtuoso & Mentor caliber).
- 4) Capable in guiding/coaching junior engineers for on time delivery and quality.

Profile

Job Specification :

- 1) Bachelor degree in Electrical/Electronic Engineering/Physics with VLSI exposure of equivalent 4-6 years of job experience in layout design.
- 2) Hands on experience in analog layout from scratch.
- 3) Deep understanding of analog circuit; layout concepts in CMOS technology.
- 4) Possess strong technical, analytical & problem solving skills in layout design.
- 5) Ability to work as a strong team player and participate in cross functional activities
- 6) Good interpersonal, verbal and communication skills, with good initiative at work

Why Us

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We make life easier, safer and greener – with technology that achieves more, consumes less and is accessible to everyone. Microelectronics from Infineon is the key to a better future. Efficient use of energy, environmentally-friendly mobility and security in a connected world – we solve some of the most critical challenges that our society faces while taking a conscientious approach to the use of natural resources.

At a glance

Location: **Penang (Malaysia)**
Job ID: **345113**
Start date: **as soon as possible**
Entry level: **5+ years**
Type: **Full time**
Contract: **Permanent**

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