



Senior Engineer Device Characterization and Modeling (f/m/div)*

Job description

Do you enjoy being part of a team of experts working on the edge of technology? Diving into the previously unexplored and coming up with pragmatic solutions? Then join us as Senior Engineer Device Characterization and Modeling and assess the electrical characteristics of devices within CMOS or GaN technologies and derive complete sets of compact models. With this position you will be entering our „technical ladder“: our career path for experts. You will be able to focus deeply on technology, model and design – while further pursuing your career. As you continually expand your expert knowledge, puzzle over matters and solve problems, you will position yourself as a thought leader – thus helping our products improve our world a bit more each day.

In your new role you will:

- **Checking test structures on test-chips**, as well as optimizing the layout of particularly critical structures
- **Defining new structures needed to characterize new technology effects** (e.g. proximity effects, stress effects, ...)
- **Characterizing and modeling of CMOS or GaN components, as well as noise modeling**
- **Modeling corner models based on targets and tolerances** including deduction of a modeling methodology for the nominal and corner modeling
- **Verification of models** (model hardware correlation)
- **Independently handling methodological projects for optimizing measurement technology** and test structures

Profile

Based on your will to deliver highest quality, you enjoy collaborating with your team members, since the different areas of expertise and skills guarantee better results. Furthermore, you are very proactive, act independently on your own responsibility and always keep your customers in mind, which enables you to anticipate even their future needs. You feel committed to and keep your promises to the customer and focus your efforts on finding solutions that offer added value for both sides.

You are best equipped for this task if you have:

- A degree in physics, microelectronics or a related field
- At least 3 years of subject-specific experience, e.g. in DC, AC measurement, ICCAP programming, compact model development, semiconductors and device physics

At a glance

Location: **Munich (Germany)**
Job ID: **344559**
Start date: **as soon as possible**
Entry level: **1-3 years**
Type: **Full time**
Contract: **Permanent**

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Contact

Bari Guritno
Talent Attraction Manager



- Basic knowledge of PDK structures
- Profound knowledge of programming languages (e.g. Python, R)
- Experience in dealing with Windows/UNIX environments
- Good skills in dealing with MS-Office
- Fluent English skills, German language would be beneficial

Benefits

- **Munich:** Coaching, mentoring networking possibilities; Wide range of training offers & planning of career development; International assignments; Different career paths: Project Management, Technical Ladder, Management & Individual Contributor; Flexible working conditions; Home office options; Part-time work possible (also during parental leave); Sabbatical; On-site creche and kindergarden with 120 spots, open until 6pm; Holiday child care; On-site social counselling and works doctor; Health promotion programs; On-site gym, jogging paths, beachvolleyball, tennis & soccer court; On-site canteen; Private insurance offers; Wage payment in case of sick leave; Corporate pension benefits; Flexible transition into retirement ; Performance bonus; Reduced price for public transport and very own S-Bahn station; Access for wheelchairs

Why Us

Part of your life. Part of tomorrow.

We make life easier, safer and greener – with technology that achieves more, consumes less and is accessible to everyone. Microelectronics from Infineon is the key to a better future. Efficient use of energy, environmentally-friendly mobility and security in a connected world – we solve some of the most critical challenges that our society faces while taking a conscientious approach to the use of natural resources.

The central R&D organization „**Design Enabling and Services**“ (DES) provides the design environment to the different Infineon product development teams. With state-of-the-art design methods, building blocks and a wide range of product development services DES supports Infineon's advanced IC development from early high level system models to verified products ready for manufacturing.

** The term gender in the sense of the General Equal Treatment Act (GETA) or other national legislation refers to the biological assignment to a gender group. At Infineon we are proud to embrace (gender) diversity, including female, male and diverse.*

