



Staff Engineer Design Verification

Job description

We are currently seeking talented Staff Engineers Design Verification who want to join a dynamic and experienced team and take their technical knowledge to the next level in our mission of excellence.

In your new role you will:

- Understand analog and mixed-signal designs
- Work with spice or models to verify analog circuits with complex SV based mixed-signal test benches
- Execute test plans; develop tests and debug test failures to fully verify mixed-signal designs
- Analyze and document simulation results, use scripting languages to improve efficiency
- Actively participate in cross functional collaboration with design teams to ensure a successful product delivery; work with post-Silicon teams to bring up and assist in validation testing
- Self-motivated with the initiative to seek constant improvements in working flow and verification quality, support team activities
- Adopt modern verification methodologies including UVM and other digital techniques for verifying analog, mixed-signal designs Possess strong initiative, analytical/problem solving skills, team working skills and ability to multitask

Profile

You are best equipped for this task if you have:

- Master's degree in electrical Engineering and 1-3 years of work experience or Bachelor's degree and 3-5 years of work experience
- Solid understanding of basics of analog and digital design, able to describe circuit behavior and functionality
- Experience in simulating circuits in spice simulators, debugging schematics, plotting and analyzing waveforms
- Demonstrated ability to read and write code in Verilog or SystemVerilog, use of digital simulators for verifying simple designs
- Work experience with mixed-signal test benches, SVA, functional coverage, constrained randomization and UPF
- Basic knowledge of any HDL for modeling such as Verilog-AMS, SV-RNM is highly desirable; UVM knowledge is a plus
- Knowledge of C, python, Tcl, Perl etc. for developing test content and scripts

At a glance

Location: **Lynnwood, WA (United States)**
Job ID: **343947**
Start date: **as soon as possible**
Entry level: **1-3 years**
Type: **Full time**
Contract: **Permanent**

Apply to this position online by following the URL and entering the Job ID in our job search:

Job ID: **343947**
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Why Us

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We make life easier, safer and greener – with technology that achieves more, consumes less and is accessible to everyone. Microelectronics from Infineon is the key to a better future. Efficient use of energy, environmentally-friendly mobility and security in a connected world – we solve some of the most critical challenges that our society faces while taking a conscientious approach to the use of natural resources.

Cypress Semiconductor Corporation is an equal opportunity employer. All qualified applicants will receive consideration for employment without regard to race, color, religion, sex (including pregnancy, childbirth, or related medical conditions), gender identity, national origin, ancestry, citizenship, age, physical or mental disability, legally protected medical condition, family care status, military or veteran status, marital status, domestic partner status, sexual orientation, or any other basis protected by local, state, or federal laws. Applicants with questions about access or requiring a reasonable accommodation for any part of the application or hiring process should contact the Talent Network by phone at (408) 503-2194.

Employment at Infineon is contingent upon proof of your legal right to work in the United States under applicable law, verification of satisfactory references and successful completion of a background check and drug test, and signing all your onboarding documents .

In some instances, if applicable, U.S. export control laws require that Infineon obtain a U.S. government export license prior to releasing technologies to certain persons. This offer is contingent upon Infineon's ability to satisfy these export control laws as related to your employment and anticipated job activities. The decision whether or not to submit and/or pursue an export license to satisfy this contingency, if applicable, shall be at Infineon's sole discretion.

IMPORTANT NOTICE :

Infineon is requiring all new U.S. employees and contractors to be fully vaccinated against COVID-19. Full vaccination is defined as two weeks after both doses of a two-dose vaccine or two weeks since a single-dose vaccine has been administered. Anyone unable to be vaccinated, either because of a sincerely held religious belief or a medical condition or disability that prevents them from being vaccinated, can request a reasonable accommodation.

Infineon Technologies takes data privacy and identity theft very seriously. As such, we do not request personally-identifiable information (PII) from applicants over the internet or electronically. Please kindly refrain from disclosing your PII electronically during the application process or to unauthorized websites that may purport to be Infineon or any of our affiliates.

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