Staff Engineer - SOC Digital Verification

Job description

We are looking for a Staff Engineer SOC Digital Verification to architect, implement and bring-up SOC top level builds, including Testbench Automation. You will be responsible in simulating and debugging at RTL, Unit Delay and Gate Level using appropriate tools and flows including Emulator, Portable Stimulus or Formal methodologies and help with functional and toggle coverage closure.

In your new role you will:

- Execute **SoC Verification tasks** under close supervision of a team lead.
- **Architect, implement and bring-up SOC top level builds**, including **Testbench Automation.**
- Work closely with team members and team leads to review and understand and discuss the relevant **functional and safety related requirements.**
- Write **verification plans** to meet these requirements after close alignment with lower level verification groups for proper work split according to mutually acceptable verification assignment.
- Execute the verification plan by **developing C/C++ test cases** and **System Verilog /UVM testbench components** and **by integrating 3rd part VIP components.**
- Simulate and debug at RTL, Unit Delay and Gate Level using appropriate tools and flows including **Emulator, Portable Stimulus or Formal methodologies and help with functional and toggle coverage closure**.

Profile

You are best equipped for this task if you have:

- Masters/Bachelors in Electrical Engineering or Computer Science with at least **5 years of relevant experience.**
- Good foundational knowledge of **digital design & verification**.
- Advanced knowledge of **SystemVerilog** and **UVM**.
- Possess hands-on experience on **hardware-software debugging** at the system or application level.
- Strong plus if domain knowledge of bring up at SoC level including **testbench automation and regression management competency.**

Why Us

Part of your life. Part of tomorrow.

At a glance

Location:
Job ID: **343543**
Start date: **as soon as possible**
Entry level: **5+ years**
Type: **Full time**
Contract: **Permanent**

Apply to this position online by following the URL and entering the Job ID in our job search:

We make life easier, safer and greener – with technology that achieves more, consumes less and is accessible to everyone. Microelectronics from Infineon is the key to a better future. Efficient use of energy, environmentally-friendly mobility and security in a connected world – we solve some of the most critical challenges that our society faces while taking a conscientious approach to the use of natural resources.

In accordance with the requirements set by the Singaporean Government, Infineon Technologies Asia Pacific Pte Ltd ("Infineon") can only allow individuals who are (a) fully vaccinated, (b) certified to be medically ineligible for a vaccine or (c) have recovered from COVID-19 within a prescribed period, onto company premises. Therefore, Infineon requires all new employees, as well as contractors and business partners, to be fully vaccinated against COVID-19. "Fully vaccinated" means individuals have completed the full regime of an approved COVID-19 Vaccine as listed under the World Health Organization (WHO) Emergency Use Listing (EUL) including the respective post-vaccination period to ensure the vaccine has become full effective. Anyone who is unable to be vaccinated due to an approved and/or recognised exemption condition may apply for special consideration.