Staff Engineer Systems Design

Job description

Your role will span from development and implementation hardware accurate reference model for wireless LAN PHY baseband processing in pre-Silicon phase to PHY-Firmware bring-up in post Si phase.

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Your key responsibilities in Pre-Si design phase would be
a. Design, analysis, development & maintenance of WLAN Physical layer reference model in C/C++, System C and Matlab
b. Use case definition and generation of reference vectors for pre-Si RTL verification of the PHY blocks and working with the PHY design team to complete design verification.
c. Collaborating with RF and Analog design teams in creation of the specs of the RF and modelling it in the simulation environment.
d. Working with various Cross functional teams like MAC systems, Firmware development, Hardware board design teams, RF design teams to define, document the interfaces to PHY layer and model them in reference design to enable verification
In Post- Silicon phase, your core responsibilities would involve
a. Functional validation of WLAN PHY and its interfaces on emulation platforms
b. Hands on silicon bring-up and validation with characterization over PVT to meet KPI's in the datasheet in the test/productional environment.
c. Debugging and solving cross layer customer issues – working across boundaries of WLAN PHY, MAC, RF and board – with a sharp focus on solving the underlying problem.
Your role would also give you opportunities to identifying issues and proposing design improvements and filing patents.

Profile

Solid knowledge of Communication Theory /Signal processing concept/Wireless Communication and ability to apply these concepts to analyze and solve system level issues.
Familiarity with PHY layer blocks like Carrier detection, Timing and frequency synchronization, AGC, filters, demodulators, tracking loops, decoders and their practical implementations in a wireless/wireline modem
Good understanding of RF impairments and system level understanding of RF architectures and their implications on system design
Hands on experience of programming in Matlab/C fixed point models is essential
Good debugging skills in C, using gdb, analzsing crashes using stack trace are essential
Familiarity with IEEE 802.11/802.15/3GPP standards is desirable
Knowledge of C++/Tcl/Python/
Ability to work with large, global, cross functional team
Good communication and presentation skills.

At a glance

Location:	**Bangalore (India)**
Job ID:	**343028**
Start date:	**as soon as possible**
Entry level:	**5+ years**
Type:	**Full time**
Contract:	**Permanent**

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Contact

Jyoti.Vimal@infineon.com

Benefits

- **Bangalore:** Coaching, mentoring & networking possibilities ; Wide range of training offers & planning of career development; Regional and local talent programs; International assignments; Career paths: Management career, Project management career, Technical ladder career, Individual contributor career, Professional career; Flexible work timing, Part time work, Work from home; Home office; Health & wellness reimbursement, Employee motivation forum, Spoorthi – Diversity club, Master health check up, Health promotion campaigns; Crèche facility; Annual success bonus; Mediclaim (dependents & top up), Personal accident, Term life; National Pension Scheme; Health promotion programs; Statutory benefits; Access for wheelchairs; On-site canteen; Paid sick leave, long term illness leave; On site Yoga classes, Sports club

Why Us

Part of your life. Part of tomorrow.

We make life easier, safer and greener – with technology that achieves more, consumes less and is accessible to everyone. Microelectronics from Infineon is the key to a better future. Efficient use of energy, environmentally-friendly mobility and security in a connected world – we solve some of the most critical challenges that our society faces while taking a conscientious approach to the use of natural resources.