



Principal Engineer Digital Verification (f/m/div)*

Job description

Do you want to be part of the growth of our R&D Center in Padova? Do you want to technically lead a digital verification team and play a crucial role at the #1 semiconductor partner in the fast-changing Automotive world? Then, you cannot miss this opportunity to join Infineon's success story! Apply now to become a Principal Engineer for Digital Verification.

As a Principal Engineer in Digital Verification, you will be responsible for technically leading a digital verification team in projects that contribute to technological progress in automotive area.

In your new role you will:

- Be responsible for the **technical coordination of the digital verification team related to ongoing projects**;
- **Create verification plans and test suites** based on design specification;
- **Build verification environments** consisting of test benches for top/block level;
- **Design self-checking test benches** using verification techniques based on SystemVerilog and UVM methodology;
- **Implement functional coverage and assertions** using SystemVerilog;
- **Develop test and functional coverage plans** based on device specifications;
- Be responsible for **analyzing and debugging simulation failures**;
- **Make regression simulation** while analyzing functional coverage and performance results.

Profile

High-quality standards are part of your daily life. You act fast and strive for continuous improvement. You are a problem solver with great analytical skills. Furthermore, you have a collaborative, result-oriented and proactive mindset aligned with your good communication skills.

You are best equipped for this role if you have:

- A Master **Degree or PhD in Electronic Engineering** or similar field of studies with specific knowledge of **microelectronic devices and related digital circuit design**;
- **Solid experience on digital design** implemented on sub-micron technologies;
- **At least 3 years of Power Management ICs (PMICs) experience** for automotive;
- **At least 4 years of experience as a Technical Lead or Mentor**;
- **Solid UVM experience**;

At a glance

Location: **Padua (Padova) (Italy)**
Job ID: **342322**
Start date: **as soon as possible**
Entry level: **5+ years**
Type: **Full time**
Contract: **Permanent**

Apply to this position online by following the URL and entering the Job ID in our job search:

Job ID: **342322**
www.infineon.com/jobs

Contact

Ana Rita Costa
Talent Attraction Manager



- Experience in **Power-aware verification using UPF/CPF**;
- Solid **knowledge of VManager and VPlanner**;
- **Solid experience with hardware description languages**, such as SystemVerilog and VHDL;
- Good **experience with common design tools**;
- **In-depth knowledge of Unix OS and scripting languages**, such as Perl and Python;
- **Fluency in English** (mandatory).

It is an advantage if you have:

- Knowledge of analog & mixed-signal IC architectures and integration requirements;
- Knowledge of Jama and Jira;
- Knowledge of Requirements Management methodologies;
- Experience with automotive standards in particular with ISO26262 and requirements-driven simulation.

Benefits

- **Padua (Padova):** Coaching, mentoring networking possibilities ; Wide range of training offers & planning of career development; International assignments; Different career paths: Project Management, Technical Ladder, Management & Individual Contributor; Flexible Working Time; Part-time work possible; Medical coverage; On-site canteen; Wage payment in case of sick leave; Corporate pension benefits; Performance bonus; Accessibility, access for wheelchairs

Why Us

Part of your life. Part of tomorrow.

We make life easier, safer and greener – with technology that achieves more, consumes less and is accessible to everyone. Microelectronics from Infineon is the key to a better future. Efficient use of energy, environmentally-friendly mobility and security in a connected world – we solve some of the most critical challenges that our society faces while taking a conscientious approach to the use of natural resources.

– Automotive (ATV) shapes the future of mobility with microelectronics enabling clean, safe, and smart cars –

Our semiconductors are essential for supporting the automotive megatrends: electromobility, automated driving, connectivity, and advanced security. They link the real and the digital world, driving the ever-advancing pace of automotive digitalization. Infineon **ATV** is the number one semiconductor partner in the fast-changing automotive world, based on our system knowledge and our passion for innovation and quality.

[Click here](#) for more information about working at ATV with interesting employee and management insights and an overview with more #ATVDreamJobs.

** The term gender in the sense of the General Equal Treatment Act (GETA) or other national legislation refers to the biological assignment to a gender group. At Infineon we are proud to embrace (gender) diversity, including female, male and diverse.*

