



Staff Engineer Mask Design

Job description

Job Description:

- Participate in block/IP/chip floor planning from scratch, performing routing /layout verification
- Co-lead the physical layout of block/IP/full chip by working closely with cross functional team leader
- Conduct thorough layout design reviews internally & external review with circuit designers
- Extensive use of CAD Tools (Cadence virtuoso & Mentor caliber)
- Capable in guiding/coaching junior engineer for on time delivery & quality

Profile

Candidate's profile:

- Bachelor degree in Electrical/Electronic Engineering/Physics with VLSI exposure of equivalent 6 to 10 years of job experience in layout design
- Hands on experience in analog layout from scratch
- Deep understanding of analog circuit ;layout concepts in CMOS technology
- Possess strong technical, analytical & problem solving skills in layout design
- Ability to work as strong team player and participate in cross functional activities
- Good interpersonal, verbal and communication skill with good initiative at work

Why Us

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At a glance

Location: **Penang (Malaysia)**
Job ID: **340437**
Start date: **as soon as possible**
Entry level: **5+ years**
Type: **Full time**
Contract: **Permanent**

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