



## Senior Staff Engineer Digital Verification (f/m/div)\*

### Job description

As a Senior Engineer Digital Verification you are part of an international team within our Concept and Digital Design team. Our products enable automotive key trends like autonomous driving, secure and connected cars. If you are a communicative team player and want to be part of shaping the future, become part of our team at Infineon Graz and apply now!

You are experienced in **digital design processes**, **verification** techniques, and **assertion** languages with a focus in digital and/or analog design. Even better, you have working knowledge of **Verilog/ SystemVerilog** as well as programming languages such as **C/C++**, **Python** or **Perl**. In this role, your keen attention to **detail** and quality will be key while working with **test plans**, test **cases**, **analysis** and **debugging** of test **results**. This is a great opportunity to be part of **strong team** of **semiconductor professionals** that support world class products and technology that achieves more, consumes less and is accessible to everyone

In your new role you will:

- **Translate** design **requirements** to a **test & verification plan**, **maintain** the test plan throughout a **project**
- **Develop** test **cases** based on **functional requirements**
- Make use of **active advancement** of **methods** and **tool development**
- Perform **analysis** and **debug** of **test results**, **refinement** and **documentation** of test cases
- Do **scripting** and **automation**
- Perform **design** and **test case review**
- Demonstrate **clear** and **consistent communication** with design, verification, and application teams

### Profile

You are **passionate** about **innovation** and demonstrate **strong communication skills**. As a true **team player**, you treat your colleagues with respect and trust. You can convince with a **structured** and **precise way of working** and a strong **focus on quality**.

You are best equipped for this task if you have:

- A university master **degree** in **Electrical Engineering**, **Computer Science** or a **comparable** education
- **3-5 years** or more of experience in **verification techniques**
- **Good knowledge** of **digital design processes**
- Experience in **leading** a team of **engineers** towards product **development projects**
- Working knowledge of preferably **Verilog** and **SystemVerilog** (SysVerilog is preferred language)

### At a glance

Location: **Graz (Austria)**  
Job ID: **337871**  
Start date: **as soon as possible**  
Entry level: **3-5 years**  
Type: **Full time**  
Contract: **Permanent**

Apply to this position online by following the URL and entering the Job ID in our job search:

Job ID: **337871**  
[www.infineon.com/jobs](http://www.infineon.com/jobs)

### Contact

Daniela Hofbauer



- Working knowledge of **C/C++**, **Python** or **Perl** programming languages
- Knowledge of **Unix/ Linux** environment and **shell programming**
- Experience with **industry standard tools** and methodologies
- Experience with **embedded processors/ firmware** development is a plus
- Experience with **ISO26262 Functional Safety** is a plus
- **Fluent English** skills, **German** skills are a **plus**

This position is subject to the collective agreement for workers and employees in the electrical and electronics industry, employment group H (<https://www.feei.at/leistungen/informations-service/mindestlohne-und-gehalter-2020/>). The monthly salary is paid 14 times p.a. A higher payment is negotiable depending on your expertise and skills. Furthermore, Infineon offers additional employee benefits.

## Benefits

- **Graz:** Coaching, mentoring networking possibilities; Wide range of training offers & planning of career development; International assignments; Different career paths: Project Management, Technical Ladder, Management & Individual Contributor; Flexible working conditions; Home office options; Part-time work possible (also during parental leave); Sabbatical; On-site social counselling and works doctor; Health promotion programs; Discounted lunch possibilities; Private insurance offers; Corporate pension benefits; Flexible transition into retirement; Performance bonus; Accessibility, access for wheelchairs

## Why Us

**Part of your life. Part of tomorrow.**

We make life easier, safer and greener – with technology that achieves more, consumes less and is accessible to everyone. Microelectronics from Infineon is the key to a better future. Efficient use of energy, environmentally-friendly mobility and security in a connected world – we solve some of the most critical challenges that our society faces while taking a conscientious approach to the use of natural resources.

**Automotive (ATV) shapes the future of mobility with microelectronics enabling clean, safe, and smart cars –**

Our semiconductors are essential for supporting the automotive megatrends: electromobility, automated driving, connectivity, and advanced security. They link the real and the digital world, driving the ever-advancing pace of automotive digitalization. Infineon **ATV** is the number one semiconductor partner in the fast-changing automotive world, based on our system knowledge and our passion for innovation and quality. [Click here](#) for more information about working at ATV with interesting employee and management insights and an overview with more #ATVDreamJobs.

The **Development Center** of Infineon in **Graz** is working on leading edge contactless, security and sensor technologies. Whether we are talking about the Near Field Communication (NFC) transmission standard, vehicle components for Radar, optical distance measurements or 3D image sensor chips for Augmented Reality and Virtual Reality – our Development Center is a driving force in innovations in security, mobility, and the Internet of Things.

*\* The term gender in the sense of the General Equal Treatment Act (GETA) or other national legislation refers to the biological assignment to a gender group. At Infineon we are proud to embrace (gender) diversity, including female, male and diverse.*

