



Staff/Senior Staff Engineer - Digital Design

Job description

Are you ready for a new challenge? This job offers the opportunity to bring in your Digital Design knowledge and develop your technical and personal skills in a growing, fast-paced and motivated team.

In your new role you will:

- You will take designs through the **complete development lifecycle from concept /study to silicon bring-up**
- Research and develop **new communication DSP algorithms**
- Perform **architectural studies** to determine **optimal hardware implementations of IP/SOC infrastructure, peripherals to meet product requirements**
- **Develop and review** detailed and concise **architecture and design specification documents**
- **RTL logic design of modules using Verilog HDL**. Designs may include power and clock management units, IP subsystem, digital interfaces to analog functions, accelerators, filters, demodulators supporting various wireless and wired communication protocols.
- **Prepare and hold architecture, design and verification reviews** with technical staff throughout project lifecycle
- **Perform logic synthesis, timing and power analysis** to optimize designs
- **Pre-silicon verification** utilizing various methodologies such as constrained random verification with block/subsystem/chip level UVM test benches, spice co-simulation of mixed signal blocks and FPGA emulation.
- **Validation/bring-up** of designs on silicon, providing support to cross-functional teams.

Profile

You are best equipped for this task if you have:

- M.S. or Ph. D in Electrical Engineering with minimum 5+ years of professional experience in **digital CMOS IC design**
- Experience using **Matlab** or other high-level modeling language/tool for designing and evaluating communication systems and algorithms
- Strong understanding of **block and chip-level verification and emulation**
- Experience with **logic simulators for both RTL and gate-level simulation, design /waveform browsers, and power analysis tools, Verilog RTL design preferably on DSP/math-centric designs**

At a glance

Location: **(Singapore)**
Job ID: **337610**
Start date: **as soon as possible**
Entry level: **5+ years**
Type: **Full time**
Contract: **Permanent**

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- Experience in implementing and analyzing fixed-point DSP, numerical processing elements, RF transceiver functions and impairments will be a plus

Why Us

Part of your life. Part of tomorrow.

We make life easier, safer and greener – with technology that achieves more, consumes less and is accessible to everyone. Microelectronics from Infineon is the key to a better future. Efficient use of energy, environmentally-friendly mobility and security in a connected world – we solve some of the most critical challenges that our society faces while taking a conscientious approach to the use of natural resources.

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