



Senior Staff analog Design engineer

Job description

The Connected Secure Systems (CSS) segment offers comprehensive systems for a secure, networked world with a portfolio built around reliable, trendsetting microcontrollers as well as wireless connectivity and security solutions. Over the past decades, CSS has developed microcontroller, Wi-Fi, Bluetooth and combined connectivity solutions (known as connectivity combos) and hardware-based security technologies. The products are used in a broad range of applications: from consumer electronics, IoT and home appliances to IT equipment, cloud security and networked cars all the way to credit and debit cards, electronic passports and IDs. With its leading technologies in the areas of computing, connectivity and security, CSS makes a decisive contribution to protecting today's and tomorrow's networked systems.

Candidate will be part of SRSS team in India, Owns and contribute towards Designing of Analog I/P focused on clock and will provide technical leadership to team, mentor junior people in the design of Analog I/P .

Profile

Experience:

Bachelor's - 9 to 11 years experience, Master - 8 to 10 years experience.

Influence:

- Candidate should be able to lead, own development of Analog I/P focused on clocks.
- Able to mentor and lead a small team to design clock IP from specification to silicon qualification.
- Candidate must have cross domain expertise , should have ability to under stand system level spec and derive the corresponding IP level specification from it .
- Should have expertise in the exploration and development of new architecture, and silicon debug.

MUST have

- Good understanding on analog fundamentals.
- Ability to solve problems, architecture development, silicon debug of complex clock system like precision oscillator (RC , crystal) ,PLL, DLL, precision comparator, reference network , Regulators, signal chains.
- Experience in supporting the IP silicon qualification, and silicon debug.

Good to have

- Designing Exposure on switching converter designs.

At a glance

Location: **Bangalore (India)**
Job ID: **337156**
Start date: **as soon as possible**
Entry level: **5+ years**
Type: **Full time**
Contract: **Permanent**

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Contact

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- Patents or published papers on clock related I/Ps.

Benefits

- **Bangalore:** Coaching, mentoring & networking possibilities ; Wide range of training offers & planning of career development; Regional and local talent programs; International assignments; Career paths: Management career, Project management career, Technical ladder career, Individual contributor career, Professional career; Flexible work timing, Part time work, Work from home; Home office; Health & wellness reimbursement, Employee motivation forum, Spoorthi – Diversity club, Master health check up, Health promotion campaigns; Crèche facility; Annual success bonus; Medclaim (dependents & top up), Personal accident, Term life; National Pension Scheme; Health promotion programs; Statutory benefits; Access for wheelchairs; On-site canteen; Paid sick leave, long term illness leave; On site Yoga classes, Sports club

Why Us

Part of your life. Part of tomorrow.

We make life easier, safer and greener – with technology that achieves more, consumes less and is accessible to everyone. Microelectronics from Infineon is the key to a better future. Efficient use of energy, environmentally-friendly mobility and security in a connected world – we solve some of the most critical challenges that our society faces while taking a conscientious approach to the use of natural resources.

