



Senior Staff Verification Engineer#336195

Job description

5+ years Verilog test bench development & behavioral verification of custom IC designs

In your new role you will:

- Be a core member of a SoC and IP development team.
- Primary responsibility will be test bench development and verification test of custom IC logic designs.
- working with others involved in verification and test development.

Profile

You are best equipped for this task if you have:

- 5+ years Verilog test bench development & behavioral verification of custom IC designs
- Experienced with UVM/OVM based verification methodology
- Proficiency in Perl, Tcl, Tk, C/C++ languages
- Master's Degree or above in EE/CE

Benefits

- **Shanghai, :** Coaching, mentoring networking possibilities; Wide range of training offers & planning of career development; International assignments; Different career paths: Project Management, Technical Ladder, Management & Individual Contributor; Flexible working conditions; Medical coverage; On-site social counselling and works doctor, in Wuxi only.; Provision of health tips, health knowledge sharing, annual medical check; Employees in Shanghai are entitled to use a gym located close to the office for free; On-site canteen, in Wuxi only.; Private insurance offers in some sites.; Wage payment in case of sick leave; Corporate pension benefits in some sites.

Why Us

Part of your life. Part of tomorrow.

We make life easier, safer and greener – with technology that achieves more, consumes less and is accessible to everyone. Microelectronics from Infineon is the key to a better future. Efficient use of energy, environmentally-friendly mobility and security in a

At a glance

Location: **Shanghai, (China)**
Job ID: **336195**
Start date: **as soon as possible**
Entry level: **5+ years**
Type: **Full time**
Contract: **Permanent**

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connected world – we solve some of the most critical challenges that our society faces while taking a conscientious approach to the use of natural resources.

