



Principal Engineer Design System Infrastructure

Job description

1. Scope: a. Infineon Design System Infrastructure concept, methodology and development processes. b. Technical Lead of Flow Architecture project involving refactoring of legacy software, following TDD, clean coding and SOLID design principles. c. Developing requirements, specification, implementation, test and support of software for the Infineon Design Flow Infrastructure d. Understand the VLSI business domain, analyzing domain requirements and transforming them to S/W specification. 2. As the technical lead of the project, fully responsible for a. Overall software architecture, design and documentation b. Provide technical leadership of the project, c. Making technical proposals including benefits and effort. d. Documentation and review of s/w architecture e. Providing technical guidance and mentoring for team members including reviews. f. Hands-on programming and owner of components i. Implementation of enhancements and bug-fixes in Perl/Python/C++ ii. Responsible for code, tests, documentation & support of components iii. use software quality tools like code coverage, performance tests, memory checks etc. and propose improvements g. Assisting project management by providing effort estimations. h. Ensuring compliance with all the Infineon development process methodology and milestones i. Project related communication to stakeholders 3. In the role of a software architect, for the software products of Design Flow infrastructure (including QA infrastructure) a. Having in depth understanding of the current S/W architecture of existing Infrastructure components and proposing alternative and more relevant architecture leading to a more scalable, maintainable, usable robust product. b. Responsible for propose and implement the re-architecture of legacy software. c. Should have sufficient knowledge of relevant trending s/w architectures, tools, frameworks in the industry and make informed decisions. d. Contribute with innovative ideas and implementation to increase productivity of the design flow via improved Kernel components, like better coding practices, publishing best practices, automating manual operations etc. e. Work with Designers and Development teams to understand their explicit and implicit automation requirements.

1. Scope:

a. Infineon Design System Infrastructure concept, methodology and development processes.
b. Technical Lead of Flow Architecture project involving refactoring of legacy software, following TDD, clean coding and SOLID design principles.
c. Developing requirements, specification, implementation, test and support of software for the Infineon Design Flow Infrastructure
d. Understand the VLSI business domain, analyzing domain requirements and transforming them to S/W specification.

2. As the technical lead of the project, fully responsible for
a. Overall software architecture, design and documentation
b. Provide technical leadership of the project,
c. Making technical proposals including benefits and effort.
d. Documentation and review of s/w architecture

At a glance

Location: **Bangalore (India)**
Job ID: **335504**
Start date: **as soon as possible**
Entry level: **5+ years**
Type: **Full time**
Contract: **Permanent**

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- e. Providing technical guidance and mentoring for team members including reviews.
- f. Hands-on programming and owner of components
- i. Implementation of enhancements and bug-fixes in Perl/Python/C++
- ii. Responsible for code, tests, documentation & support of components
- iii. use software quality tools like code coverage, performance tests, memory checks etc. and propose improvements
- g. Assisting project management by providing effort estimations.
- h. Ensuring compliance with all the Infineon development process methodology and milestones
- i. Project related communication to stakeholders

3. In the role of a software architect, for the software products of Design Flow infrastructure (including QA infrastructure)

- a. Having in depth understanding of the current S/W architecture of existing Infrastructure components and proposing alternative and more relevant architecture leading to a more scalable, maintainable, usable robust product.
- b. Responsible for propose and implement the re-architecture of legacy software.
- c. Should have sufficient knowledge of relevant trending s/w architectures, tools, frameworks in the industry and make informed decisions.
- d. Contribute with innovative ideas and implementation to increase productivity of the design flow via improved Kernel components, like better coding practices, publishing best practices, automating manual operations etc.
- e. Work with Designers and Development teams to understand their explicit and implicit automation requirements.

Profile

BE / BTech / MS in Computer Science or electronics or similar

14+ years of experience in the areas of CAD, EDA tools and/or Design Infrastructure development

- Mandatory: Rich experience in architecting and experience in developing large scale Unix based Software applications.
- Mandatory: Prior experience in Semiconductor /EDA/ tool development domains.
- Mandatory: Expert knowledge in C++/Qt/SWIG, architecture patterns, OOAD, and related tools, frameworks
- Mandatory: Strong focus on software architecture and software engineering methods.
- Mandatory: Ability to drive technical alignment meetings, strong interpersonal skills and acts in a self-responsible way.
- Highly desired: Knowledge of DevOps and CI/CD tools like Jenkins, Git-BitBucket, and SonarQube.
- Highly desired: Experience in technology migration from scripting languages to object-oriented languages (like Perl to C++)
- Desired: Experience of working in Agile scrum teams (as a Product Owner experience would be an advantage)

Benefits

- **Bangalore:** Coaching, mentoring & networking possibilities ; Wide range of training offers & planning of career development; Regional and local talent



programs; International assignments; Career paths: Management career, Project management career, Technical ladder career, Individual contributor career, Professional career; Flexible work timing, Part time work, Work from home; Home office; Health & wellness reimbursement, Employee motivation forum, Spoorthi – Diversity club, Master health check up, Health promotion campaigns; Crèche facility; Annual success bonus; Mediclaim (dependents & top up), Personal accident, Term life; National Pension Scheme; Health promotion programs; Statutory benefits; Access for wheelchairs; On-site canteen; Paid sick leave, long term illness leave; On site Yoga classes, Sports club

Why Us

Part of your life. Part of tomorrow.

We make life easier, safer and greener – with technology that achieves more, consumes less and is accessible to everyone. Microelectronics from Infineon is the key to a better future. Efficient use of energy, environmentally-friendly mobility and security in a connected world – we solve some of the most critical challenges that our society faces while taking a conscientious approach to the use of natural resources.

