



## Principal Engineer Elect Design

### Job description

- Responsible for leading Physical Design and Timing Closure of low power SoCs.
- Responsible to achieve die area, performance, power goals for hierarchical blocks and top.
- Drive physical design implementation which includes package co-design, floor planning, power grid design and signoff, place and route, timing closure, physical verification checks.
- Influence tools, flows and methodology activities to improve upon QoR.
- Interact with cross-functional team members to improve design, methodology and process aspects.

### Profile

- Bachelor's or Master's degree with specialization in VLSI design
- 11+ Years of experience
- Hand -on experience in physical design implementation and timing closure of large blocks and full chip
- Deep Understanding of low power design techniques and implementation methodologies
- In-depth knowledge of 40nm/28nm technologies and associated physical design challenges
- Expert user of industry standard tools like innovus for physical design
- Expert in scripting languages (shell, Perl, etc)
- Should be self motivated and take initiative to drive new methodologies
- Should have strong written and verbal communication skills
- Should be able to work with multi site design team

### Benefits

- **Bangalore:** Coaching, mentoring & networking possibilities ; Wide range of training offers & planning of career development; Regional and local talent programs; International assignments; Career paths: Management career, Project management career, Technical ladder career, Individual contributor career, Professional career; Flexible work timing, Part time work, Work from home; Home office; Health & wellness reimbursement, Employee motivation forum, Spoothi – Diversity club, Master health check up, Health promotion campaigns; Crèche facility; Annual success bonus; Medclaim (dependents & top up),

### At a glance

Location: **Bangalore (India)**  
Job ID: **333973**  
Start date: **as soon as possible**  
Entry level: **5+ years**  
Type: **Full time**  
Contract: **Permanent**

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Personal accident, Term life; National Pension Scheme; Health promotion programs; Statutory benefits; Access for wheelchairs; On-site canteen; Paid sick leave, long term illness leave; On site Yoga classes, Sports club

## Why Us

**Part of your life. Part of tomorrow.**

Infineon is a world leader in semiconductor solutions that make life easier, safer, and greener. Our solutions for efficient energy management, smart mobility, and secure, seamless communications link the real and the digital world.

