



Senior Staff Engineer

Job description

We are looking for talented Senior Staff Engineer to come join our Automotive division and develop the latest products for the market.

In your new role you will:

- Plan for project Design-for-Test (DfT) and test execution based on test concept, DfT and product architecture in coordination with rest of chip integration and cross functional team to achieve product goals for manufacturing test coverage, test cost, diagnosability, manufacturability and yield
- Develop test pattern plan for verification, debug, bring-up, characterization and manufacturing test patterns according to boundary conditions specified by test engineering and customers
- Communicate with IP owners about DfT issues introduced by IP design or IP integration
- Conduct RTL and Gate level DfT insertion including scan interface and compression logic, Memory BIST (Built-in Self Test)(MBIST) and repair logic, and other DfT infrastructure
- Verify DfT design features are logically correct (RTL and zero delay logic gate simulations) and that functional logic is not impacted (i.e. logical equivalency checking)
- Generate patterns based on test pattern plans (Scan-ATPG Automatic-Test-Pattern-Generation, MBIST, Boundary-Scan (BSCAN), etc.) and any coverage improvements needed.
- Proactively track DfT and test impact on cost of product (area impact, test time impact, development schedule)
- Ensure DfT features and test patterns perform with proper timing (Static Timing Analysis (STA), and timing annotated logic gate simulations) and power profile (e.g. dyn-IR analysis)
- Close logic fault coverage according to product requirements for logic using high fault coverage targets, and for memories using select algorithms
- Be fully ready for supporting test program preparation, bring-up, debug, and immediate production diagnosis when necessary
- Participate in the development of new or improved DfT methods to improve delivery, improve quality, and reduce test costs

Profile

You are best equipped for this task if you have:

At a glance

Location: **Lynnwood, WA (United States)**
Job ID: **333781**
Start date: **as soon as possible**
Entry level: **5+ years**
Type: **Full time**
Contract: **Permanent**

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Job ID: **333781**
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- Bachelors or Masters degree in a related field
- 3+ years of related experience to the job functions
- Experience effectively working with small/nimble teams
- Experience with communication within a large multinational corporation
- Experience with RTL based synchronous chip design process

Why Us

Part of your life. Part of tomorrow.

We make life easier, safer and greener – with technology that achieves more, consumes less and is accessible to everyone. Microelectronics from Infineon is the key to a better future. Efficient use of energy, environmentally-friendly mobility and security in a connected world – we solve some of the most critical challenges that our society faces while taking a conscientious approach to the use of natural resources.

Cypress Semiconductor Corporation is an equal opportunity employer. All qualified applicants will receive consideration for employment without regard to race, color, religion, sex (including pregnancy, childbirth, or related medical conditions), gender identity, national origin, ancestry, citizenship, age, physical or mental disability, legally protected medical condition, family care status, military or veteran status, marital status, domestic partner status, sexual orientation, or any other basis protected by local, state, or federal laws. Applicants with questions about access or requiring a reasonable accommodation for any part of the application or hiring process should contact the Talent Network by phone at (408) 503-2194.

Employment at Infineon is contingent upon proof of your legal right to work in the United States under applicable law, verification of satisfactory references and successful completion of a background check and drug test, and signing all your on-boarding documents .

In some instances, if applicable, U.S. export control laws require that Infineon obtain a U.S. government export license prior to releasing technologies to certain persons. This offer is contingent upon Infineon's ability to satisfy these export control laws as related to your employment and anticipated job activities. The decision whether or not to submit and/or pursue an export license to satisfy this contingency, if applicable, shall be at Infineon's sole discretion.

IMPORTANT NOTICE :

Infineon is requiring all new U.S. employees and contractors to be fully vaccinated against COVID-19. Full vaccination is defined as two weeks after both doses of a two-dose vaccine or two weeks since a single-dose vaccine has been administered. Anyone unable to be vaccinated, either because of a sincerely held religious belief or a medical condition or disability that prevents them from being vaccinated, can request a reasonable accommodation.

Infineon Technologies takes data privacy and identity theft very seriously. As such, we do not request personally-identifiable information (PII) from applicants over the internet or electronically. Please kindly refrain from disclosing your PII electronically during the application process or to unauthorized websites that may purport to be Infineon or any of our affiliates.

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