



## Principal Engineer Physical SoC Design & Implementation

### Job description

Responsible for SoC DFT Architecture definition/implementation/verification/silicon debug of SoC/Full Chip. Need to implement Scan insertion, LPCT, LBICT, Hybrid-TK, Compression Logic and DRC analysis of implemented Testability logic structures. Responsible for ATPG, DRC analysis, Test coverage debug, Memory BIST implementation and verification.

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- Responsible for ATPG, DRC analysis, Test coverage debug, Memory BIST implementation and verification.
- Ownership of JTAG/BSCAN/iJTAG, P1500 implementation and verification, Stuck-at/TDF/Bridging/Cell-aware/iddq fault models.
- Good debug skills in ZERO delay and SDF based scan/MBIST/JTAG simulations.
- Hands on experience in analysis and debug of above-mentioned test domains.
- Hands on experience in post silicon debug of scan/MBIST patterns/yield fall out.

### Profile

- ASIC flow understanding.
- Experienced in LEC, CLP, power analysis flow is preferred
- The ability to work as an individual and as part of a team to deliver complex SoCs starting from the creation of the DFT spec, implementation, verification, and Post silicon debug.
- In addition, be self-motivated with the initiative to seek constant improvements in the DFT design methodologies.
- The candidate must also possess strong initiative, analytical/problem solving skills, team working skills, ability to multitask and be able to work within a diverse team environment.
- Scripting skills such as PERL/TCL/Python are preferred.

### Benefits

### At a glance

Location: **Bangalore (India)**  
Job ID: **333653**  
Start date: **as soon as possible**  
Entry level: **5+ years**  
Type: **Full time**  
Contract: **Permanent**

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### Contact

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- **Bangalore:** Coaching, mentoring & networking possibilities ; Wide range of training offers & planning of career development; Regional and local talent programs; International assignments; Career paths: Management career, Project management career, Technical ladder career, Individual contributor career, Professional career; Flexible work timing, Part time work, Work from home; Home office; Health & wellness reimbursement, Employee motivation forum, Spoorthi – Diversity club, Master health check up, Health promotion campaigns; Crèche facility; Annual success bonus; Medclaim (dependents & top up), Personal accident, Term life; National Pension Scheme; Health promotion programs; Statutory benefits; Access for wheelchairs; On-site canteen; Paid sick leave, long term illness leave; On site Yoga classes, Sports club

## Why Us

### Part of your life. Part of tomorrow.

We make life easier, safer and greener – with technology that achieves more, consumes less and is accessible to everyone. Microelectronics from Infineon is the key to a better future. Efficient use of energy, environmentally-friendly mobility and security in a connected world – we solve some of the most critical challenges that our society faces while taking a conscientious approach to the use of natural resources.

The Connected Secure Systems (CSS) segment offers comprehensive systems for a secure, networked world with a portfolio built around reliable, trendsetting microcontrollers as well as wireless connectivity and security solutions. Over the past decades, CSS has developed microcontroller, Wi-Fi, Bluetooth and combined connectivity solutions (known as connectivity combos) and hardware-based security technologies. The products are used in a broad range of applications: from consumer electronics, IoT and home appliances to IT equipment, cloud security and networked cars all the way to credit and debit cards, electronic passports and IDs. With its leading technologies in the areas of computing, connectivity and security, CSS makes a decisive contribution to protecting today's and tomorrow's networked systems.

