



## Senior Staff Engineer Test Chip and PCM Layout (f/m /div)\*

### Job description

Are you eager to be part of our product and technology landscape to ensure effective new technology introduction and qualification? As a Senior Staff Engineer Test Chip and PCM Layout you will support the technology and product portfolio of Infineon by making a very important contribution to qualification. Furthermore, you will cooperate with various interfaces from technology and product development as well as design systems, manufacturing including foundries. Take now the chance and become a part of our team! The Technical Ladder is a special career path for those who share innovative ideas, demonstrate comprehensive technical knowledge, show thought leadership, possess problem solving abilities and are able to create business value.

In your new role you will:

- **Be responsible for layout of test chip structures supporting a variety of technologies** including CMOS, RF, SiGe, etc for tapeout at internal and external fabs
- **Be responsible for layout of Process Control & Monitoring (PCM), Reliability Control & Monitoring (RCM) and Seal Ring structures**
- **Ensure silicon-aware design** with focus on design for manufacturing (DfM), reliability, ESD, modelling and PDK development support
- **Planning, tracking and providing documentation of high-quality on-time layout deliveries** incl. verification steps
- **Active contribution to improvement of layout and design methodologies**, e.g. automation scripts, documentation structure etc.
- **Work at interfaces** with engineers, project managers **and be able to handle shifting priorities**

### Profile

You take in information quickly, evaluate it from different points of view, and consider relevant correlations as well as you work conscientiously on making things better, faster, and more efficient. In doing so, you focus your efforts on finding solutions that offer added value for both sides – Infineon and customers.

You are best equipped for this task if you have:

- A **degree in Electrical Engineering, Physics** or a similar discipline
- More than **5 years of experience in layout** with strong emphasis on technology, process or product development

### At a glance

Location: **Munich (Germany)**  
Job ID: **333289**  
Start date: **as soon as possible**  
Entry level: **5+ years**  
Type: **Full time**  
Contract: **Permanent**

Apply to this position online by following the URL and entering the Job ID in our job search:

Job ID: **333289**  
[www.infineon.com/jobs](https://www.infineon.com/jobs)

### Contact

**Mona Straßmair**  
Talent Attraction Manager



- **Founded knowledge in all areas of physical design** with a deep understanding of layout effects
- **Extensive hands-on experience with CAD tools for layout and verification** including Cadence and Mentor (Calibre)
- **Good programming and scripting language skills** (Cadence-SKILL, Tcl/Tk)
- **Excellent English** communication **skills** and at least the willingness to learn German

## Benefits

- **Munich:** Coaching, mentoring networking possibilities; Wide range of training offers & planning of career development; International assignments; Different career paths: Project Management, Technical Ladder, Management & Individual Contributor; Flexible working conditions; Home office options; Part-time work possible (also during parental leave); Sabbatical; On-site creche and kindergarden with 120 spots, open until 6pm; Holiday child care; On-site social counselling and works doctor; Health promotion programs; On-site gym, jogging paths, beachvolleyball, tennis & soccer court; On-site canteen; Private insurance offers; Wage payment in case of sick leave; Corporate pension benefits; Flexible transition into retirement ; Performance bonus; Reduced price for public transport and very own S-Bahn station; Access for wheelchairs

## Why Us

**Part of your life. Part of tomorrow.**

We make life easier, safer and greener – with technology that achieves more, consumes less and is accessible to everyone. Microelectronics from Infineon is the key to a better future. Efficient use of energy, environmentally-friendly mobility and security in a connected world – we solve some of the most critical challenges that our society faces while taking a conscientious approach to the use of natural resources.

The central R&D organization „**Design Enabling and Services**“ (**DES**) provides the design environment to the different Infineon product development teams. With state-of-the-art design methods, building blocks and a wide range of product development services DES supports Infineon's advanced IC development from early high level system models to verified products ready for manufacturing.

*\* The term gender in the sense of the General Equal Treatment Act (GETA) or other national legislation refers to the biological assignment to a gender group. At Infineon we are proud to embrace (gender) diversity, including female, male and diverse.*

