



Senior Elect Design Engineer

Job description

RTL Design Engineers

- Candidate will be responsible for building/maintaining highly configurable and reusable IO Subsystems (Note: An IO Subsystem is a logic IP that processes the IO Pads/IO Ring information and builds required logic to allow multiple on-chip peripherals to share the same IOs in a configurable manner)
- Candidate will be responsible for RTL design for integration of IO pads into SoC, building the required multiplexing logic and necessary power control signals integration.

At a glance

Location: **Bangalore (India)**
Job ID: **332457**
Start date: **as soon as possible**
Entry level: **3-5 years**
Type: **Full time**
Contract: **Permanent**

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Job ID: **332457**
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Profile

- Must have worked in ASIC Design flow for more than 3 years
- Must be strong in scripting using PERL.
- Must be familiar with RTL design for ASIC development using Verilog.
- Must be familiar with LINT (LEDATA/Spyglass), Clock-Domain-Crossing analysis, UPF, MVRC, Synthesis, Timing constraints and debugging STA reports.
- Strong mindset towards automation of repetitive work.
- Strong fundamentals in DFT/Fault-grading and/or hands on experience.
- Moderate Technical Contribution into Logic IP Design activities
- Fair written and verbal communication skills
- Moderate individual contributor with freedom to act, team work and learn

Benefits

- **Bangalore:** Coaching, mentoring & networking possibilities ; Wide range of training offers & planning of career development; Regional and local talent programs; International assignments; Career paths: Management career, Project

Contact

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management career, Technical ladder career, Individual contributor career, Professional career; Flexible work timing, Part time work, Work from home; Home office; Health & wellness reimbursement, Employee motivation forum, Spoorthi – Diversity club, Master health check up, Health promotion campaigns; Crèche facility; Annual success bonus; Mediclaim (dependents & top up), Personal accident, Term life; National Pension Scheme; Health promotion programs; Statutory benefits; Access for wheelchairs; On-site canteen; Paid sick leave, long term illness leave; On site Yoga classes, Sports club

Why Us

Part of your life. Part of tomorrow.

We make life easier, safer and greener – with technology that achieves more, consumes less and is accessible to everyone. Microelectronics from Infineon is the key to a better future. Efficient use of energy, environmentally-friendly mobility and security in a connected world – we solve some of the most critical challenges that our society faces while taking a conscientious approach to the use of natural resources.

