



Senior Digital Verification Engineer (f/m/div)*

Job description

Are you good in understanding and explaining complex technical details? You like to take responsibility for the entire process in digital verification? You enjoy being innovative and you like to actively form the future of a dedicated product family in our automotive division? Then we have a great opportunity for you as a Senior Digital Verification Engineer in our growing product development team. With this position you will be entering our technical ladder: A special career path for those who share innovative ideas, demonstrate comprehensive technical knowledge, show thought leadership, possess problem solving abilities and are able to create business value.

In your new role you will:

- Be responsible for the entire digital verification for automotive high-voltage driver ICs
- Translate product specifications and design requirements to verification plans
- Build verification environments and design test benches based on System Verilog / UVM
- Develop Assertions for formal verification
- Execute regression, debug fails and cooperate with post-silicon verification engineers
- Interact with concept, analog and digital designers as well as with top level mixed signal verification engineers in order to define a holistic verification strategy
- Report your planning, progress, and results – Ensure a clear and transferable documentation of your work to fulfill automotive quality requirements (ISO 26262)
- Contribute to digital verification method improvement

Profile

At a glance

Location: **Munich (Germany)**
Job ID: **331473**
Start date: **immediately**
Entry level: **5+ years**
Type: **Full time**
Contract: **Permanent**

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Contact

Sören Schmögner
Talent Attraction Manager



Taking new challenges without knowing the right path to an eventual solution in advance is not a problem for you. Respecting different attitudes and points of view and using the creative potential they offer reflects your mindset. You inspire others for your ideas and you explain complex issues in a clear and convincing manner.

You are best equipped for those tasks if you have:

- A degree in Electrical Engineering, Information Technology or similar field with focus on electronics
- At least 5 years of relevant working experience in digital verification, experience in agile methods is a plus
- Knowledge of System Verilog test-bench language and UVM
- Hands-on experience with Assertion Based Verification
- Expert in simulation and regression tools e.g. Cadence Incisive, Specman E, vManager
- Good understanding of Verilog and VHDL and the ability to contribute debugging on RTL and gate level
- Mixed-signal product development experience will be a benefit
- Ideally knowledge about fault simulation for product functional safety (ISO26262)
- Fluent English skills (written and spoken), German will be an added advantage

