



Principal Engineer

Job description

We are looking for candidates who can drive the low power implementation and verification for WiFi-subsystem.

- Will drive the low power implementation and verification for WiFi-subsystem.
- Participate in architecture discussions to define the low power strategy.
- UPF generation for blocks and top level.
- UPF verification using static checks.
- Dynamic verification - power aware simulations.
- Come-up with the low power test plan and drive the execution

Profile

- 10+ years of experience in low power implementation and verification.
- Hands on expertise in coming up with the UPF for a complex sub-system with multiple power domains.
- Hands-on expertise in static checks and power aware simulations including PG GLS.
- Knowledge of ARM CPUs and AXI, AHB will be plus

At a glance

Location: **Bangalore (India)**
Job ID: **330696**
Start date: **immediately**
Entry level: **5+ years**
Type: **Full time**
Contract: **Permanent**

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