



Principal Systems Engineer

Job description

Join our exciting and growing PSS division as a Principal Engineer by participating in FPGA porting of SoC design and SoC architecture definition and RTL flow.

In your new role you will:

- Own the FPGA porting of SoC design on Xilinx family of FPGAs
- Work closely with cross-functional teams in SoC architecture definition and RTL flow
- Perform System level validation of the SoC during both Pre and Post Silicon phases of the design
- Own the definition and development of test cases pertinent to target applications
- Review the board design architecture and board bring-up plans

Profile

You are best equipped for this task if you have:

- Master's degree in Electrical or Computer Engineering with 8+ years of experience
- Hands-on experience with Xilinx Vivado, Vivado HLS, ISE tools
- Strong knowledge of timing closure strategies on FPGAs with complex designs involving Serdes implementations
- Led the Lab bring-up of the RTL blocks on the FPGA platforms.
- Extensive Lab debug experience Xilinx chipscope/other debug tools
- Knowledge of USB protocols is highly desired
- Knowledge of industry standard interfaces like PCIE,I2C,SPI,UART,DDR memories
- Bring-up of FPGA solutions on the PCB
- Led the Lab bring-up of the RTL blocks on the FPGA platforms

Benefits

- **San Jose, CA:** Medical, Dental & Vision Plans; Flexible Reimbursement Accounts (FSAs); Industry leading 401k Employer Contribution/Match; Company Performance Bonus; Holiday Pay & Paid Time Off (PTO); Flexible Working Conditions, Part-time Options; Different career paths: Project Management, Technical Ladder, Management & Individual Contributor; Wide range of Training Offers & Career Development Planning; Coaching, Mentoring, Networking

At a glance

Location: **San Jose, CA (United States)**
Job ID: **325163**
Start date: **as soon as possible**
Entry level: **5+ years**
Type: **Full time**
Contract: **Permanent**

Apply to this position online by following the URL and entering the Job ID in our job search:

Job ID: **325163**
www.infineon.com/jobs



Possibilities; International assignments; Basic Life & Dependent Life Insurance; Paid Sick Leave, Accidental Death & Disability Insurance (AD&D) ; Short-term & Long-term Disability ; Employee Assistance Program (EAP) ; Health Promotion Programs ; Reduced Price for Public Transportation

Why Us

Part of your life. Part of tomorrow.

We make life easier, safer and greener – with technology that achieves more, consumes less and is accessible to everyone. Microelectronics from Infineon is the key to a better future. Efficient use of energy, environmentally-friendly mobility and security in a connected world – we solve some of the most critical challenges that our society faces while taking a conscientious approach to the use of natural resources.

Cypress Semiconductor Corporation is an equal opportunity employer. All qualified applicants will receive consideration for employment without regard to race, color, religion, sex (including pregnancy, childbirth, or related medical conditions), gender identity, national origin, ancestry, citizenship, age, physical or mental disability, legally protected medical condition, family care status, military or veteran status, marital status, domestic partner status, sexual orientation, or any other basis protected by local, state, or federal laws. Applicants with questions about access or requiring a reasonable accommodation for any part of the application or hiring process should contact the Talent Network by phone at (408) 503-2194.

Employment at Infineon is contingent upon proof of your legal right to work in the United States under applicable law, verification of satisfactory references and successful completion of a background check and drug test, and signing all your onboarding documents .

In some instances, if applicable, U.S. export control laws require that Infineon obtain a U.S. government export license prior to releasing technologies to certain persons. This offer is contingent upon Infineon's ability to satisfy these export control laws as related to your employment and anticipated job activities. The decision whether or not to submit and/or pursue an export license to satisfy this contingency, if applicable, shall be at Infineon's sole discretion.

IMPORTANT NOTICE:

Infineon is requiring all new U.S. employees and contractors to be fully vaccinated against COVID-19. Full vaccination is defined as two weeks after both doses of a two-dose vaccine or two weeks since a single-dose vaccine has been administered. Anyone unable to be vaccinated, either because of a sincerely held religious belief or a medical condition or disability that prevents them from being vaccinated, can request a reasonable accommodation.

Infineon Technologies takes data privacy and identity theft very seriously. As such, we do not request personally-identifiable information (PII) from applicants over the internet or electronically. Please kindly refrain from disclosing your PII electronically during the application process or to unauthorized websites that may purport to be Infineon or any of our affiliates.

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