



## Senior Engineer Design Automation (f/m/div)\*

### Job description

As a Senior Engineer Design Automation you work across our complete product range in conjunction with the local design teams and flow/technology departments. You identify and provide reliable solutions for all technical issues to assure internal customer (IT, CAD and technology departments) satisfaction through all stages of the development process. If you are a communicative team player and want to be part of shaping the future, become part of our team at Infineon Graz and apply now!

You are fully committed to making product development processes more efficient, while keeping the focus on excellence.

You are customer oriented, with a great team spirit and willingness to cooperate successfully across boundaries. Moreover, you demonstrate high quality standards for yourself and you are permanently connected to the current state-of-the-art developments.

In your new job, you will:

- **Support R&D chip designer and verification engineers** for daily issues related to **mixed-signal IC development** flow and related **software tools and methodology** (1<sup>st</sup> level support)
- **Proactively consult chip design projects** to ensure proper and efficient usage of working environment, software tools and related methodology
- **Collect application specific requirements** to the design environment and **provide fast solutions** (e.g. hot fixes) where needed
- **Trigger and track implementation of enhancements of the design environment** to ensure on-time availability for productive usage
- **Work closely with internal software tool and methodology providers** on **improving and developing new solutions**
- **Promote new methodology solutions** and **support adoption** of these in design projects
- **Share knowledge** within (IFX world-wide) "Design Application Engineering Community"

### Profile

You are best equipped for this job if you have:

- A **university degree** in **Electronic Engineering, Computer Science** or equivalent

### At a glance

Location: **Graz (Austria)**  
Job ID: **324792**  
Start date: **immediately**  
Entry level: **1-3 years**  
Type: **Full time**  
Contract: **Permanent**

Apply to this position online by following the URL and entering the Job ID in our job search:

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### Contact

**Christina Walder**  
Talent Attraction Manager



- At least **3 years of experience** in mixed signal IC development/layout/verification or support
- Good programming and scripting skills: **Perl, Python**
- Experience with **Unix/Linux**
- Hands-on with some mixed signal **EDA tools** like **Cadence** (Virtuoso, Spectre, Xcelium, AMS-Designer, Genus, Innovus), **Synopsys** (DC, ICC) and **Siemens/Mentor** (Calibre)
- Knowledge of design languages (**SystemVerilog**) is a plus
- **fluent English skills** with **German** as a strong plus

This position is subject to the collective agreement for workers and employees in the electrical and electronics industry, employment group G (<https://www.feei.at/leistungen/informations-service/mindestlohne-und-gehalter-2020/>). The monthly salary is paid 14 times p.a. A higher payment is negotiable depending on your expertise and skills. Furthermore, Infineon offers additional employee benefits.

