



Senior Staff Engineer Systems

Job description

Job Description:

- Work with an experienced engineer and develop memory subsystem based on AHB or AXI interconnect protocol using Verilog/System Verilog
- Work with an experienced engineer and develop high speed serial interfaces with SDR and DDR protocol using FPGA resources
- Follow a test plan, develop Verilog or C based test case, execute and report test results
- Maintain simulation environment based on Verilog and C, perform modification based on change in requirements, target device RTL etc.
- Participate in continuous improvement process and leverage the capabilities of the latest FPGA technology to ensure quality and faster time to market

Profile

Requirements:

- 3 to 5 years of experience in FPGA development (hardware and firmware) preferred.
- Fresh graduates with Masters degree in electrical/electronics/computer engineering with knowledge relevant to FPGA development are also encouraged to apply.
- Knowledge on Xilinx or Altera based FPGA design, implementation, and testing and embedded systems with ARM CPU.
- Knowledge on Verilog RTL design, verification, coverage analysis.
- Knowledge on C or similar low-level programming language
- Experience with regression test bench & test suit development, debug
- Experience with design methodology tools such as (or equivalent) Vivado, Questa, Oscilloscope, Logic Analyzer
- Able to write synthesizable RTL, integrate hard IP, Soft IP and create FPGA based design
- Must have strong teamwork and communication skills, passion, productivity, and self-learning ability

Why Us

Part of your life. Part of tomorrow.

We make life easier, safer and greener – with technology that achieves more, consumes less and is accessible to everyone. Microelectronics from Infineon is the key to a better future. Efficient use of energy, environmentally-friendly mobility and security in a connected world – we solve some of the most critical challenges that our society faces while taking a conscientious approach to the use of natural resources.

At a glance

Location: **Penang (Malaysia)**
Job ID: **320554**
Start date: **as soon as possible**
Entry level: **5+ years**
Type: **Full time**
Contract: **Permanent**

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