



## Design Engineer

### Job description

- Familiarity with VLSI domain and Micro Electronic
- Knowledge of Microprocessor architecture
- Knowledge of industry standard bus architecture like AMBA
- Basic understanding of verification concepts
- Good understanding of C/C++ languages and Object Orient programming
- Proficiency in Verilog/SV/VHDL

### Profile

- Actively participate in definition of Verification infrastructure including test bench components and testcases which are scalable and portable
- Define verification strategy (constraint random, Formal, Directed etc.) for IP/Chip /System level verification
- Identify, document and execute test plans on verification platforms, develop and debug random constrain verification test suite
- Actively participate in post-silicon bring-up, validation and compliance testing
- Actively participating in cross functional collaboration with design, software and hardware teams to ensure a successful product delivery

### At a glance

Location: **Bangalore (India)**  
Job ID: **320199**  
Start date: **Jan 03, 2022**  
Entry level: **0-1 year**  
Type: **Full time**  
Contract: **Permanent**

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