



Staff Engineer Design Engineering

Job description

We have a new position is for Physical Design and Timing Closure of complex, low power SoCs targeted for IOT and MCU markets.

In your new role, you will:

- Be responsible to **drive die area, performance, power goals** during physical design implementation
- Work on **various stages of physical design implementation** which includes floorplanning, power grid design, place and route, clock tree synthesis, timing closure, Static/Dynamic IRdrop, physical verification checks
- **Implementing SOCs** with multiple voltage islands, power islands and other power reduction techniques

Profile

You are best equipped for this task if you have:

- **B.E./B.Tech. with 5+ years** of experience or **M.E./MTech. with 3+ years** of experience and specialization in **VLSI design**
- Hands-on experience in **physical design and timing closure of SoCs**
- Experience of industry standard tools for **physical design and signoff**
- Experience in **scripting languages** (shell, perl, tcl) and Make flow
- Understanding of **40nm/28nm technologies** and associated physical design challenges
- Must be a good team player and should have desire to learn and explore

At a glance

Location: **Bangalore**
Job ID: **317825**
Start date: **immediately**
Entry level: **3-5 years**
Type: **Full time**
Contract: **Permanent**

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