



Staff Engineer

Job description

The IC design verification team of Infineon ICW is looking for a hands-on, team-oriented design verification engineer.

- The IC design verification team of Infineon ICW is looking for a hands-on, team-oriented design verification engineer. In this role, the engineer will be responsible for verifying digital functions that are part of the IoT Bluetooth IP as well as verifying SoC's in which this IP (as well as others) is integrated. We have world's top intelligent IC design teams include system, digital/analog design, firmware, software team all over the world, you will have the chance to work with and be part of them. We are looking for a self-motivated verification engineer will work closely with our digital design team and will also be engagement with firmware and algorithm teams.

Detail:

1. Verification for Bluetooth baseband, include BLE, BT5.x
2. SoC platform verification with AMBA interconnect and ARM CPU's
3. Build up test bench and simulation environment
4. Be able to create test plan according to design

Profile

- 1. Be familiar with logic simulator (Questa/VCS/IUS) and debug tools (Verdi/Visualizer)
- 2. Have basic Verilog and C language ability
- 3. Be familiar with AMBA protocol
- 4. At least one script language (TCL, Perl, Python and etc)
- 5. Be familiar with SystemVerilog, UVM is a plus
- 6. Familiarity with Bluetooth is a plus
- 7. English proficiency is a plus

At a glance

Location: **Hsinchu**
Job ID: **313114**
Start date: **immediately**
Entry level: **5+ years**
Type: **Full time**
Contract: **Permanent**

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