Mixed-Signal/Analog Integration Engineer

Job description

You are a talented individual with a good understanding of analog circuit design to drive the analog behavioral modeling role and to become an integral part of the Infineon R&D team. In this role, you will join a dynamic team of designers, chip architects, and systems engineers to develop leading-edge high-performance power management solutions for the computing market. Do you enjoy working in a highly motivated, innovative team and, capable of driving tasks to a successful conclusion? This is the perfect role.

In your new role, you will:

- Support **design and verification of mixed-signal IP** utilizing the Cadence suite of tools
- Work with the design team to **develop analog solutions** to the power management problem
- Verification of mixed-signal designs utilizing the **Cadence Incisive Suite of tools**.
- Generate **VerilogAMS/SystemVerilog** models and correlate results with schematic level performance.
- Generate simulation **workbenches** and interpret the results to verify compliance with system-level requirements.
- Verification of **DFT plans**

Additional responsibilities may include:

- **RTL code generation** for products with smaller digital footprints.
- Backend design on the Cadence Platform
- Synthesis
- Place & Route with static timing analysis
- Scan insertion, vector generation

Profile

You are best equipped for this task if you have:

- BSEE/MSEE/ Electrical engineering
- Experience with **Cadence Virtuoso** Tool Suite
- Capable of creating **schematics and generating netlist** for simulation.
- Good understanding of **analog design techniques**.

At a glance

Location:	**Warwick, RI**
Job ID:	**312513**
Start date:	**immediately**
Entry level:	**5+ years**
Type:	**Full time**
Contract:	**Permanent**

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Job ID: **312513**
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- Understanding the signal flow and boundaries between the **analog and digital domain**s.
- Prior experience in compiling, elaborating, and running mixed-signal simulation.
- Good understanding of analog spectre simulation and associated modeling of analog circuit blocks.
- Good debugging skills in both the analog and digital domains.
- Familiar with lab test equipment such as oscilloscope, DMM, power supplies, etc.
- Experience with Microsoft Office Tools (Word, Powerpoint, Excel) and Unix/Linux shell commands and editors.

Infineon **Power & Sensor Systems (PSS**) semiconductors play a vital role in enabling intelligent power management, smart sensitivity as well as fast and reliable data processing in an increasingly digitalized world.

Our leading-edge power devices make chargers, adapters, power tools and lighting systems smarter, smaller, lighter, and more energy-efficient. Our trusted sensors increase the context-sensitivity of "things" and systems such as HMI, and our RF chips power fast and reliable data communication.

– We drive leading-edge power management, sensing, and data transfer capabilities –

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