



Senior Staff Software Engineer for Semiconductor Model Integration (f/m/div)*

Job description

We ensure that Infineon's in-house circuit simulator stays a first class tool – but what is in it for you? First of all: **VERSATILITY** – you will be involved in development, implementation, maintenance & tool support at the same time. Secondly: **IMPACT** – you will be in a position to influence simulation methodology used by Infineon's designers worldwide. And last but not least: **PERSONAL DEVELOPMENT** – through learning from highly skilled colleagues. The Technical Ladder is a special career path for those who share innovative ideas, demonstrate comprehensive technical knowledge, show thought leadership, possess problem solving abilities and are able to create business value.

In your new role you will:

- **Develop Infineon's in-house analog circuit simulator**
- **Integrate semiconductor models and constantly optimize interfaces** to achieve maximum performance, feature set and maintainability
- **Improve existing software modules** including refactoring and maintenance of existing code
- **Support our worldwide user community**
- **Write technical documentation**
- **Participate in EU-funded projects** that are actively pursuing topics relevant to Infineon-internal simulation communities

Profile

An Infineon user is your first priority – we want to develop functionalities that help in the daily chip development process. Therefore it is important to 'translate' user requirements into specifications – while having an eye for the details. As we are working in a team, but also in standalone projects, so you should bring flexibility in your working style as well as good self-management.

These qualifications help you master the tasks:

- A **degree in Computer Science, Electrical Engineering, Mathematics or Physics** or related areas
- Minimum **5 years of work experience in electronic design automation (EDA) area or software design and implementation** or a related technical area
- **Very good programming skills with focus on C++/C in UNIX environments.** Advantageous but not mandatory: programming experience in Windows environment and/or FORTRAN skills and scripting languages (perl, Python, etc)
- Knowledge about one or more of the following topics: **analog circuit simulation**, standard **semiconductor models** (BSIM, PSP, HiCUM, etc), **algorithm development** and numerical mathematics

At a glance

Location: **Munich**
Job ID: **310379**
Start date: **Jan 01, 2021**
Entry level: **3-5 years**
Type: **Full time**
Contract: **Permanent**

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Job ID: **310379**
www.infineon.com/jobs

Contact

Janina Schumann



- Some hands-on experience with tools like HSPICE, Spectre, PSpice or languages such as SPICE, Cadence Skill or Verilog-A
- **Nice to have:** Working experience in adjacent areas such as **semiconductor modelling** or **analog design**
- **In the best case:** Already previous **experience in circuit simulator development**
- **Fluent English communication** with German language skills is a plus

