



Senior Staff Engineer - Software & Firmware Engr

Job description

Create models/ data structure to hold design data. Extract data from HDL models via commercial parsers, store them in models/data structures, modify them, and write generators to generate code from them.

In your new role you will:

- Create models/ data structure to hold design data
- Extract data from HDL models via commercial parsers, store them in models/data structures, modify them, and write generators to generate code from them.
- Support design/ verification engineers by understanding their problems and develop tools to automate their processes.
- To provide quick automation solutions always keeping quality in mind.
- Prepare and maintain a regression suit to test and verify the written software/ tool.

Profile

You are best equipped for this task if you have:

B.Tech/B.E/M.Tech in Electronics/Electrical/ComputerScience with VLSI/EDA background

Approximately 6+ years of experience in R&D in semiconductor industry

- Experienced in designing/ coding in software/EDA tools and flows
- Hands on in C++ (Object Oriented Concepts) and worked on Data Structures and Algorithms
- Working knowledge of C++ STL libraries preferred
- Exposure to Unix/ Linux Platforms
- Knowhow in digital design and a hardware description/verification language (VHDL/E or SystemVerilog or SystemC)
- Preferably having working knowledge in Perl/ Python
- Understanding of UML modelling language preferred

At a glance

Location: **Bangalore**
Job ID: **307399**
Start date: **immediately**
Entry level: **6+ years**
Type: **Full time**
Contract: **Permanent**

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