Job description

To strengthen our team we are looking for an Analog/Mixed-Signal Design Engineer (f/m).

In your new role you will:

- design and verify **analog and mixed signal circuits**, in particular **CMOS signal processing circuits** (e.g.: amplifiers, ADCs, DACs, oscillators, PLLs, voltage regulators, filters, ESD/EMC suppression etc.)
- **document the work products** according to established guidelines
- **collaborate closely** with concept engineering, test engineering and component verification
- conduct **feasibility studies** and **innovation projects** to support decisions for future solutions
- support and further develop **design and verification methodology** for optimal efficiency
- secure new intellectual property by filing **patents**

Profile

You generate value with your ideas and solutions and demonstrate high quality standards for yourself and other people. You communicate openly, clearly, and coherently and are able to quickly establish a successful cooperation.

You are best equipped for this task if you have:

- a university degree in **Electrical Engineering** or comparable
- **3+ years** of experience in **analog/mixed-signal design** as a plus
- experience in **modelling of analog circuits** (Verilog-A, System-C, Matlab-Simulink) as a plus
- experience in **CMOS analog design** (dimensioning of transistors, matching calculations, noise calculations, circuit topologies) as a plus
- expertise in **EMC** and **ESD** as a plus
- experience with common **analog design tools**
- **flu**ent **English** skills with **German skills** as a plus

This position is subject to the collective agreement for workers and employees in the electrical and electronics industry, employment group H (www.feei.at/arbeitswelt/kv-tabellen-statistik/mindestloehne-und-gehaelter-2017). The monthly salary is paid 14 times p.a. A higher payment is negotiable depending on your expertise and skills. Furthermore, Infineon offers additional employee benefits.