

Driving decarbonization and digitalization. Together.



FPGA Engineer (f/m/div)

Job description

Do you want to be part of the technology evolution of your day-to-day life? In Lviv, we develop innovative Human-Machine Interface products, mainly capacitive touchscreen and trackpad system solutions for top Automotive customers, battery management systems for next generation of the electric vehicles. Come and join us as FPGA Engineer!

You will participate in the full-chip microcontroller ASIC RTL conversion to FPGA, and define FPGA design constraints to get the ASIC project running on FPGA successfully. You will work on developing the RTL conversion scripts to help the FPGA tools process the ASIC RTL correctly in an automatic way.

Also, you will spend the majority of your time on tough issues debugging with a focus on the hardware/firmware co-execution analysis.

Occasionally, you develop the custom logic blocks that mimic the ASIC functionality that is not possible to implement in the FPGA like ADC, DAC, or other mixed signal peripherals.

In your job, you also will:

- Write and/import verification of the test benches for the FPGA and joint debug sessions with the verification team to ensure the design functionality was not lost as part of the ASIC to FPGA conversion
- Review the test firmware written in C language, help the firmware team for the test benches debug
- Use ARM Tarmac Trace Utilities to help with tough issues debugging by analyzing the ARM assembly instructions together with the firmware team
- Defining coding rules for the ASIC RTL design to help the design team to code RTL in the way that is both ASIC and FPGA-friendly
- Work with the global FPGA prototyping team to improve ASIC to FPGA conversion flow

Profile

You are best equipped for this job if you have:

- A **university degree** in **Computer science, Electronics**, or equivalent
- **5+ years of experience** in **digital design using FPGA**
- Experienced with large-scale FPGA development in Verilog or VHDL on Xilinx devices or other FPGA

At a glance

Location: **Lviv (Ukraine)**
Job ID: **102008**
Start date: **as soon as possible**
Entry level: **5+ years**
Type: **Full time**
Contract: **Temporary**

Apply to this position online by following the URL and entering the Job ID in our job search. Alternatively, you can also scan the QR code with your smartphone:

Job ID: **102008**
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Contact

Diogo Venâncio
Recruiter



- Hands-on with lab FPGA debug methodologies, such as ChipScope, Mentor Certus, oscilloscopes and logic analyzers, protocol analyzers
- Familiarity with some scripting languages, e.g. Perl, Python, TCL
- Excellent English communication skills (B2 or above)
- Stress resistant, ability to maintain high productivity long-time under tight project schedules and strong management scrutiny

Please attach your CV in English so we can get to know you better.

Note: this is a temporary position with the possibility to convert it to direct contract with Infineon if both parts are satisfied

Why Us

Driving decarbonization and digitalization. Together.

Infineon designs, develops, manufactures, and markets a broad range of semiconductors and semiconductor-based solutions, focusing on key markets in the automotive, industrial, and consumer sectors. Its products range from standard components to special components for digital, analog, and mixed-signal applications to customer-specific solutions together with the appropriate software.

We are on a journey to create the best Infineon for everyone.

This means we embrace diversity and inclusion and welcome everyone for who they are. At Infineon, we offer a working environment characterized by trust, openness, respect and tolerance and are committed to give all applicants and employees equal opportunities. We base our recruiting decisions on the applicant´s experience and skills.

We look forward to receiving your resume, even if you do not entirely meet all the requirements of the job posting.

Please let your recruiter know if they need to pay special attention to something in order to enable your participation in the interview process.

[Click here](#) for more information about Diversity & Inclusion at Infineon.

