



Staff Firmware Architect (f/m/div)*

Job description

Do you have experience in high-level definition of embedded software? Do you consider yourself to be a leader, who is a detailed thinker with strong problem-solving skills? Then, we have an exciting challenge for you. Apply now, to join our growing Firmware team at Infineon Padova as a Firmware Architect.

As Firmware (FW) R&D Architect, your focus will be the high-level definition of embedded software, with special focus on process compliance to new quality standards, for a new platform of microcontrollers-based products, which are integrated in the majority of cars all across the globe. You will actively support the product development from feasibility study till product ramp up, interacting with Product Definition Engineers (Requirements), HW Concept Engineers, FW Implementers, FW Verification Team and Quality Engineers.

In your new role you will:

- **Be responsible of FW Architecture and support in FW Design** in a context of System development based on a common ARM based CPU Sub-system platform;
- **Explore State of the Art Software modeling methodologies** (MBSE: Model Based System Engineering making use of Enterprise Architect) to ensure a configurable, reliable and reusable model in order to reduce development cycle time, and allow “Shift Left” in architecture verification and documentation;
- **Cooperate with colleagues** focused on FW development and verification in order to define reusable and optimized (size, performance) solutions;
- **Be required to document**, describe and defend FW solutions adopted in a HW experts’ context;
- **Define strategy and explore methodologies** to improve automation and reduce effort in the area of FW Architecture;
- **Follow IFX FW process and identify opportunities** to be more efficient during daily activities;
- **Perform requirements**, code and verification strategy reviews inside FW team;
- **Support silicon bring up and FW validation** on application Hardware and support application team when needed;
- **Contribute to the definition of the Infineon FW methodology** including ASPICE, ISO26262, ISO21434 compliant development process;
- **Network with other IFX firmware and Software experts** as well as System Concept Engineers.

Profile

At a glance

Location: **Padua (Padova) (Italy)**
Job ID: **HRC0068158**
Start date: **as soon as possible**
Entry level: **5+ years**
Type: **Full time**
Contract: **Permanent**

Apply to this position online by following the URL and entering the Job ID in our job search. Alternatively, you can also scan the QR code with your smartphone:

Job ID: **HRC0068158**
www.infineon.com/jobs



Contact

Regan Lottering
Talent Attraction Manager



You are motivated to learn and give your personal contribution to improve existing methodologies and processes. You demonstrate high quality standards for yourself, and other people and you are open to new ideas. You demonstrate strong communication skills and capable to build trustful relationship. You are a team player that supports and foster your colleagues to succeed.

You are best equipped for this task if you have:

- **A Master's degree or PhD in** Electrical/Electronic Engineering, Computer Science or equivalent degree;
- **At least 5 Years of experience** in Firmware or Embedded Software Development. Experience in Automotive would be a great plus;
- **Technical skills:** Deep UML knowledge , C Programming language knowledge , object orientated languages (python);
- **Good assembly knowledge** (ARM assembly is better), **software development practices** (collaborative and Agile, e.g. SCRUM);
- **Understanding of Firmware Development areas** like Boot mode or Bootstrap loader;
- **Experience on embedded software development** on ARM-based microcontroller and related toolchain (assembler, compiler, linker);
- **Knowledge of Automotive Functional Safety** (ISO 26262), ASPICE, AutoSAR and other standards would be a plus;
- **Tools:** Modeling tools (Enterprise Architect would be a plus), VCS tools: Git, SVN, Eclipse;
- **Leadership** - Coordinate teams of developers to meet design standards;
- **Fluency in English written and spoken.**

Please send us your CV in English.

Benefits

- **Padua (Padova):** Möglichkeit für Coaching, Mentoring & Netzwerken; Trainingsangebot & strukturierte Entwicklungsplanung; Möglichkeit zur internationalen Entsendung; Verschiedene Karrierepfade: Project Management, Technical Ladder, Management & Individual Contributor; Flexible Arbeitszeit: Vertrauensgleitzeit; Offenheit für Teilzeit (auch in der Elternzeit); Medical Coverage; Lohnfortzahlung im Krankheitsfall; Erfolgsbonus ; Barrierefreiheit ; Kantine; Arbeitgeberfinanzierte betriebliche Altersvorsorge

Why Us

Part of your life. Part of tomorrow.

Infineon is a world leader in semiconductor solutions that make life easier, safer, and greener. Our solutions for efficient energy management, smart mobility, and secure, seamless communications link the real and the digital world.

– Automotive (ATV) shapes the future of mobility with microelectronics enabling clean, safe, and smart cars –

Our semiconductors are essential for supporting the automotive megatrends: electromobility, automated driving, connectivity, and advanced security. They link the real and the digital world, driving the ever-advancing pace of automotive digitalization. Infineon **ATV** is the number one semiconductor partner in the fast-changing automotive world, based on our system knowledge and our passion for innovation and quality.

[Click here](#) for more information about working at ATV with interesting employee and management insights and an overview with more #ATVDreamJobs.



** The term gender in the sense of the General Equal Treatment Act (GETA) or other national legislation refers to the biological assignment to a gender group. At Infineon we are proud to embrace (gender) diversity, including female, male and diverse.*

