



Senior Staff Engineer Analog Verification Methodology (f/m/div)*

Job description

At Infineon, successful development is characterized by high quality and efficiency. We achieve this through continuous improvements in processes and tools – and always staying up-to-date is an exciting challenge for our team. As a member of our “Verification and Validation” department, you will take over responsibility for the automation of Infineon’s analog design verification with a special focus on methodology and customized tools. With this position you will join the Technical Ladder. This is a special career path for those who share innovative ideas, demonstrate comprehensive technical knowledge, show thought leadership, possess problem-solving abilities and are able to create business value.

In your new role you will:

- **Evaluate, improve and support methods and tools for analog verification in IC product development**, based on defined customer requirements
- **Align with internal customers (e.g. RnD departments) and EDA vendors** on the requirements and take part in implementation projects
- **Create specifications and documentation** throughout the projects
- **Pro-actively suggest improvements and discuss** them with the user community
- **Be responsible for the implementation, integration and rollout of adapted or new tools and methodologies** into the existing design flow
- **Provide trainings** for the user community

Profile

You have a pro-active mindset and always work on making things better, faster and more efficient? Great! Furthermore, you listen actively to your customers and consider their view, thoughts and ideas to find the best solution. You have the passion and courage to develop new ideas and drive them to their application.

You are best equipped for this task if you have:

- A **degree in Electrical Engineering, Microelectronics, Physics** or similar
- At least 5 years of **professional experience** with deep knowledge in the **verification of analog designs**, as well as good experience with **EDA tools for analog verification** (e. g. Cadence Spectre), for analog verification automation (e. g. library characterization suites)
- **Good Knowledge in IRDrop/EM analyses** (e. g. Ansys Totem or Cadence VoltusFI)
- Good **scripting skills** (e.g. Python, Perl, Shell programming, Cadence SKILL)

At a glance

Location:

Job ID: **355520**

Start date: **Sep 01, 2022**

Entry level: **5+ years**

Type: **Full time**

Contract: **Permanent**

Apply to this position online by following the URL and entering the Job ID in our job search:

Job ID: **355520**

www.infineon.com/jobs

Contact

Daniel Lichtblau

Talent Attraction Manager



- Basic **knowledge of numerical computing environments** like Matlab or Octave is an advantage
- An **independent and self-responsible** working style
- Fluent **English skills** and preferably also German

Why Us

Part of your life. Part of tomorrow.

Infineon is a world leader in semiconductor solutions that make life easier, safer, and greener. Our solutions for efficient energy management, smart mobility, and secure, seamless communications link the real and the digital world.

The central R&D organization „**Design Enabling and Services**“ (DES) provides the design environment to the different Infineon product development teams. With state-of-the-art design methods, building blocks and a wide range of product development services DES supports Infineon's advanced IC development from early high level system models to verified products ready for manufacturing.

** The term gender in the sense of the General Equal Treatment Act (GETA) or other national legislation refers to the biological assignment to a gender group. At Infineon we are proud to embrace (gender) diversity, including female, male and diverse.*

