



## Principal Engineer Design for Test (f/m/div)\*

### Job description

You want to work on some of the most advanced semiconductor products and bring in your Design for Test (DfT) expertise? As a Principal Engineer Design for Test at Infineon you will join forces with cross functional teams and work on DfT implementation for system-on-chip designs. In this role you have responsibility for DfT implementation, specification definition and methodology development. The Technical Ladder is a special career path for those who share innovative ideas, demonstrate comprehensive technical knowledge, show thought leadership, possess problem solving abilities and are able to create business value.

In your new role you will:

- **Define and implement DfT** (Design for Test) **and BIST** (Built-In Self-Test) concepts to maximize test coverage while minimizing costs
- Collaboratively **identify DfT methodology innovations & enhancements** for Infineon
- **Be responsible for new DfT methodologies:** definition, implementation, and roll-out
- **Execute scan insertion, Automatic Test Pattern Generation (ATPG)**, verification and support deliveries in post-silicon
- **Act as a mentor and coach** for junior developers
- **Work in close cooperation with the two major interfaces** chip design and semiconductor manufacturing as well as related areas

### Profile

You take in information quickly, evaluate it from different points of view, and uncover hidden correlations in it. Moreover, you actively look for the ideal solution and engage in professional debates with the relevant people, instead of accepting rash compromises as well as you feel responsible for your own doing and decisions.

You are best equipped for this task if you have:

- A **degree in Microelectronic, Computer Science, Physics** or a similar qualification
- At least **5 years of work experience in Design for Test**; ideally also in microcontroller architectures and semiconductor technologies
- Deep **understanding of the digital design** flow from specification to silicon debug
- Profound **experience in methodology and innovation work** is an important requirement
- Good **communication and presentation skills** in a cross functional environment

### At a glance

Location: **Munich (Germany)**  
Job ID: **350982**  
Start date: **as soon as possible**  
Entry level: **5+ years**  
Type: **Full time**  
Contract: **Permanent**

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Job ID: **350982**  
[www.infineon.com/jobs](http://www.infineon.com/jobs)



### Contact

**Mona Straßmair**  
Talent Attraction Manager



- Open minded mentality to **work with different departments and sites and the willingness to travel**
- **Experience in project management** of small project teams is a benefit, but not a pre-condition
- **Fluent English language skills;** German is a plus

## Benefits

- **Munich:** Coaching, mentoring networking possibilities; Wide range of training offers & planning of career development; International Assignments; Different Career Paths: Project Management, Technical Ladder, Management & Individual Contributor; Flexible working conditions; Home Office Options; Part-time work possible; Sabbatical; Creche and kindergarden with 180 spots and opening times until 6pm; Holiday Child Care; On-site social counselling and works doctor; Health promotion programs; On-site gym, jogging paths, beachvolleyball, tennis and soccer court ; On-site canteen; Private insurance offers; Wage payment in case of sick leave; Corporate pension benefits; Flexible transition into retirement; Performance bonus; Cheaper ticket for public transport and very own S-Bahn station; Accessibility access for wheelchairs

## Why Us

**Part of your life. Part of tomorrow.**

Infineon is a world leader in semiconductor solutions that make life easier, safer, and greener. Our solutions for efficient energy management, smart mobility, and secure, seamless communications link the real and the digital world.

The central R&D organization „**Design Enabling and Services**“ (DES) provides the design environment to the different Infineon product development teams. With state-of-the-art design methods, building blocks and a wide range of product development services DES supports Infineon's advanced IC development from early high-level system models to verified products ready for manufacturing.

*\* The term gender in the sense of the General Equal Treatment Act (GETA) or other national legislation refers to the biological assignment to a gender group. At Infineon we are proud to embrace (gender) diversity, including female, male and diverse.*

