



Senior Staff Design Application Engineer - Digital Implementation

Job description

Support Infineon's methodologies & flow within the EDA design platform for advanced SoC design and focus on enabling high-performance design environment

In your new role you will:

- Strong knowledge in the **Digital domain: Verification, RTL, Constraint, UPF, P&R** and Physical Verification
- **Create, release, and maintain standard design scripts** to automate EDA tools & flows
- Contribute to the Improvement of the **SoC Implementation methodology**
- Knowledge and **experience using industry standard formats** for timing and power constraints
- **Support the SoC design team** with design environment setup and product database management
- **Develop CAD scripts and flows** for optimization of Synthesis, Floorplan, Place and Route, Functional verification, DFT etc.
- **Interface with EDA vendors** to resolve tool issues related to design challenges
- Strong experience in **Data Management Software** such as Cliosoft, ICMange etc
- Strong collaborate with SoC design teams in **troubleshooting tools and design problems**
- Extensive experience in **Cadence SOC tool usage** for advanced SOC design Support the tape-out process

#LI-MH1

Profile

You are best equipped for this task if you have:

- Knowledge of Industry **EDA tools and semiconductor design techniques**
- Extensive **proficiency in developing scripts** using standard EDA scripting languages
- Strong **problem-solving skills with excellent debugging ability**
- Must have **strong teamwork, communication skills** and work ethics with the ability to address changes and issues with the design teams in a consistent and efficient way

At a glance

Location: **San Jose, CA (United States)**
Job ID: **348955**
Start date: **as soon as possible**
Entry level: **5+ years**
Type: **Full time**
Contract: **Permanent**

Apply to this position online by following the URL and entering the Job ID in our job search:

Job ID: **348955**
www.infineon.com/jobs



- **Excellent interpersonal skills** and the ability to operate in both centralized and distributed team environment
- Must be **capable of meeting deadlines** and achieving high level of satisfaction among users.
- **5-10 years of relevant experience**, BSEE minimum requirement, MSEE preferred

Benefits

- **San Jose, CA:** Möglichkeit für Coaching, Mentoring & Netzwerken; Trainingsangebot & strukturierte Entwicklungsplanung; Möglichkeit zur internationalen Entsendung; Verschiedene Karrierepfade: Project Management, Technical Ladder, Management & Individual Contributor; Flexible Arbeitszeit; Vertrauensgleitzeit; Gesundheitsleistungen; Gesundheits- & Vorsorgeprogramme; Arbeitgeberfinanzierte betriebliche Altersvorsorge; Erfolgsbonus; Barrierefreiheit

Why Us

Part of your life. Part of tomorrow.

Infineon is a world leader in semiconductor solutions that make life easier, safer, and greener. Our solutions for efficient energy management, smart mobility, and secure, seamless communications link the real and the digital world.

Infineon Technologies Americas Corp. is an equal opportunity employer. All qualified applicants will receive consideration for employment without regard to race, color, religion, sex (including pregnancy, childbirth, or related medical conditions), gender identity, national origin, ancestry, citizenship, age, physical or mental disability, legally protected medical condition, family care status, military or veteran status, marital status, domestic partner status, sexual orientation, or any other basis protected by local, state, or federal laws. Applicants with questions about access or requiring a reasonable accommodation for any part of the application or hiring process should contact the Talent Network by phone at (408) 503-2194.

Employment at Infineon is contingent upon proof of your legal right to work in the United States under applicable law, verification of satisfactory references and successful completion of a background check and drug test, and signing all your onboarding documents .

In some instances, if applicable, U.S. export control laws require that Infineon obtain a U.S. government export license prior to releasing technologies to certain persons. This offer is contingent upon Infineon's ability to satisfy these export control laws as related to your employment and anticipated job activities. The decision whether or not to submit and/or pursue an export license to satisfy this contingency, if applicable, shall be at Infineon's sole discretion.

IMPORTANT NOTICE:

Infineon is requiring all new U.S. employees and contractors to be fully vaccinated against COVID-19. Full vaccination is defined as two weeks after both doses of a two-dose vaccine or two weeks since a single-dose vaccine has been administered. Anyone unable to be vaccinated, either because of a sincerely held religious belief or a medical condition or disability that prevents them from being vaccinated, can request a reasonable accommodation.

Infineon Technologies takes data privacy and identity theft very seriously. As such, we do not request personally-identifiable information (PII) from applicants over the internet or electronically. Please kindly refrain from disclosing your PII electronically during the application process or to unauthorized websites that may purport to be Infineon or any of our affiliates.

