



## Senior Staff Engineer Analog and Mixed-Signal Verification (f/m/div)\*

### Job description

Do you want to be part of the growth of our R&D Center in Pavia? Do you want to technically lead a verification team and play a crucial role at the #1 supplier of gate drivers? Then, you cannot miss this opportunity to join Infineon's success story! Apply now to become a Senior Staff Engineer Analog and Mixed-Signal Verification.

As a Senior Staff Engineer Analog and Mixed-Signal Verification, you will be responsible for technically leading a verification team in projects on gate drivers.

In your new role you will:

- **Verify the functionality and performance** of our IP blocks and IC top level;
- Drive the **specification of the system level verification plan** and ensure its complete and correct execution;
- **Handle the full verification tool package** to achieve T.O. sign off: run simulations, analyze results and conduct verification reviews to ensure that requirements are met;
- Align and track the **implementation of test-benches and stimuli generation** according to the verification plan;
- **Implement behavioral models** and ensure their matching with schematics;
- **Shape the verification methodology** in close cooperation with the application, concept and design engineers;
- **Report and communicate proactively** about the status, the risks and issues towards the technical coordinator and project management.

### Profile

You have a process oriented working style and the ability to be flexible even in a complex environment. You are proactive and analyze issues from different perspectives. Furthermore, you are a team player with strong communication skills that support your ability to establish lasting relationships with others.

You are best equipped for this job if you have:

- A **Master degree in Electronic Engineering** or similar field of studies;
- More than 5 years of **experience in Mixed-Signal IC or SoC verification**;
- Experience with **Cadence Virtuoso, AMS-Designer, Spectre, Xcelium or Incisive**;
- Understanding of **behavioral and real-number modelling languages** such as Verilog-A, Verilog-AMS, SystemVerilog, VHDL and SystemC;

### At a glance

Location: **Pavia (Italy)**  
Job ID: **338693**  
Start date: **as soon as possible**  
Entry level: **5+ years**  
Type: **Full time**  
Contract: **Permanent**

Apply to this position online by following the URL and entering the Job ID in our job search:

Job ID: **338693**  
[www.infineon.com/jobs](http://www.infineon.com/jobs)

### Contact

Margarida Carneiro



- Excellent **knowledge of the entire IC design flow**;
- **Fluency in English**.

## Benefits

- **Pavia:** Möglichkeit für Coaching, Mentoring & Netzwerken ; Trainingsangebot & strukturierte Entwicklungsplanung ; Möglichkeit zur internationalen Entsendung ; Verschiedene Karrierepfade: Project Management, Technical Ladder, Management & Individual Contributor ; Flexible Arbeitszeit: Vertrauensgleitzeit ; Offenheit für Teilzeit (auch in der Elternzeit) ; Medical Coverage ; Lohnfortzahlung im Krankheitsfall ; Erfolgsbonus ; Barrierefreiheit ; Kantine; Arbeitgeberfinanzierte betriebliche Altersvorsorge

## Why Us

**Part of your life. Part of tomorrow.**

We make life easier, safer and greener – with technology that achieves more, consumes less and is accessible to everyone. Microelectronics from Infineon is the key to a better future. Efficient use of energy, environmentally-friendly mobility and security in a connected world – we solve some of the most critical challenges that our society faces while taking a conscientious approach to the use of natural resources.

**– Industrial Power Control (IPC) empowers a world of unlimited energy –**

Power semiconductors play a crucial role in increasing efficiency and reducing energy losses along the whole energy conversion chain.

As the global leader in power semiconductors, Infineon **IPC** delivers leading products and solutions for smart and efficient energy generation, transmission and consumption. We strive to make this planet a greener place where sufficient energy is accessible to everyone – wherever and whenever they need it.

[Click here](#) for more information about working at IPC with interesting employee and management insights and an overview with more #IPCDreamJobs.

*\* The term gender in the sense of the General Equal Treatment Act (GETA) or other national legislation refers to the biological assignment to a gender group. At Infineon we are proud to embrace (gender) diversity, including female, male and diverse.*

