



Principal SerDes Engineer

Job description

Infinion is looking for a PHY expert who would be working in the Wired Connectivity Solutions group on products meeting USB, PCIE and other connectivity standards requirement.

In your new role you will:

- Participate in design, integration & architecture development of 5G/10G/20G /40G-SERDES circuits meeting performance, area, power and timescale constraints using state of the art EDA tools.
- Work with system and digital teams with product definition, chip integration, chip top level verification and system validation.
- Support DFT strategy and implementation.
- Silicon bring-up support of validation, production test and char teams.
- Supervise layout and support junior engineers.
- Participate in coordinating test and product engineering tasks for super speed products including support of test development.
- Understands the strengths and limitations of underlying silicon process and SoC platforms to define compelling PHY architectures and design.
- Manages the architecture through specs and models. Continuously monitors and manages the models and product implementation to ensure they are in sync.

Profile

You are best equipped for this task if you have:

- Required:
 - BS in electrical engineering or related fields, MS and PhD strongly preferred
 - 10+ years of relevant experience
 - Broad based experience in PHY design especially for Gbps speed and integration with complex SoCs
 - Strong written and verbal communication skills.
 - Self-motivated with the initiative to seek constant improvements in the PHY design methodologies.
 - Possess strong initiative, analytical/problem solving skills, team working skills, and be able to work within a diverse team environment.

At a glance

Location: **San Jose, CA (United States)**
Job ID: **333619**
Start date: **as soon as possible**
Entry level: **5+ years**
Type: **Full time**
Contract: **Permanent**

Apply to this position online by following the URL and entering the Job ID in our job search:

Job ID: **333619**
www.infineon.com/jobs



- Good understanding of transistor level analog integrated circuits, device physics and transmission-line characteristics.
 - Design experience with analog modules such as PLL, high data rate transmitters, receivers and signal-conditioning/equalization etc.
 - Understand ESD/latch-up mechanism.
 - Solid knowledge of EDA design tools and Cadence design environment.
 - Skilled in scheduling/budgeting of his tasks and help in creating project schedule.
- Desired:
 - Experience in 5G/10G-SERDES used with USB, PCIE, SATA
 - Experience in using 5G/10Gbps test equipment
 - Data analysis (using MATLAB / Excel / Python / Perl) and test automation experience
 - Knowledge of Super speed USB standards

Benefits

- **San Jose, CA:** Möglichkeit für Coaching, Mentoring & Netzwerken; Trainingsangebot & strukturierte Entwicklungsplanung; Möglichkeit zur internationalen Entsendung; Verschiedene Karrierepfade: Project Management, Technical Ladder, Management & Individual Contributor; Flexible Arbeitszeit: Vertrauensgleitzeit; Gesundheitsleistungen; Gesundheits- & Vorsorgeprogramme; Arbeitgeberfinanzierte betriebliche Altersvorsorge; Erfolgsbonus; Barrierefreiheit

Why Us

Part of your life. Part of tomorrow.

We make life easier, safer and greener – with technology that achieves more, consumes less and is accessible to everyone. Microelectronics from Infineon is the key to a better future. Efficient use of energy, environmentally-friendly mobility and security in a connected world – we solve some of the most critical challenges that our society faces while taking a conscientious approach to the use of natural resources.

Cypress Semiconductor Corporation is an equal opportunity employer. All qualified applicants will receive consideration for employment without regard to race, color, religion, sex (including pregnancy, childbirth, or related medical conditions), gender identity, national origin, ancestry, citizenship, age, physical or mental disability, legally protected medical condition, family care status, military or veteran status, marital status, domestic partner status, sexual orientation, or any other basis protected by local, state, or federal laws. Applicants with questions about access or requiring a reasonable accommodation for any part of the application or hiring process should contact the Talent Network by phone at (408) 503-2194.

Employment at Infineon is contingent upon proof of your legal right to work in the United States under applicable law, verification of satisfactory references and successful completion of a background check and drug test, and signing all your on-boarding documents .

In some instances, if applicable, U.S. export control laws require that Infineon obtain a U.S. government export license prior to releasing technologies to certain persons. This offer is contingent upon Infineon's ability to satisfy these export control laws as related to your employment and anticipated job activities. The decision whether or not to submit and/or pursue an export license to satisfy this contingency, if applicable, shall be at Infineon's sole discretion.

IMPORTANT NOTICE:



Infineon is requiring all new U.S. employees and contractors to be fully vaccinated against COVID-19. Full vaccination is defined as two weeks after both doses of a two-dose vaccine or two weeks since a single-dose vaccine has been administered. Anyone unable to be vaccinated, either because of a sincerely held religious belief or a medical condition or disability that prevents them from being vaccinated, can request a reasonable accommodation.

Infineon Technologies takes data privacy and identity theft very seriously. As such, we do not request personally-identifiable information (PII) from applicants over the internet or electronically. Please kindly refrain from disclosing your PII electronically during the application process or to unauthorized websites that may purport to be Infineon or any of our affiliates.

#LI-MB1

