



Senior Staff Engineer Design System

Job description

We are looking for candidates who can work on design and development of Wi-Fi Physical layer signal processing algorithm design in pre-silicon design phase.

- In pre-Silicon design phase you'll be working on design and development of Wi-Fi Physical layer signal processing algorithm design along with Modelling and simulation of Wi-Fi Physical layer in MATLAB/C.
- In addition you will also be working in creation of cases and generation of test vectors and working with the PHY design team to complete design verification.
- You will also work and collaborate with RF team in creation of the specs of the RF and help model it in the system level model.
- In Post- Silicon phase, you will be working on emulation platforms to validate the WLAN PHY and it's interfaces.
- You will be working in bring-up of PHY/MAC while working with various cross functional team to deliver the chip and PHY Firmware which meets the KPI's.

Profile

- 5+ years of experience with Masters in Electrical/Electronics Engineering or 7+ years with Bachelors in Engineering in Electrical/Electronics
- Knowledge of Communication Theory and Systems/DSP/Wireless Communication including practical implementations.
- Hands on Post-silicon experience of bring-up of chips with familiarity of typical lab equipment like Spectrum analyzer, signal generators.
- Knowledge of MATLAB/C/C++/Python/Tcl.
- Good programming experience in C/C++/Python.
- Solid debugging skills in C, using gdb, analyzing crashes using stack trace.
- Familiarity with Wi-Fi/3GPP standards is preferable.
- Ability to work with large teams in a cross functional and multi-site teams.
- Good communication and presentation skills.

At a glance

Location: **Bangalore (India)**
Job ID: **321931**
Start date: **immediately**
Entry level: **5+ years**
Type: **Full time**
Contract: **Permanent**

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