KIT_PSC3M6_EVAL

CONTENTS

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Drawing Numbers

PCBA	121-60808-01
PCB	600-60808-01
FAB DRW	610-60808-01
ASSY DRW	620-60808-01
SCH DRW	630-60808-01

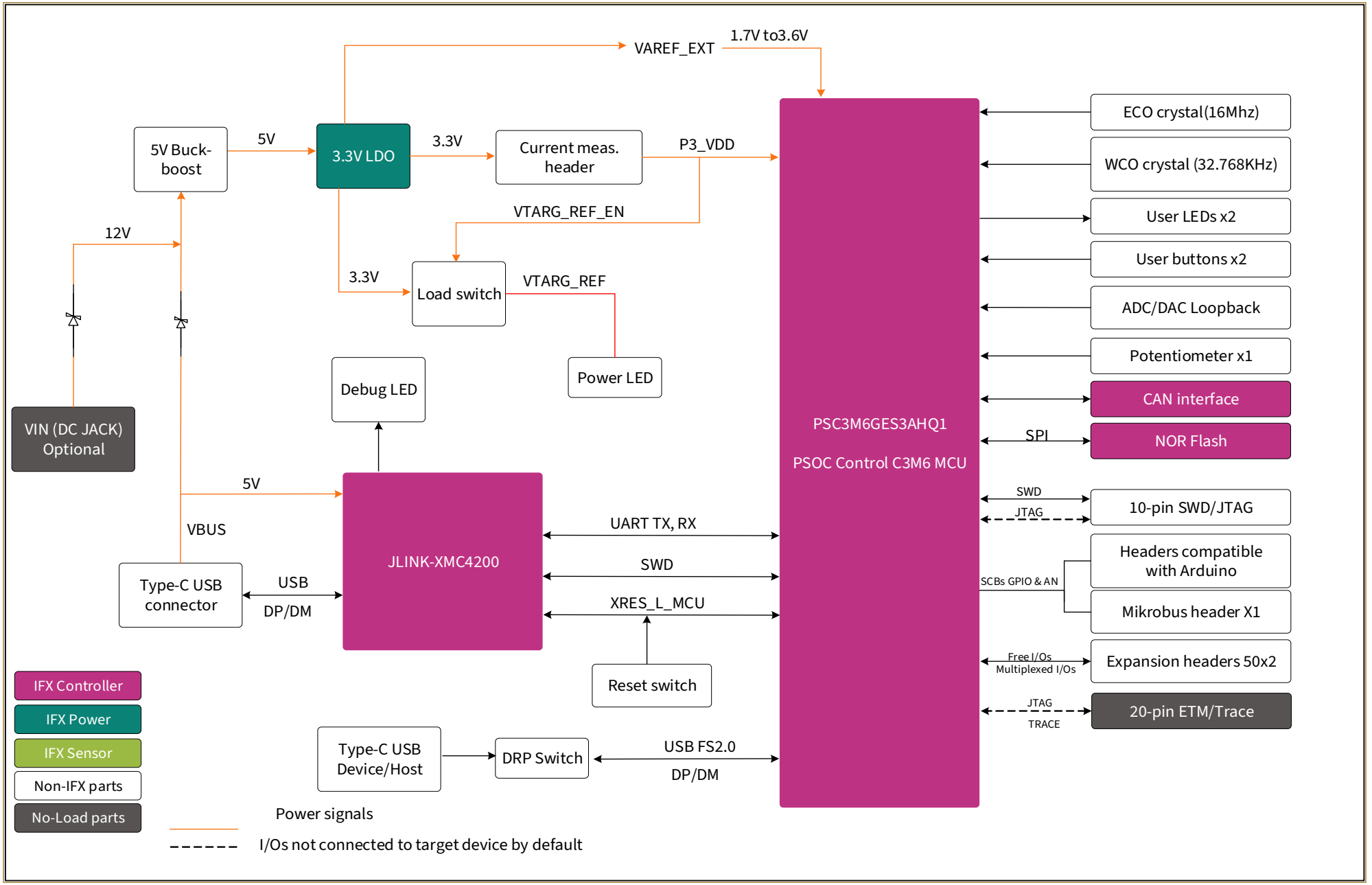
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SCH Title : PSOC CONTROL C3M6 EVALUATION KIT

Page Title: TITLE PAGE

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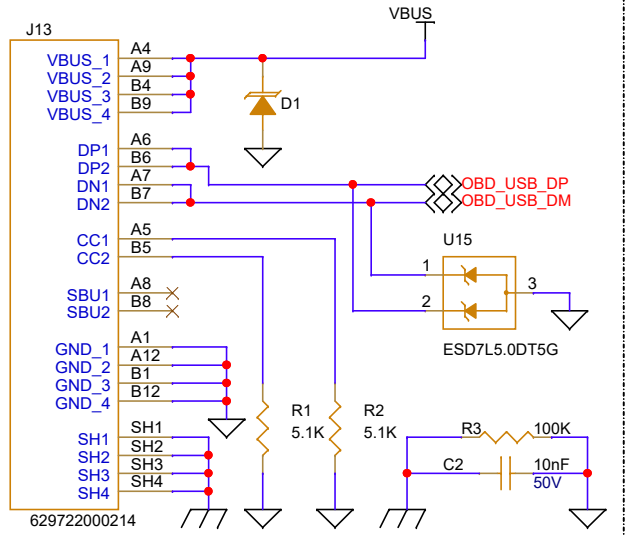
Date: Tuesday, July 07, 2026 Sheet 1 of 13



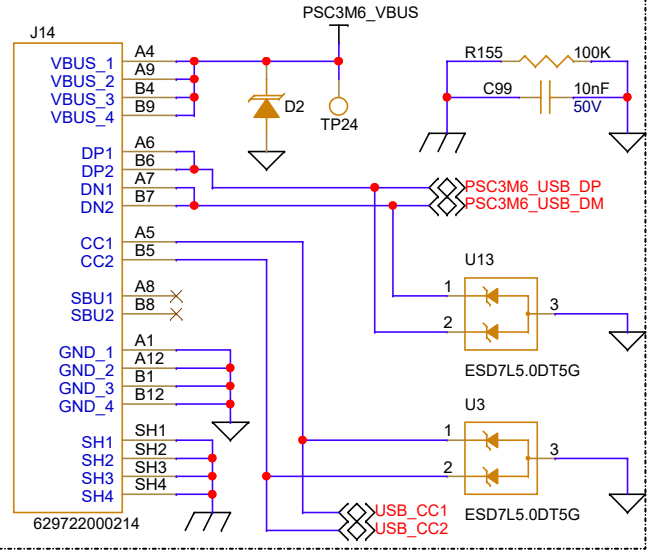
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SCH Title : PSOC CONTROL C3M6 EVALUATION KIT				
Page Title: BLOCK DIAGRAM				
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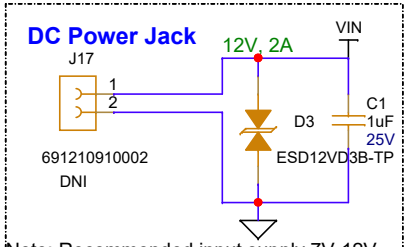
JLINK USB Interface and Power Supply



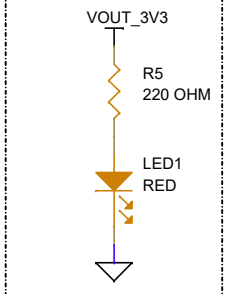
PSOC3M6 USB Device/Host Interface

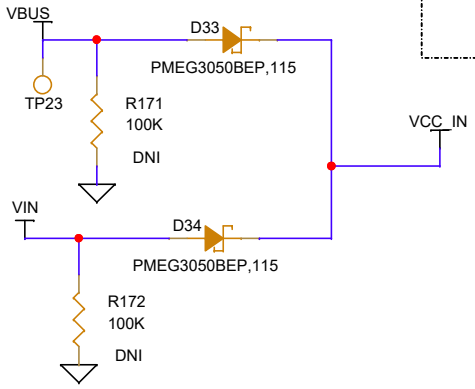


DC Power Jack

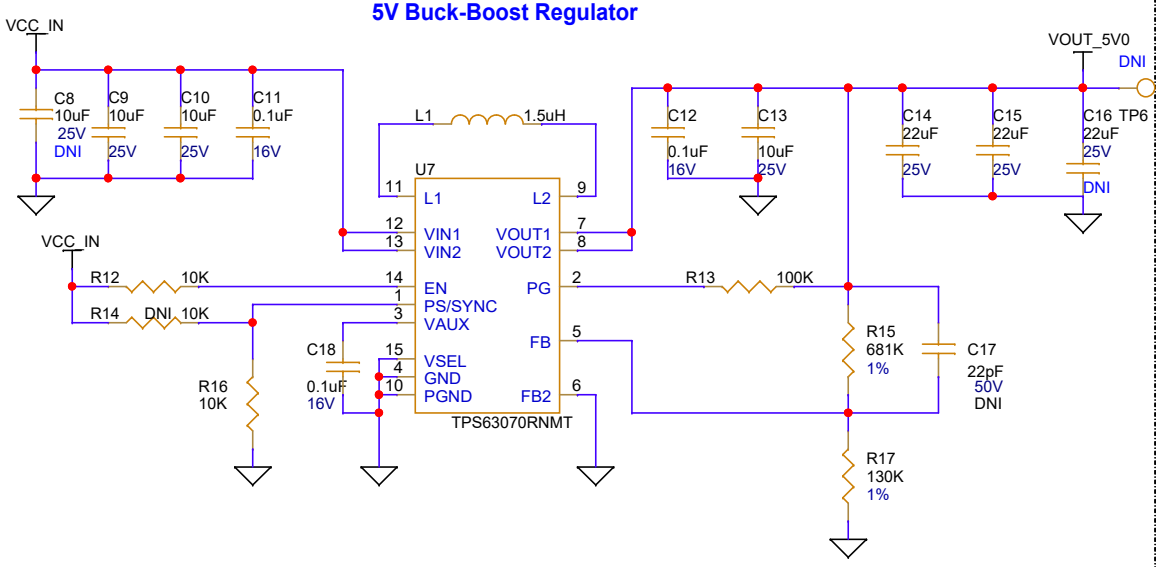


Power LED

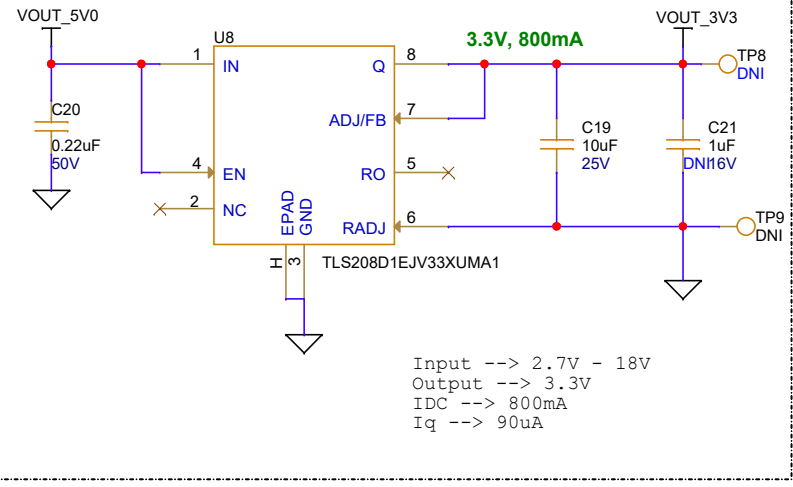




5V Buck-Boost Regulator



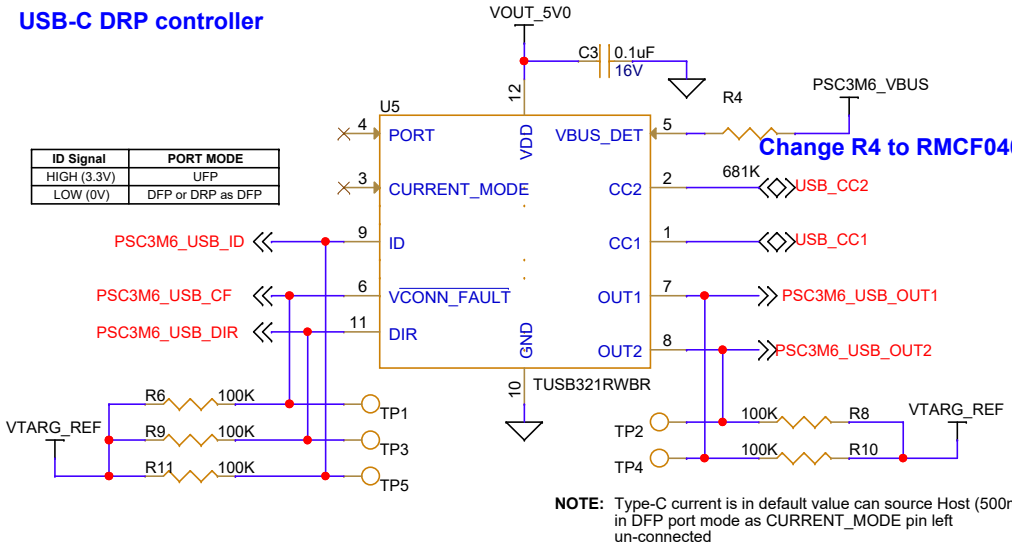
3.3V Power Supply (LDO)



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SCH Title : PSOC CONTROL C3M6 EVALUATION KIT				
Page Title: POWER SUPPLY-1				
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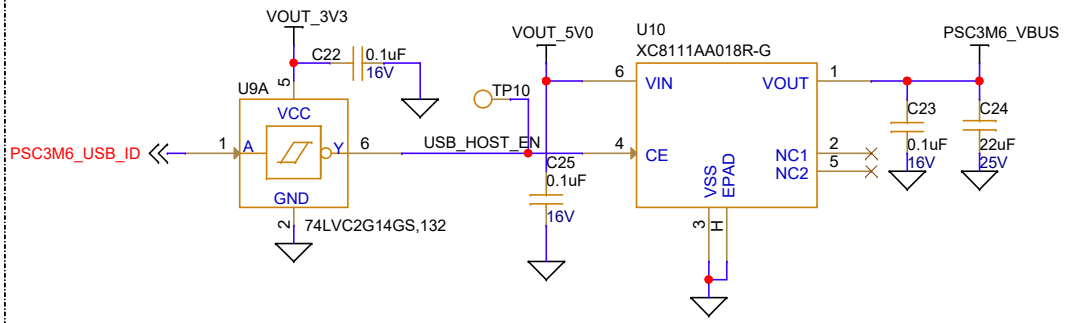
USB-C DRP controller



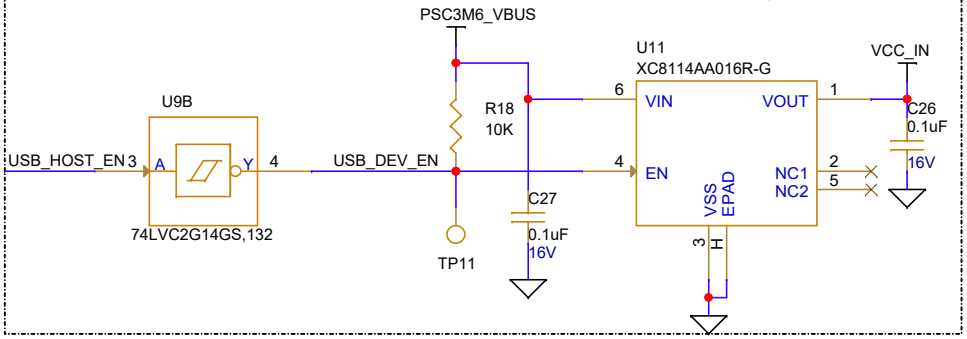
ID Signal	PORT MODE
HIGH (3.3V)	UFP
LOW (0V)	DFP or DRP as DFP

NOTE: Type-C current is in default value can source Host (500mA) in DFP port mode as CURRENT_MODE pin left un-connected

USB-C Host enable

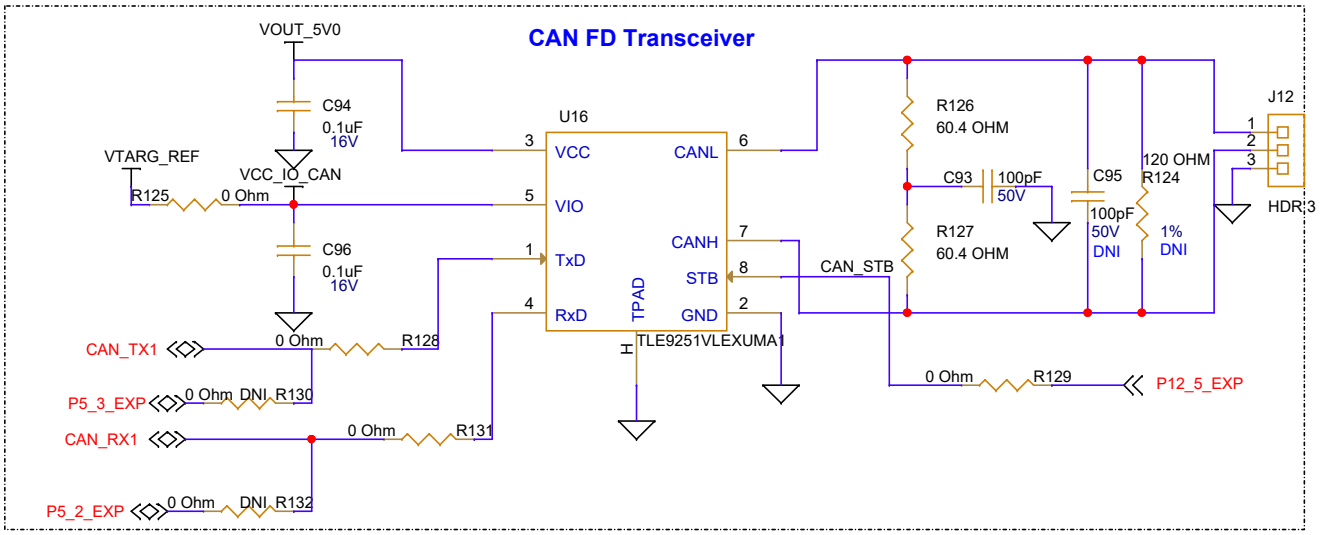


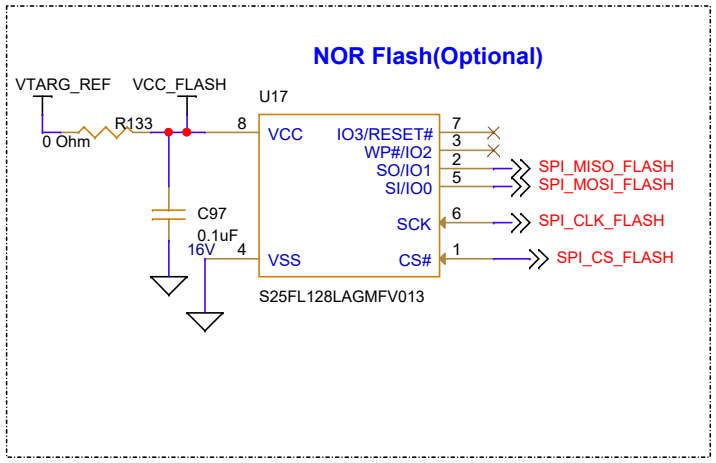
USB-C device enable and Ideal Diode Power OR'ing

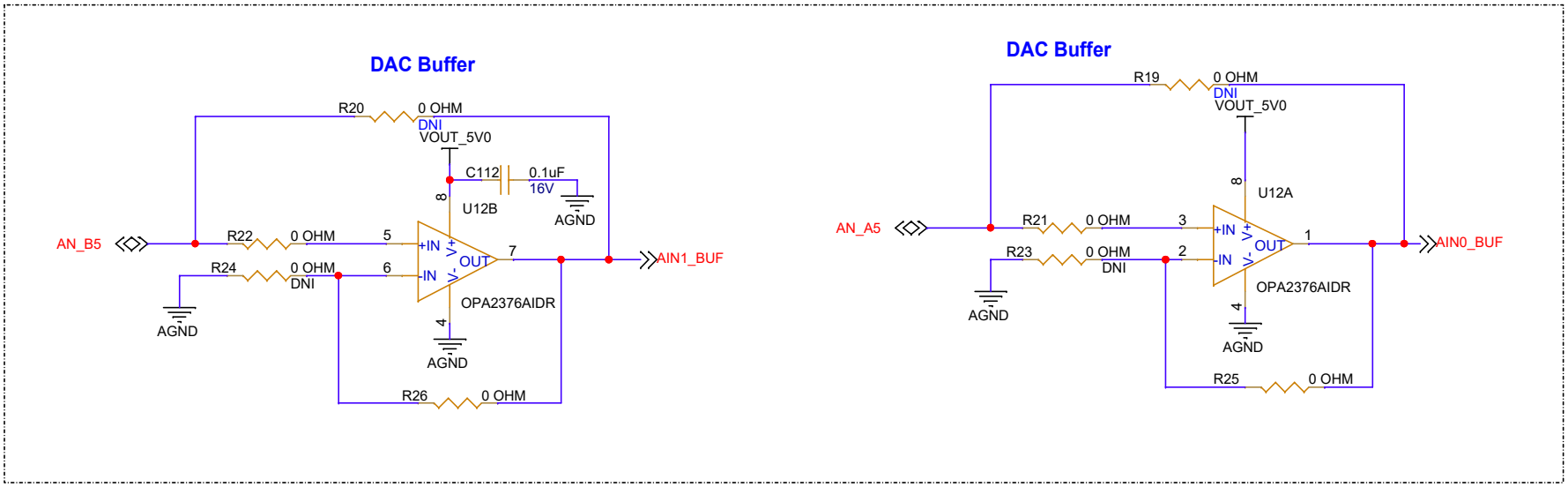


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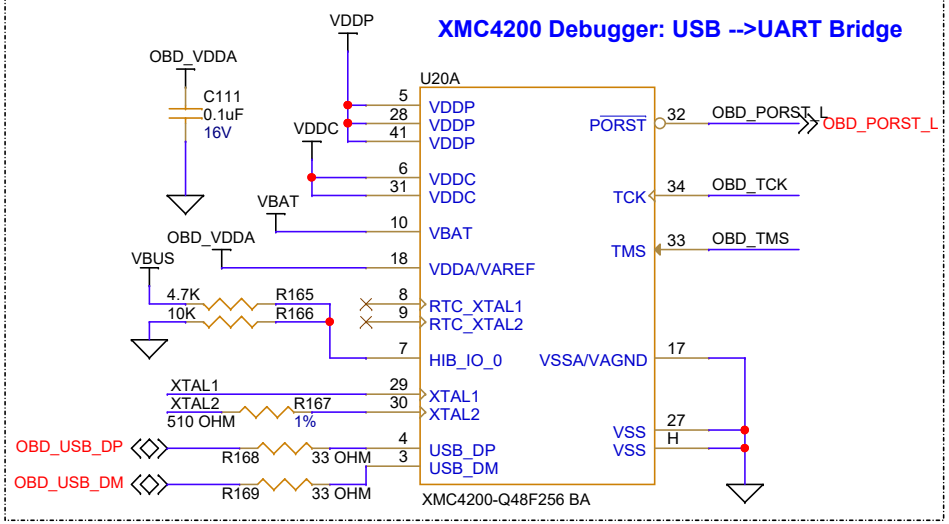


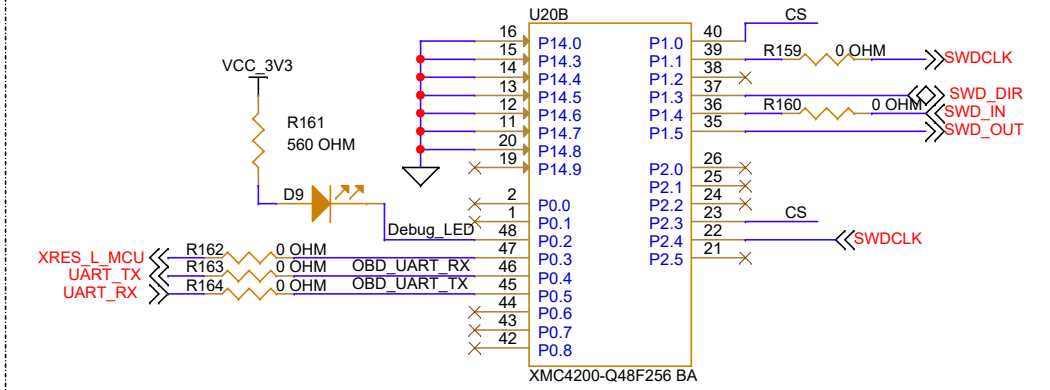


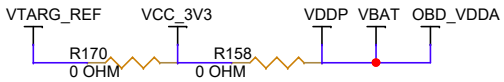
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Page Title: CAN AND DAC				
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Date: Tuesday, July 07, 2026			Sheet 5 of 13	

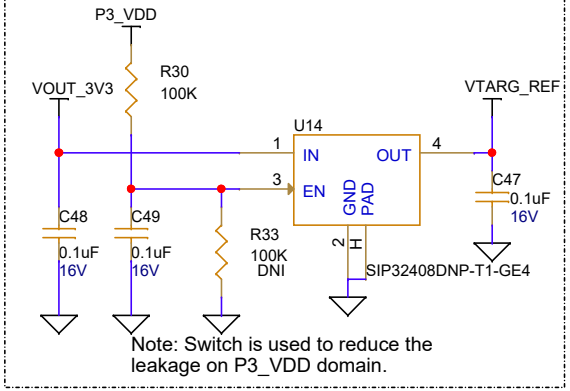
XMC4200 Debugger: USB -->UART Bridge



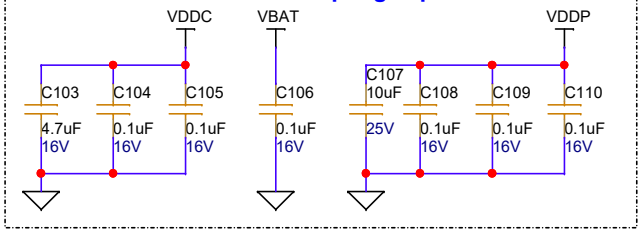




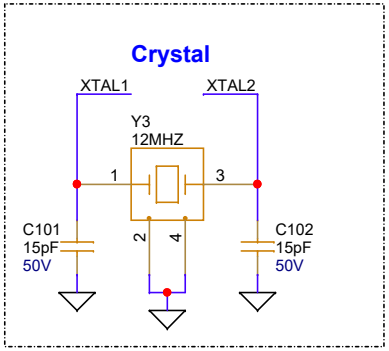
VTARG REF. Voltage



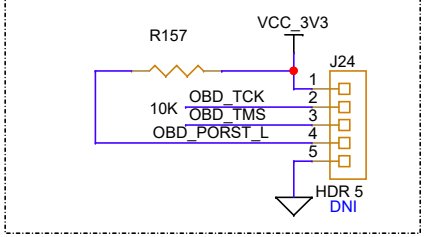
XMC4200 Decoupling capacitors

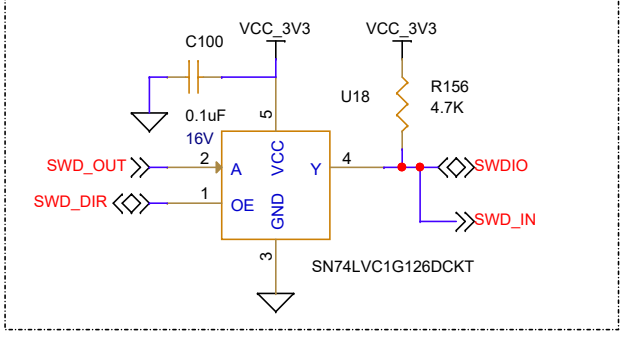


Crystal



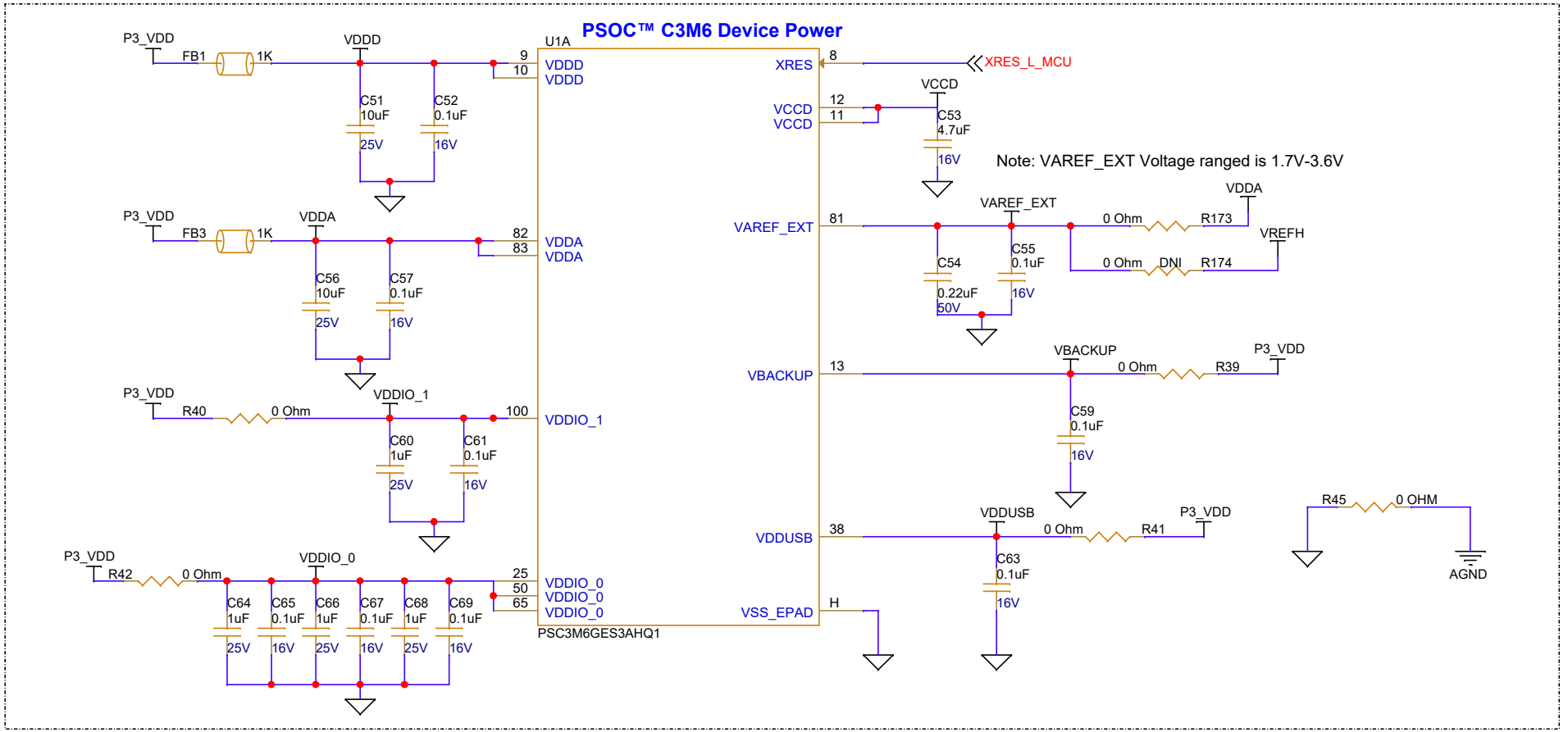
XMC4200 SWD Header



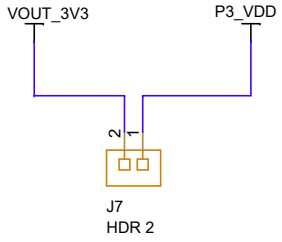


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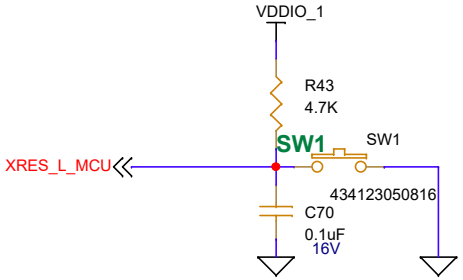
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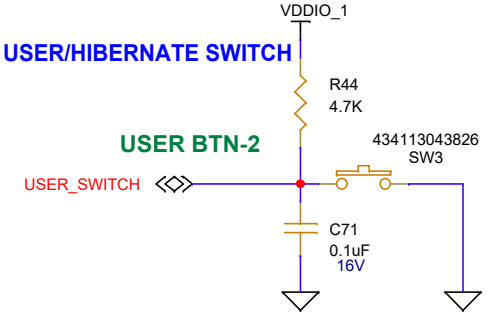
Current Measurement Jumper



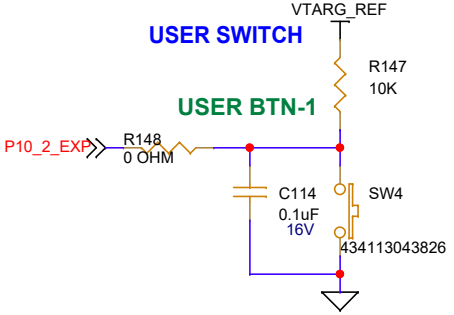
Reset Button



USER/HIBERNATE SWITCH



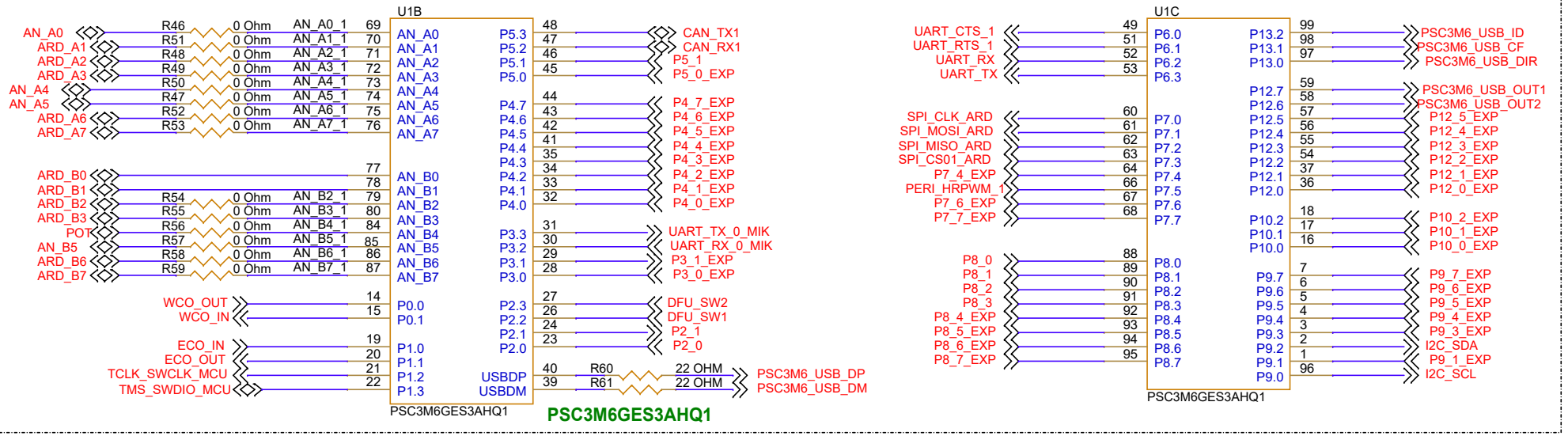
USER SWITCH



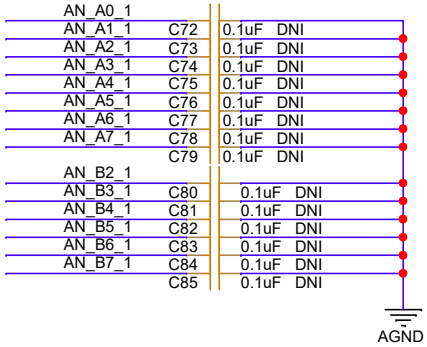
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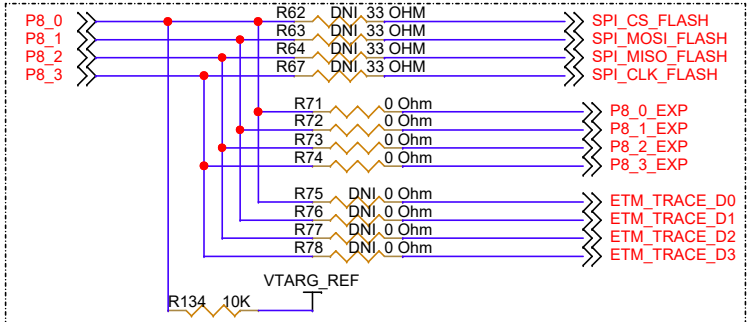
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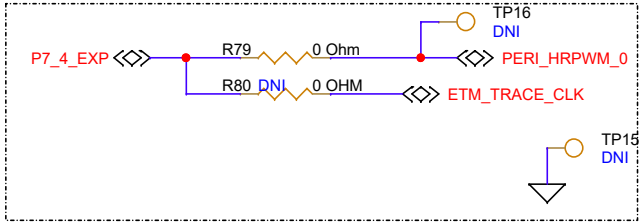
PSOC™ C3M6 I/Os



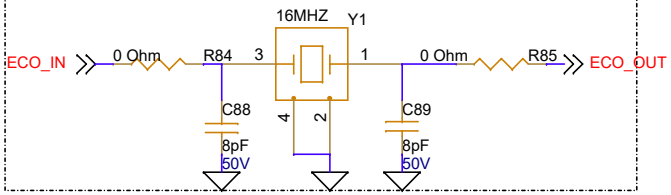
Antialiasing filter provision for ADC input



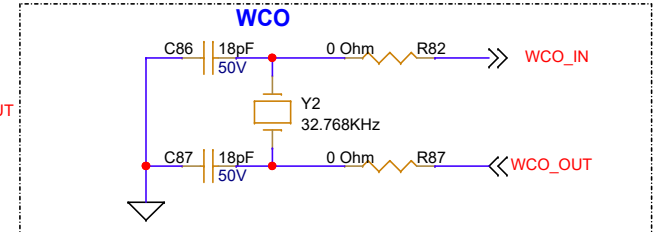


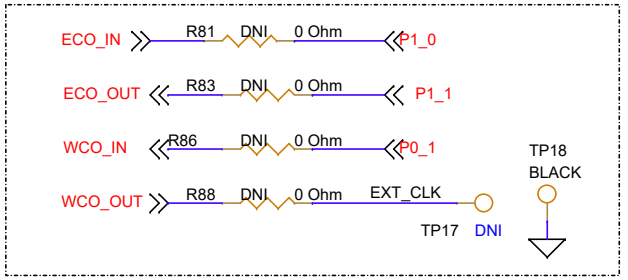


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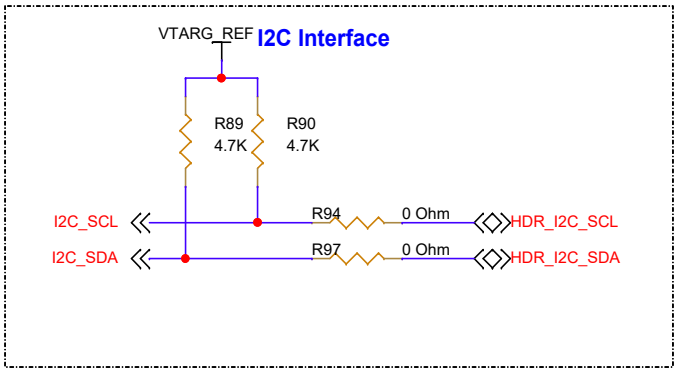
WCO

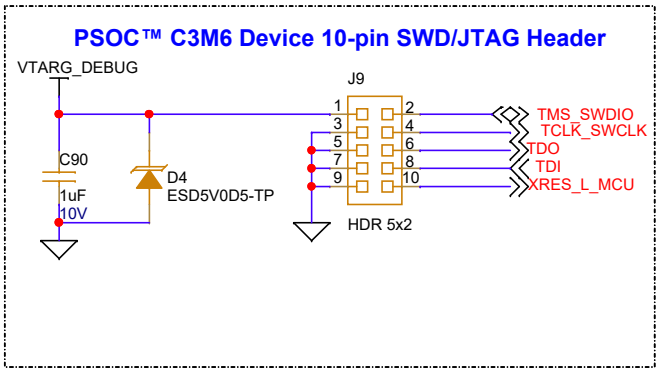


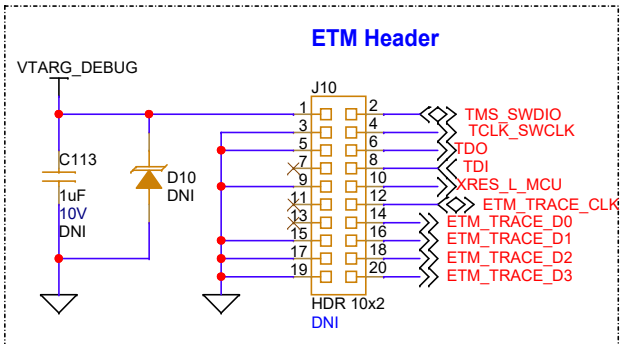


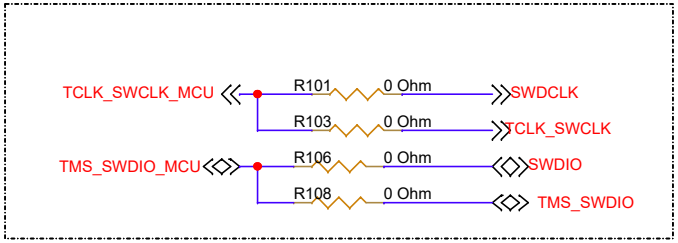
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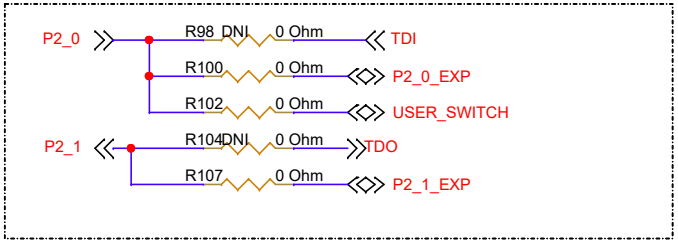
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Page Title: **PSOC C3 MCU SIGNAL**				
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Date: Saturday, September 12, 2026		Sheet **8** of **13**		

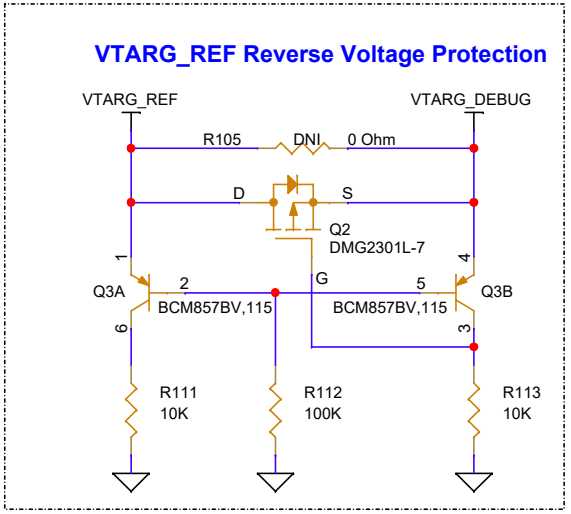


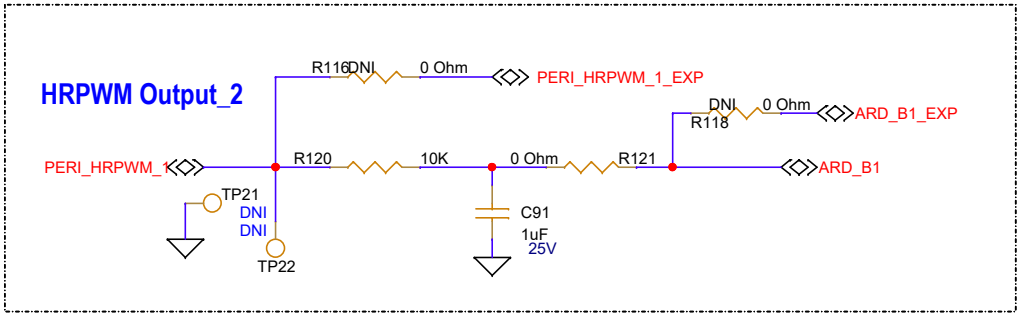


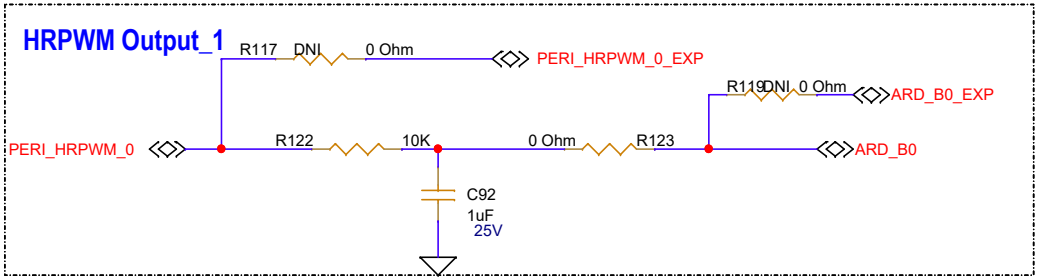








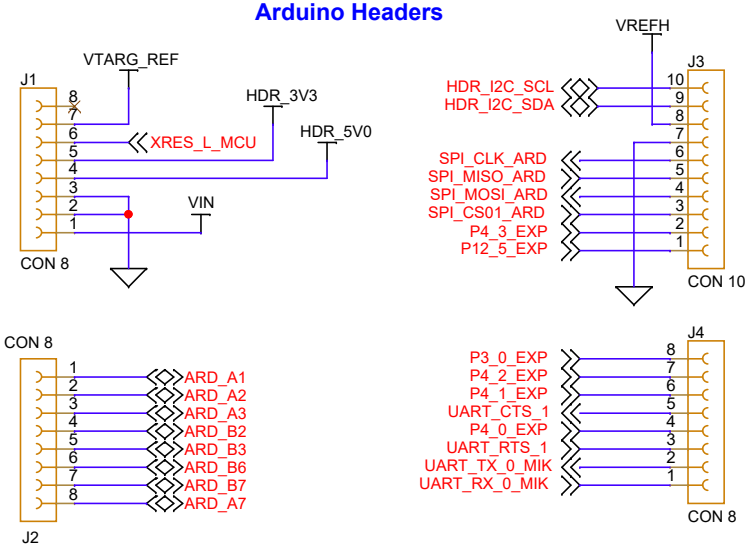




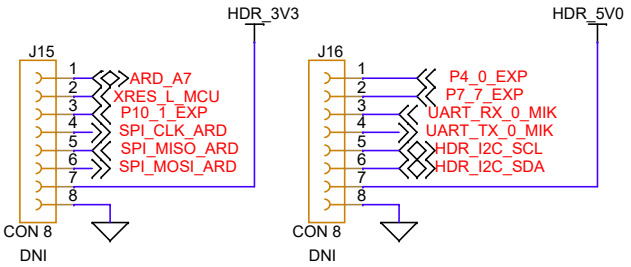
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SCH Title : PSOC CONTROL C3M6 EVALUATION KIT				
Page Title: COMMUNICATION AND PROGRAM				
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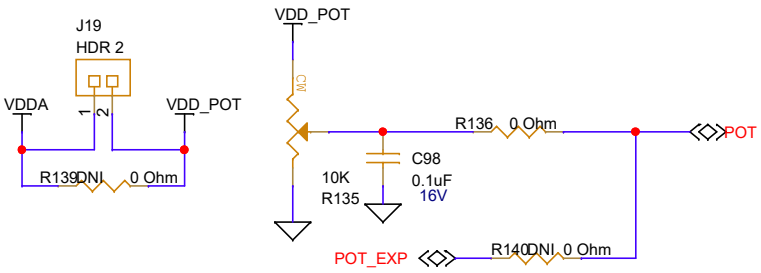
Arduino Headers



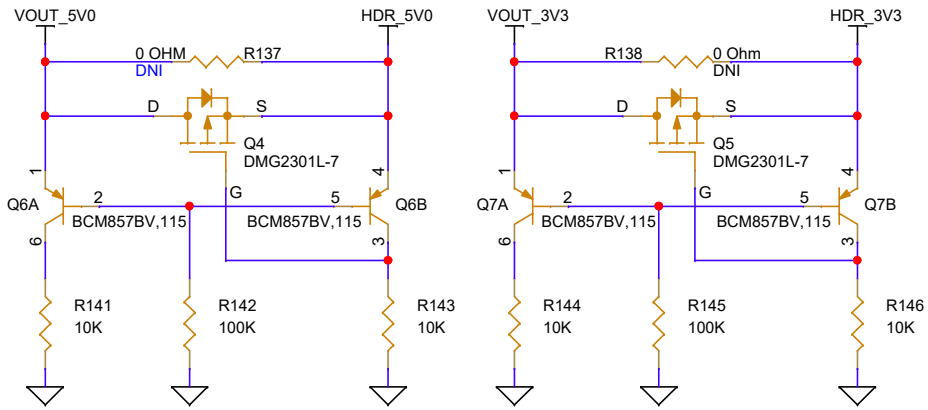
mikroBUS Headers



Potentiometer



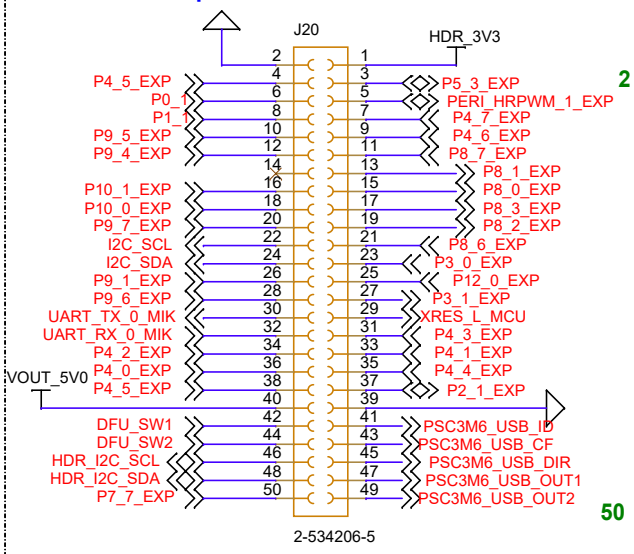
Reverse Voltage Protection for Arduino Power



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Expansion Headers-1



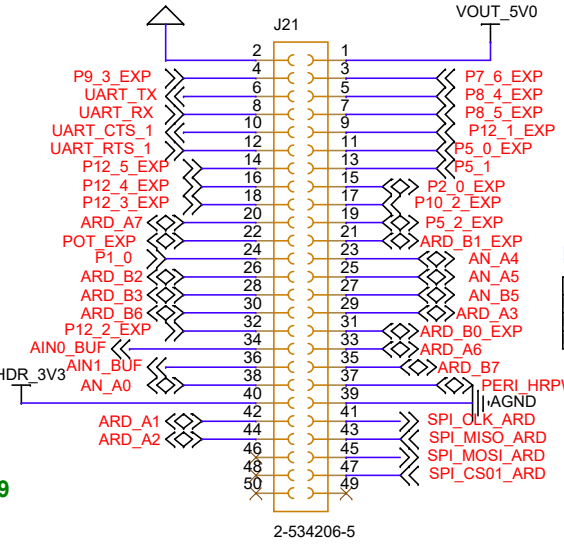
J20

2	GND	3.3V	1
	P4.5	P5.3	
	P0.1	P7.5	
	P1.1	P4.7	
	P9.5	P4.6	
	P9.4	P8.7	
	NC	P8.1/MOSI	
	P10.1	P8.0/SS	
	P10.0	P8.3/CLK	
	P9.7	P8.2/MISO	
	P9.0/SCL	P8.6	
	P9.2/SDA	P3.0	
	P9.1	P12.0	
	P9.6	P3.1	
	P3.3/TX	RESET	
	P3.2/RX	P4.3	
	P4.2	P4.1	
	P4.0	P4.4	
	P4.5	P2.1	
	5V	GND	
	P2.2	P13.2	
	P2.3	P13.1	
	P9.0	P13.0	
	P9.2	P12.7	
50	P7.7	P12.6	49

J21

2	GND	5V	1
	P9.3	P7.6/OPOUT	
	P6.3/TX	P8.4	
	P6.2/RX	P8.5	
	P6.0/CTS	P12.1	
	P6.1/RTS	P5.0/I3CSDA	
	P12.5	P5.1/I3CSCL	
	P12.4	P2.0	
	P12.3	P10.2	
	AN_A7	P5.2	
	AN_B4	AN_B1	
	P1.0	AN_A4	
	AN_B2	AN_A5	
	AN_B3	AN_B5	
	AN_B6	AN_A3	
	P12.2	AN_B0	
	AN_A5/DAC	AN_A6	
	AN_B5/DAC	AN_B7	
	AN_A0	P7.4/OPOUT	
	3.3V	AGND	
	AN_A1	P7.0/CLK	
	AN_A2	P7.2/MISO	
	NC	P7.1/MOSI	
	NC	P7.3/SS	
50	NC	NC	49

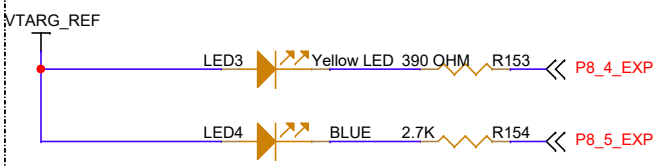
Expansion Headers-2

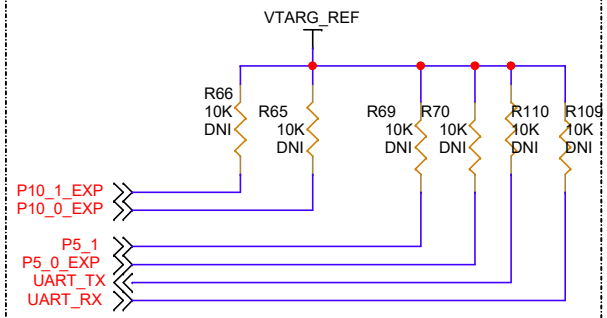


DAC - ADC Interface

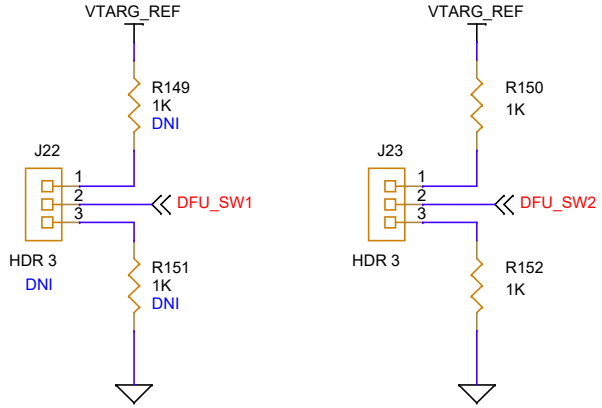
Jumper POS	Interface
Pin33 & Pin34	AIN0 to ARD_A6
Pin35 & Pin36	AIN1 to ARD_B7
Pin37 & Pin38	TCPWM to AIN2P

User LEDs





DFU Selection Jumper



Default position is 2 & 3 for J23

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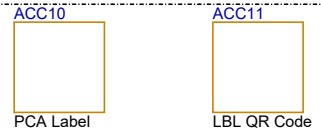
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Accessories

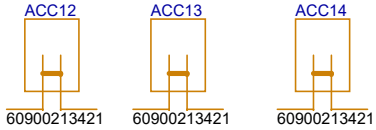
Cylindrical Bump-ons



PCBA Labels



Jumper Shunts



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REVISION HISTORY

REV	DESCRIPTION OF CHANGE	ORIG. OF CHANGE	DATE
01	Initial release version	RAJB	05/03/2026
02	Release version	SAMANTH	07/07/2026

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Page Title: REVISION HISTORY

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