



Student for IC Digital Design & Verification

You will:

- Perform RTL design and synthesis, gate level simulation, power and static timing analysis for digital blocks;
- Develop verification environments using dedicated tools&languages in order to check proper behavior of circuits.



You are best suited for the tasks if you had courses on:

- Electronic Devices and Circuits;
- Digital Integrated Circuits;
- Microprocessors Architectures;
- Object oriented programming.

Helpful skills:

- VHDL, Verilog, System Verilog and C/C++;
- Python, Perl or TCL for scripting.