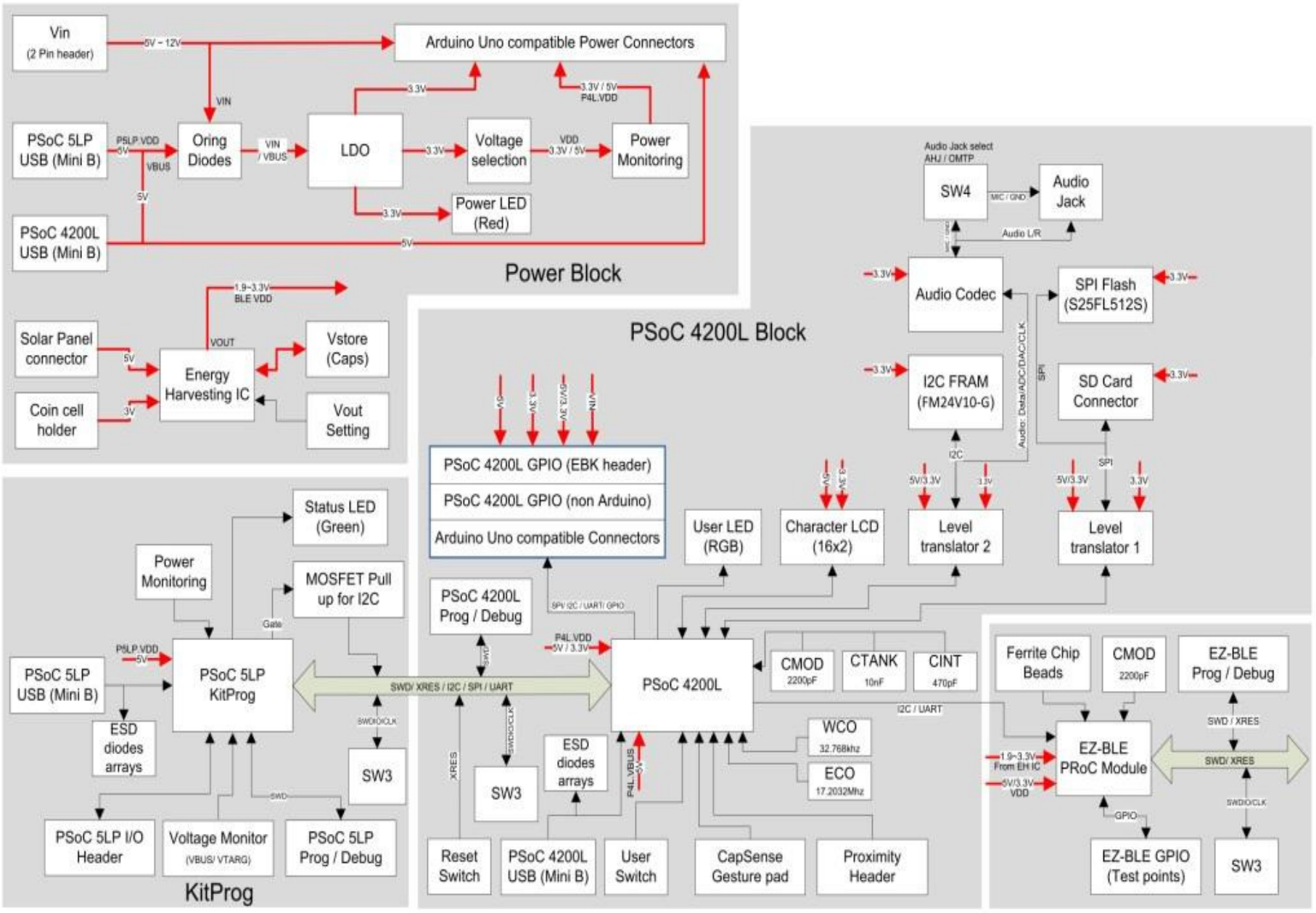
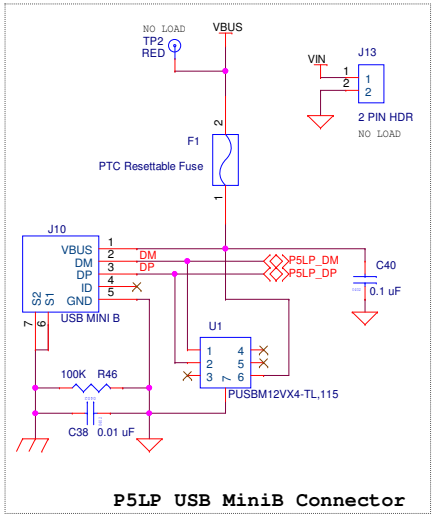
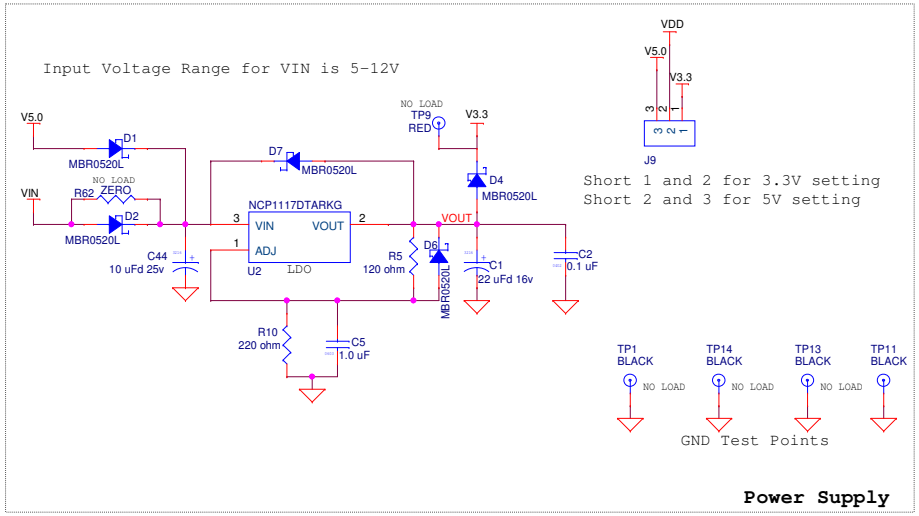
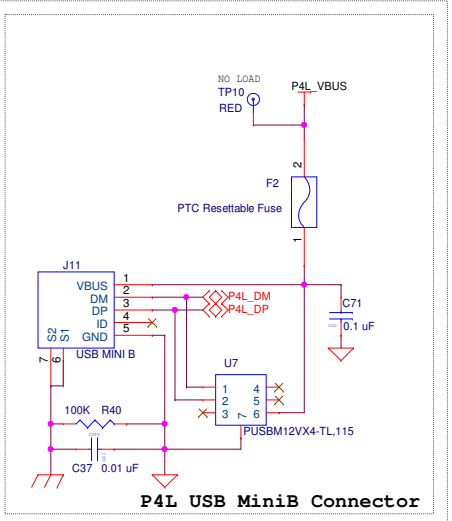


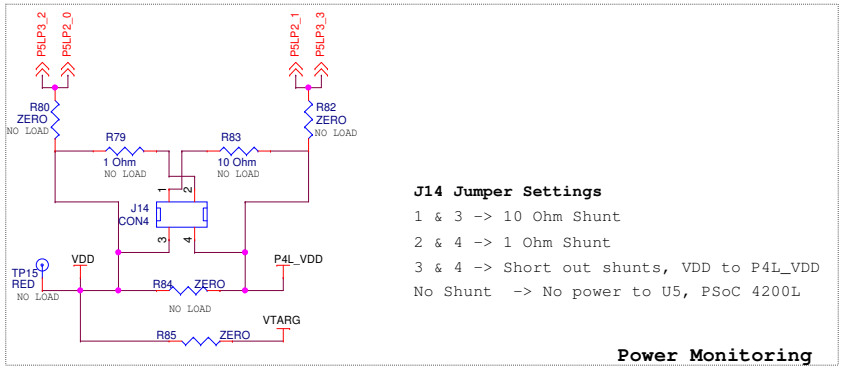
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ASSY DRW: 620-60273-01

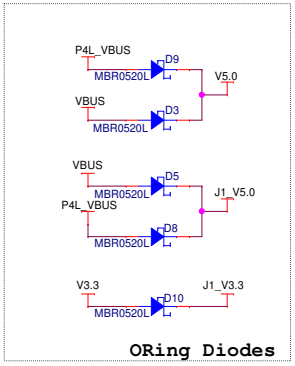
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Title: CY8CKIT-046 PSoC 4 L-Series Pioneer Kit		
Size: A3	Document Number: 630-60271-01	Rev: 06
Date: Monday, October 12, 2015	Sheet 1 of 8	

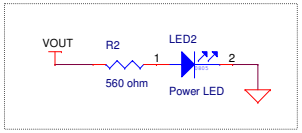




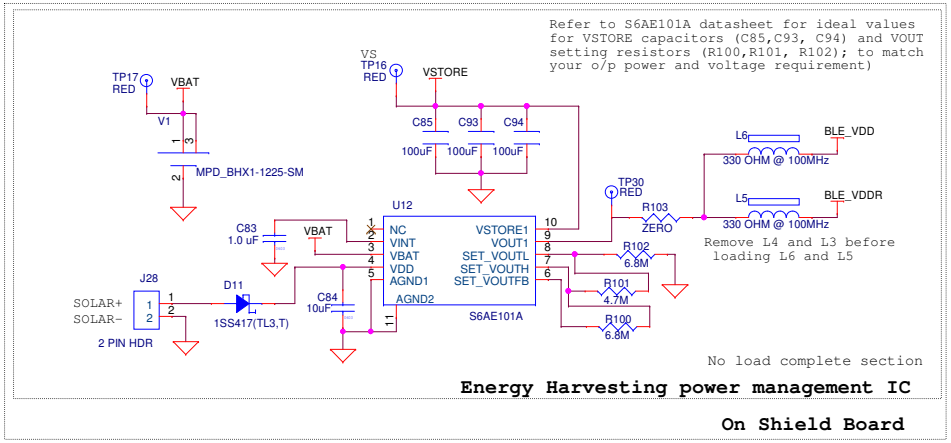




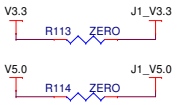




On Main Board



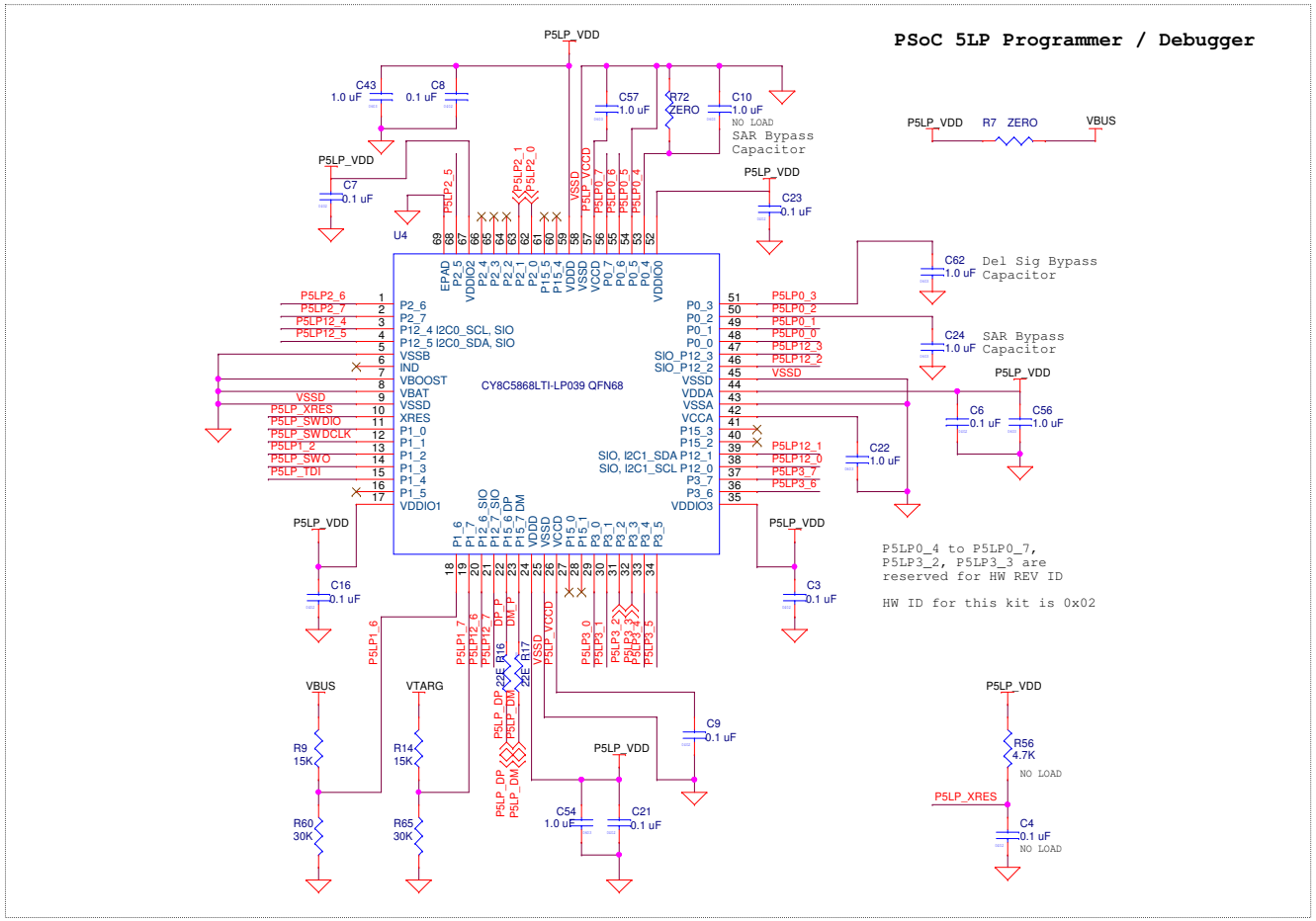
On Shield Board

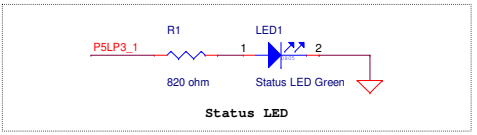


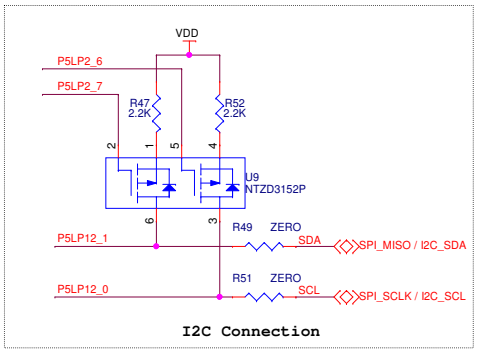
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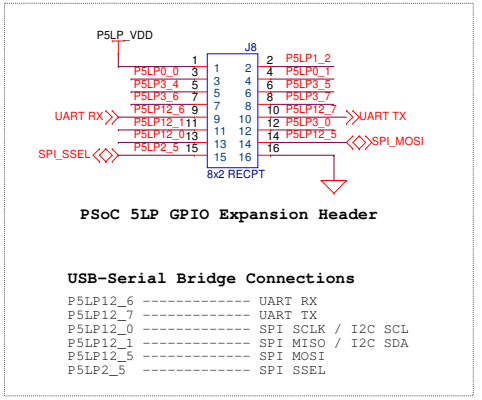
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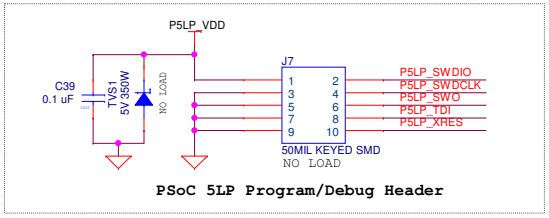
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Date: Monday, October 12, 2015	Sheet 2 of 8	

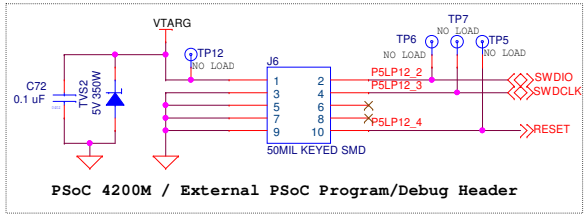








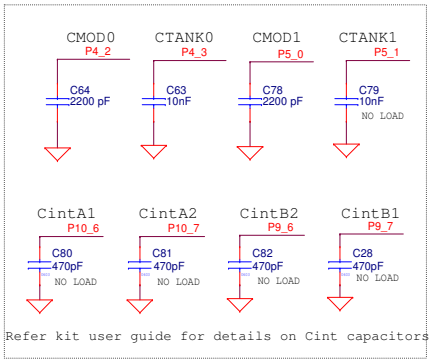


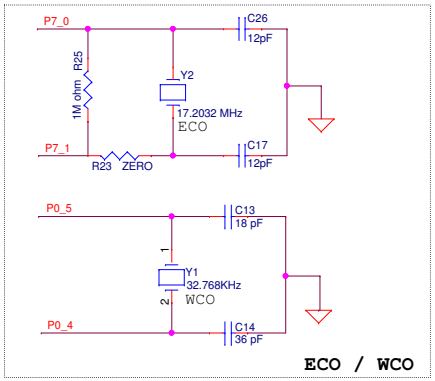


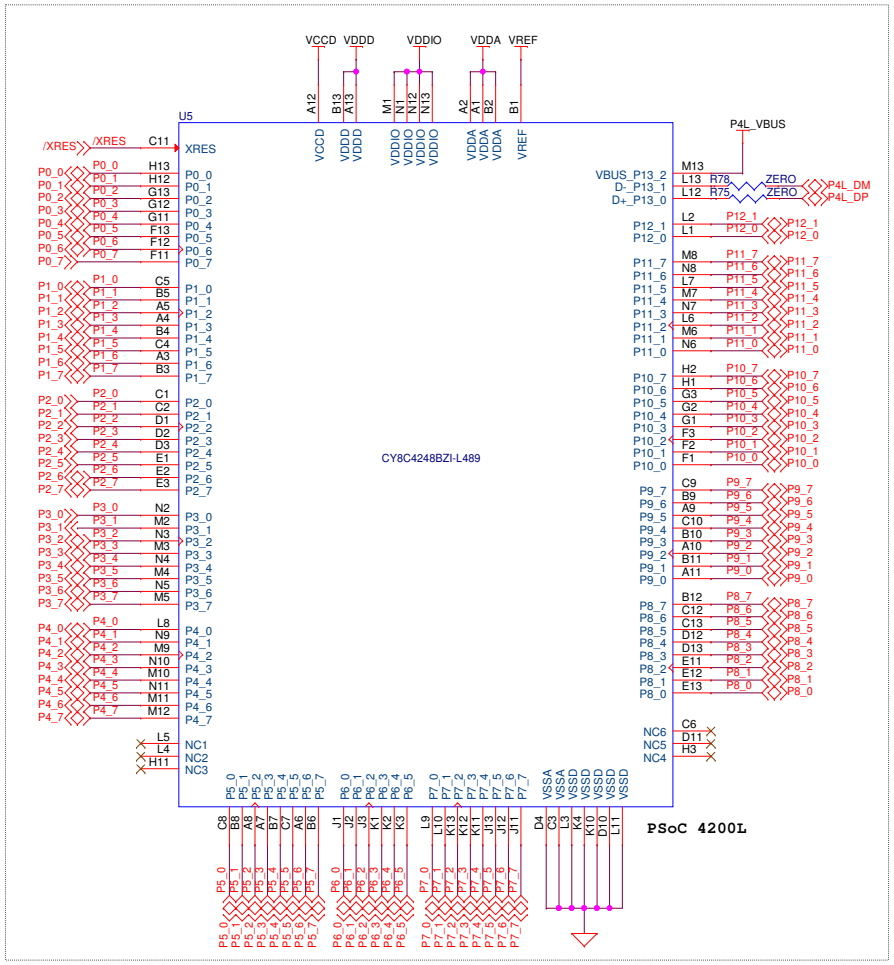
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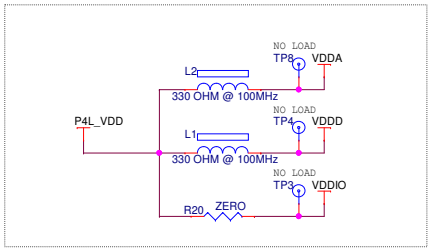
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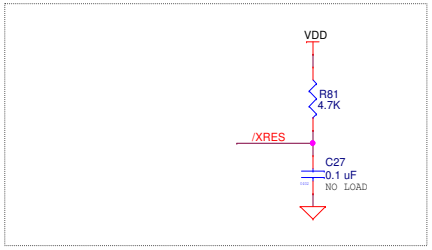
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Date: Monday, October 12, 2015	Sheet 3 of 8	

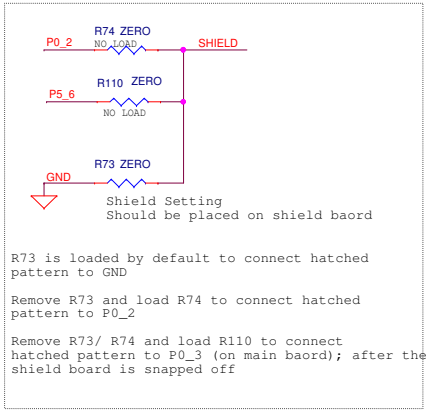


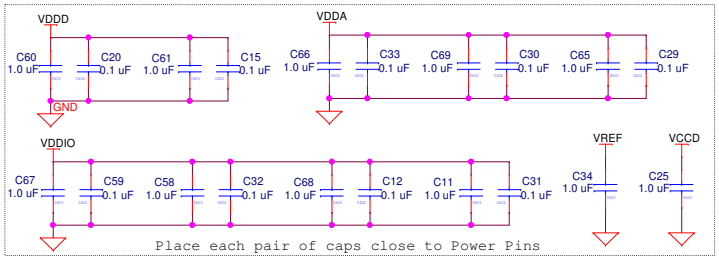








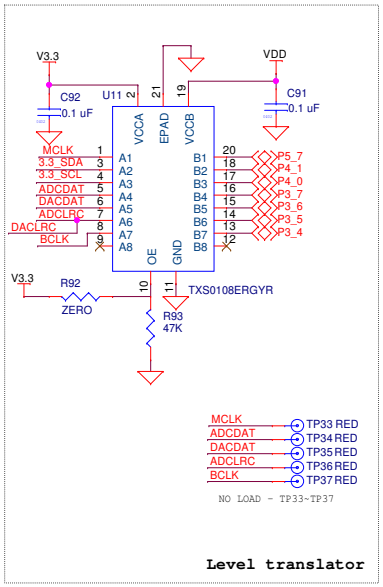




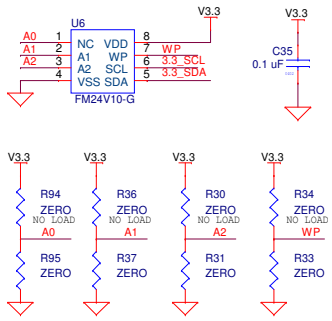
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Title: CY8CKIT-046 PSoC 4 L-Series Pioneer Kit		
Size: A3	Document Number: 630-60271-01	Rev: 06
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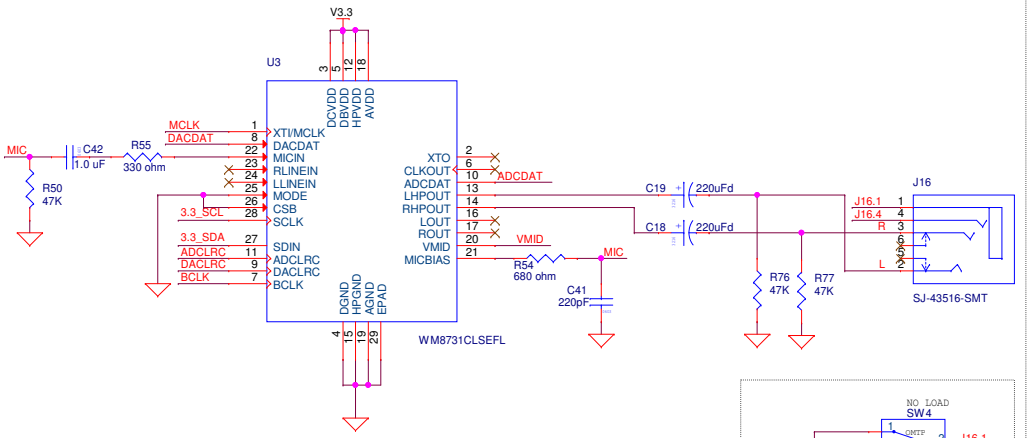
Level translator



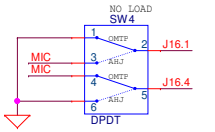
Device Select Address

The 8-pin SOIC footprint provided for the F-RAM FM24V10 on the PSoC 4 L-Series Pioneer Kit is compatible with all I2C-based F-RAM devices from Cypress (FM24Vxx, FM24CLxx, and CY15BxxxJ parts). The F-RAM parts with more than 64 KB size support only four addresses (four devices of the same type on the same I2C bus); resistors connected to A1 (R36/R37) and A2 (R30/ R31) pins can be used to select any of the four addresses. The F-RAM parts with less than 64 KB and FM24CLxx parts support eight addresses; resistors connected to A0 (R94/R95), A1 (R36/R37), and A2 (R30/R31) pins can be used to select one of the eight addresses

F-RAM



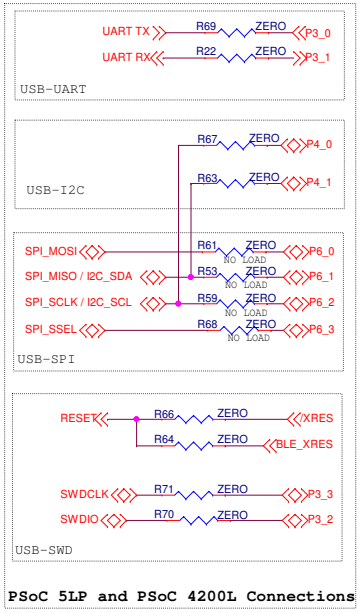
Place each pair of caps close to Power Pins



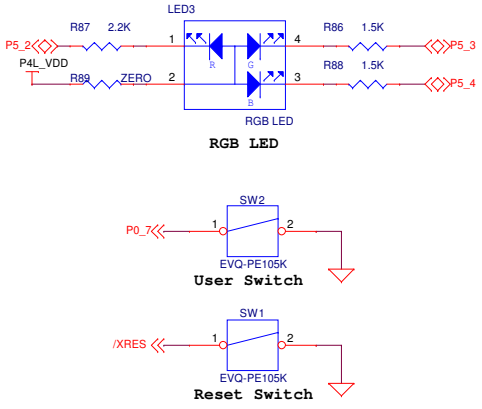
Audio Jack Section

Default - AHJ, when R111 and R112 are populated.
For OMTP remove R111, R112 and populate SW4.

Audio Codec



PSoC 5LP and PSoC 4200L Connections



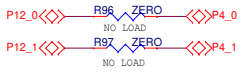
RGB LED

User Switch

Reset Switch

User Interface

Place this on main baord

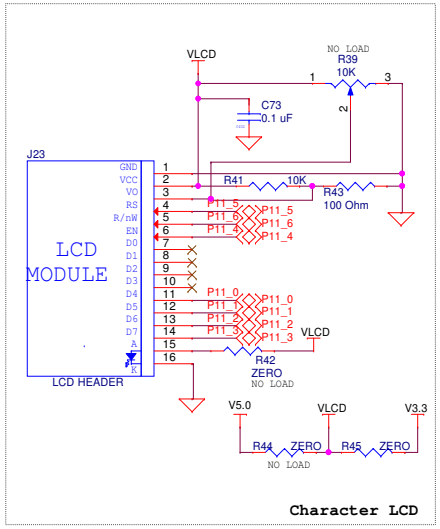


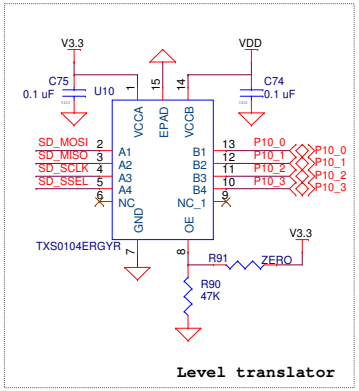
Load these to make a common I2C bus

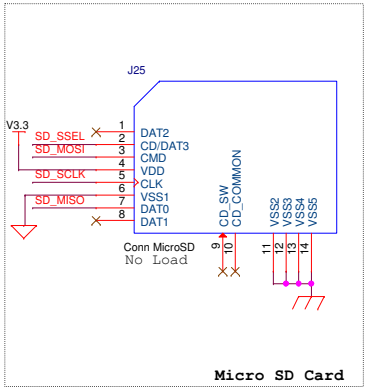
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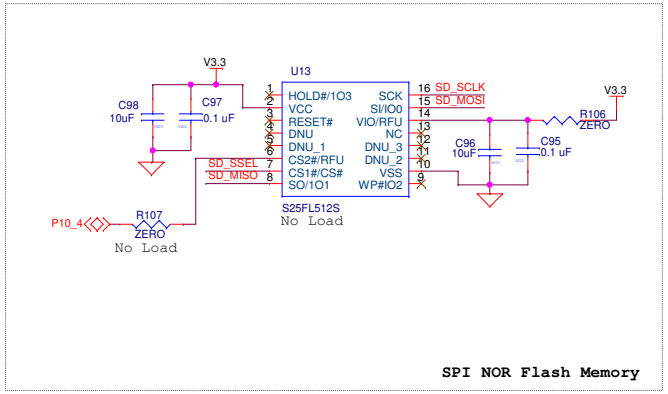
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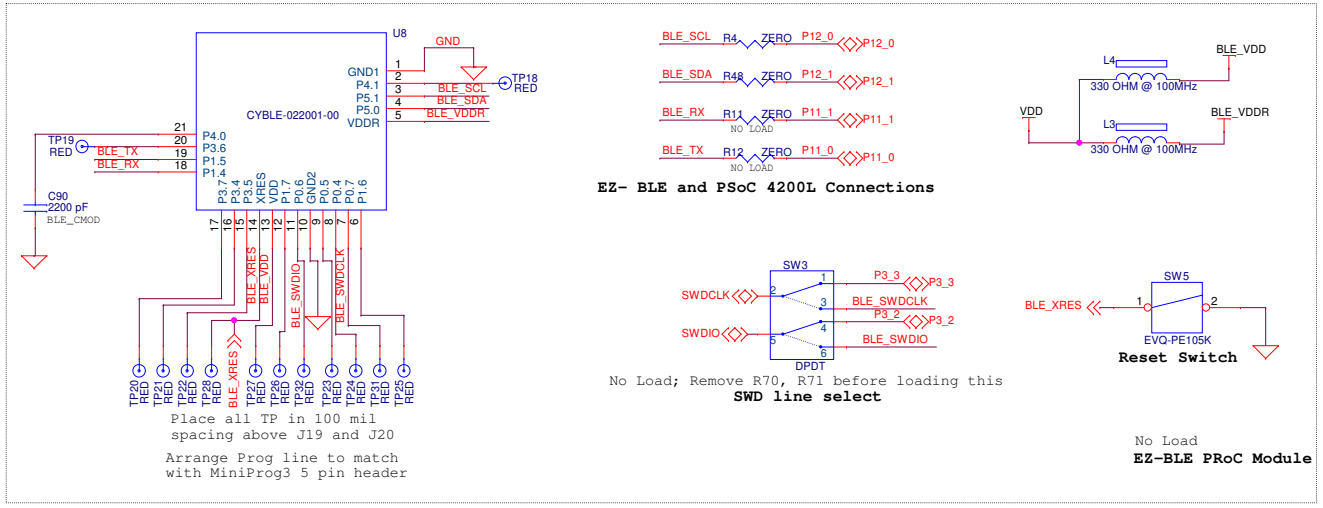
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Size A3	Document Number 630-60271-01	Rev 06
Date: Monday, October 12, 2015	Sheet 5 of 8	

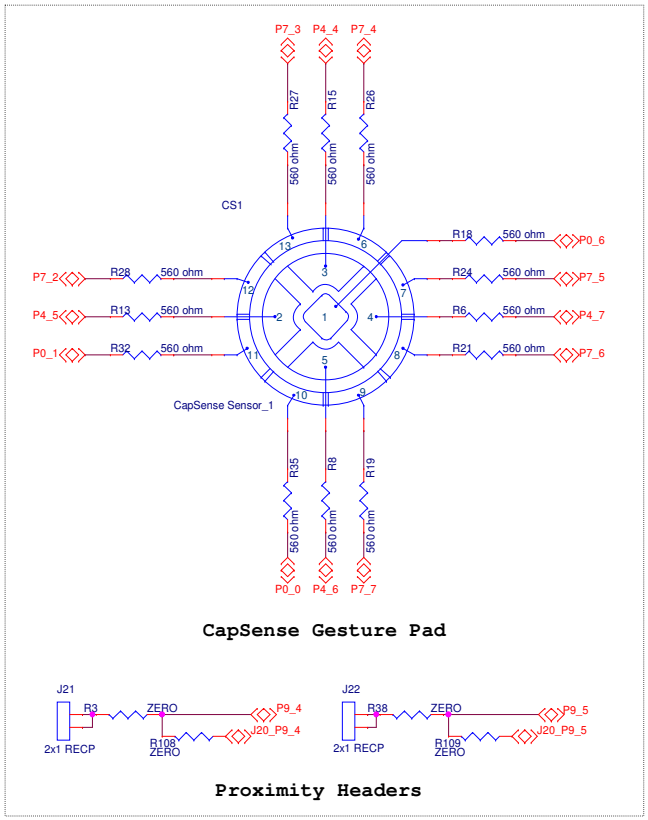




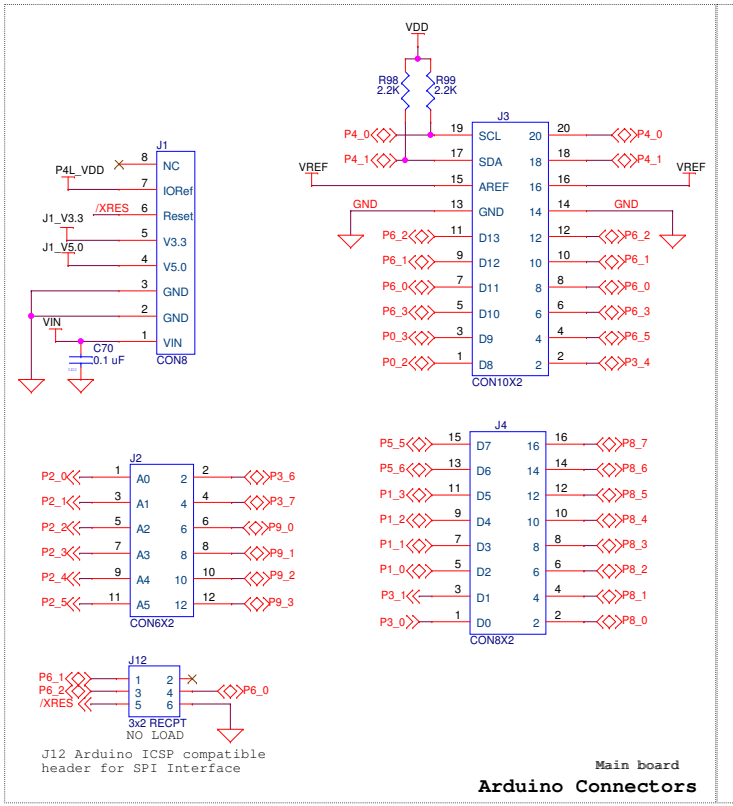


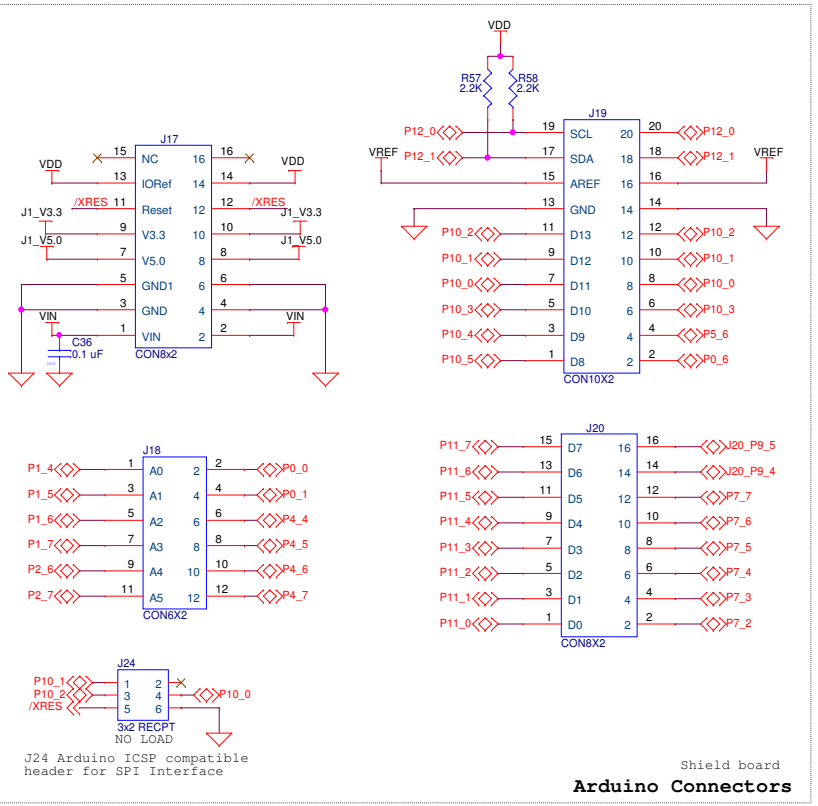


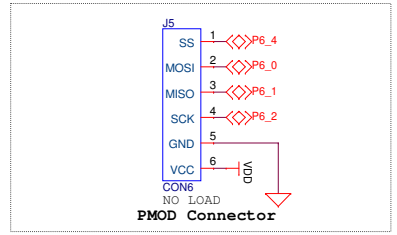


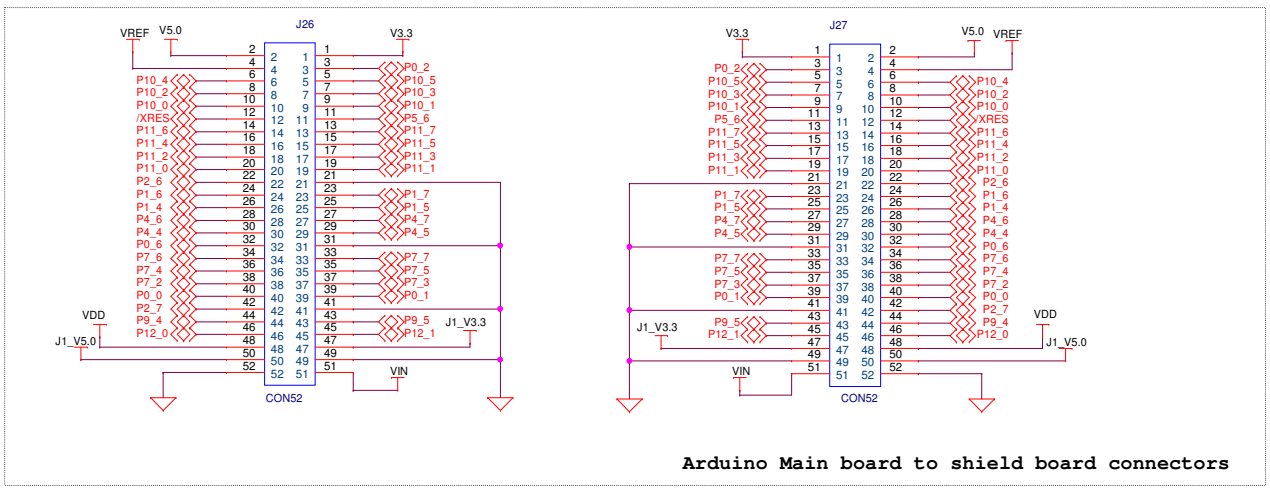


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Arduino Main board to shield board connectors

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PCB: 600-60276-01
FAB DRW: 610-60265-01
ASSY DRW: 620-60273-01

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