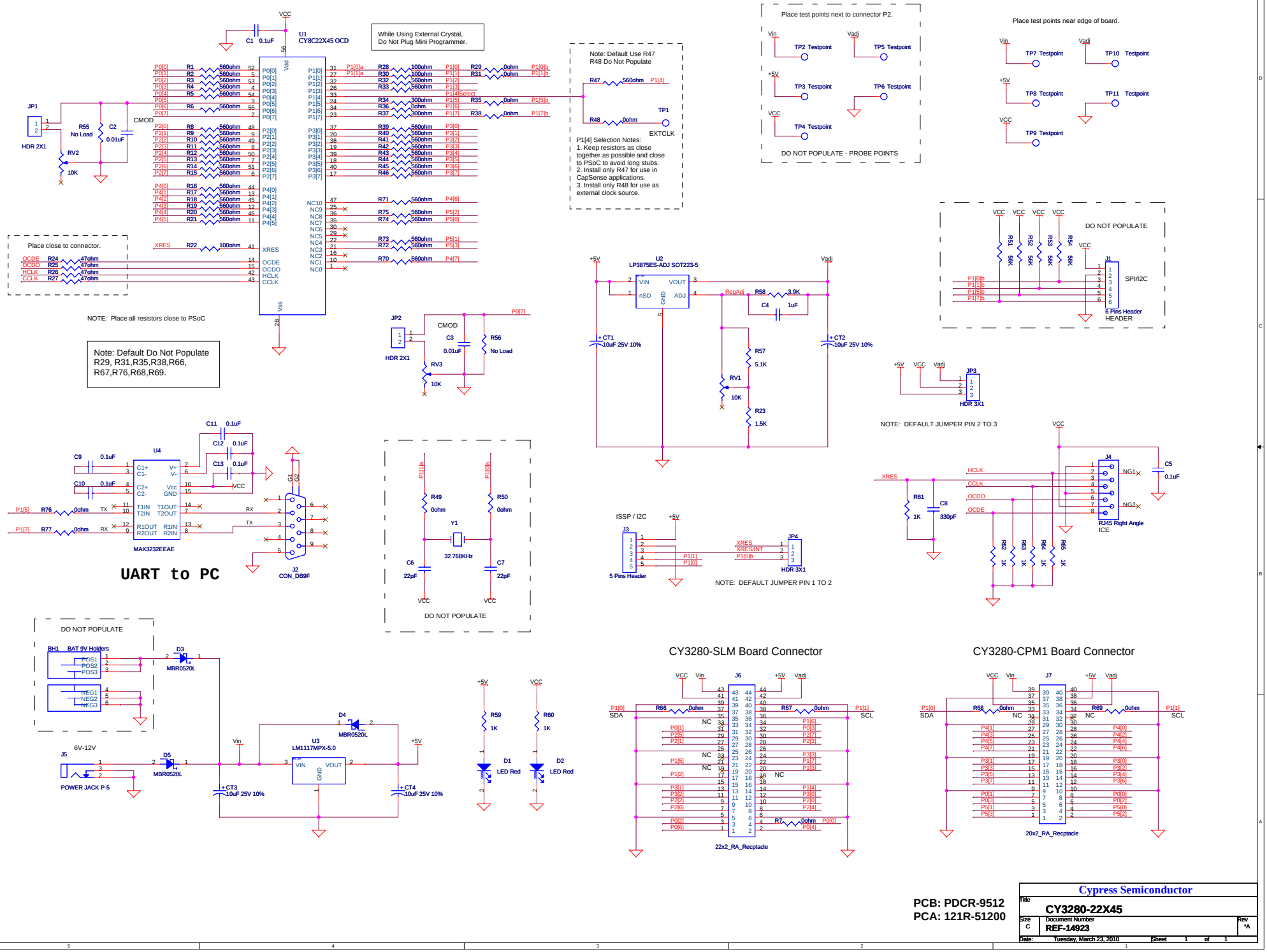
U1
CY8C22X45 OCD
While Using External Crystal,
Do Not Plug Mini Programmer.
Note: Default Use R47
R48 Do Not Populate
P1[4] Selection Notes:
1. Keep resistors as close together as possible and close to PSoC to avoid long stubs.
2. Install only R47 for use in CapSense applications.
3. Install only R48 for use as external clock source.
Place test points next to connector P2.
DO NOT POPULATE - PROBE POINTS
Place test points near edge of board.
Place close to connector.
NOTE: Place all resistors close to PSoC
Note: Default Do Not Populate
R29, R31,R35,R38,R66,
R67,R76,R68,R69.
DO NOT POPULATE
U2
LP3875ES-ADJ SOT223-5
NOTE: DEFAULT JUMPER PIN 2 TO 3
NOTE: DEFAULT JUMPER PIN 1 TO 2
UART to PC
MAX3232EEAE
ISSP / I2C
5 Pins Header
6 Pins Header
HEADER
SPI/I2C
RJ45 Right Angle
ICE
32.768KHz
CY3280-SLM Board Connector
CY3280-CPM1 Board Connector
22x2_RA_Recptacle
20x2_RA_Recptacle
POWER JACK P-5
6V-12V
LED Red
PCB: PDCR-9512
PCA: 121R-51200
Cypress Semiconductor
CY3280-22X45
REF-14923
Tuesday, March 23, 2010