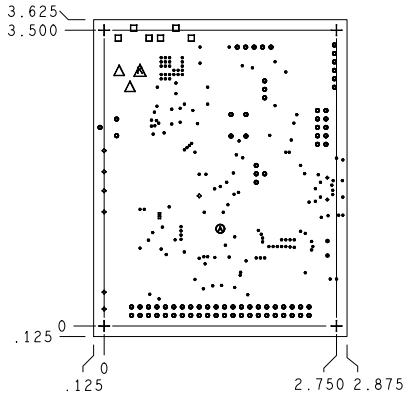
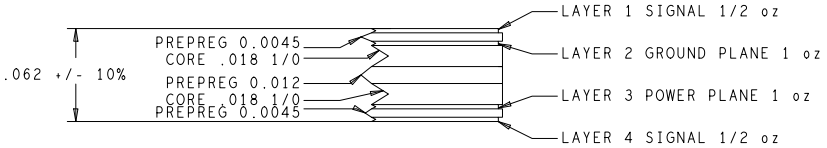
REVISIONS			
REV	DESCRIPTION	DATE	APPROVED
01	RELEASED TO MANUFACTURING	07/01/08	GIR
02	RELEASED TO MANUFACTURING	12/03/08	GIR
03	RELEASED TO MANUFACTURING	01/26/09	GIR
**	RELEASED TO MANUFACTURING	04/02/09	GIR
*A	ADDED CAP TO Y1 SIG P1[1],P1[7] INTERCHANGED	12/10/10	RKPM





NOTE: 9 MIL 17 MIL SPACING TRACES ON LAYER 1 & 2 DIFFERENTIAL IMPEDANCE CONTROLLED TO 90 OHMS +/- 10% TESTED AND CERTIFIED (VENDOR ALLOWED TO ADJUST DIELECTRIC AND LINE WIDTH TO MATCH IMPEDANCE).

DRILL CHART: TOP to BOTTOM ALL UNITS ARE IN MILS				
FIGURE	SIZE	TOLERANCE	PLATED	QTY
•	12.0	+3.0/-12.0	PLATED	158
▪	20.0	+3.0/-3.0	PLATED	2
●	40.0	+3.0/-3.0	PLATED	79
◆	43.0	+3.0/-3.0	PLATED	7
□	70.0	+3.0/-3.0	PLATED	6
⊗	100.0	+3.0/-3.0	PLATED	1
△	120.0	+3.0/-3.0	PLATED	2
◬	140.0	+3.0/-3.0	PLATED	1
•	28.0	+3.0/-3.0	NON-PLATED	2
•	36.0	+3.0/-3.0	NON-PLATED	2
+	125.0	+3.0/-3.0	NON-PLATED	4

NOTES:(UNLESS OTHERWISE SPECIFIED)

1. RoHS COMPLIANT CERTIFICATION OR MATERIAL DECLARATION REQUIRED
2. MATERIAL:
   A. IS410 OR EQUIVALENT. MATERIAL MUST CONFORM TO UL94V-0.
   B. USE HTE COPPER, AS SPECIFIED IN THE CROSS SECTION DIAGRAM
   C. OVERALL METAL TO METAL THICKNESS AS SPECIFIED IN THE CROSS SECTION
3. DRILLING:
   A. DIAMETERS IN DRILL TABLE ARE FINISHED HOLE SIZES +/-.003 tol. UNLESS OTHERWISE SPECIFIED IN DRILL TABLE.
   B. TEARDROP ALLOWED ON ENTRY OF VIA ON EVERY TRACE LAYER.
4. PLATING:
   COPPER PLATING IN THRU-HOLES .001 min.
5. MARKING:
   A. SILKSCREEN IN WHITE NON-CONDUCTIVE EPOXY INK ON PRIMARY SIDE OF THE BOARD OR BOTH SIDES IF APPLICABLE.
   B. FABRICATOR TO PLACE DATE CODE AND LOGO ON SECONDARY SIDE IN ETCH (PREFERRED) OR SILKSCREEN.
6. FINAL FABRICATION:
   A. SOLDRMASK PRIMARY AND SECONDARY SIDE OF BOARD USING LIQUID PHOTOIMAGABLE MASK MATERIAL OVER BARE COPPER. PER IPC-SM-840. MASK ARTWORKS PROVIDED ARE 1:1. SOLDERMASK COLOR **GREEN**.
7. FINISH:
   A. SHALL BE ELECTROLYTIC NICKEL/GOLD. ELECTROLESS NIKEL / IMMERSION GOLD. (ENIG)
   B. NICKEL THICKNESS: 100-200 MICROINCHES.
   C. GOLD THICKNESS: 3-10 MICROINCHES.
8. BOARDS SHALL BE PURCHASED FROM UL RECOGNIZED VENDORS ONLY AND SHALL BE MARKED IN COPPER ON SECONDARY SIDE OF BOARD WITH VENDORS UL IDENT. FLAMABILITY RATING (94-V0), DATE CODE (WWYY), AND RoHS COMPILANT SYMBOL.
9. MANUFACTURE BOARD TO BE IN ACCORDANCE WITH PERFORMANCE STANDARD IPC-6011/6012 CLASS 2 BOARD TO BE INSPECTED PER IPC-600-A CLASS 2.
10. MAXIMUM WRAP OR TWIST SHALL NOT EXCEED .010 in/in.
11. TESTING:
   A. FABRICATOR TO ADD TEST STRUCTURES OR CUPONS AS NEEDED.
   B. T-LINE IMPEDENCE TO BE TESTED OR GURANTEED WITHIN 10% IF SPECIFIED IN THE CROSS SECTION. TOLERANCE TO BE +/-10% UNLESS OTHERWISE SPECIFIED.

UNLESS OTHERWISE SPECIFIED
DIMENSIONS ARE IN INCHES
TOLERANCES ON; ANGLES +/- 2°
2 PL DECIMALS +/- .010
3 PL DECIMALS +/- .005
THIRD ANGLE PROJECTIONS

CYPRESS PROPRIETARY
THIS DOCUMENT CONTAINS CONFIDENTAL, PROPRIETARY INFORMATION THAT IS CYPRESS SEMICONDUCTOR PROPERTY. DO NOT DISCLOSE TO OR DUPLICATE FOR OTHERS EXCEPT AS AUTHORIZED BY CYPRESS.

SIGNATURES		DATE
DRAWN	RKPM	12/10/10
CHECKED	RKPM	12/10/10
ENGRG	RKPM	12/10/10
ISSUED		

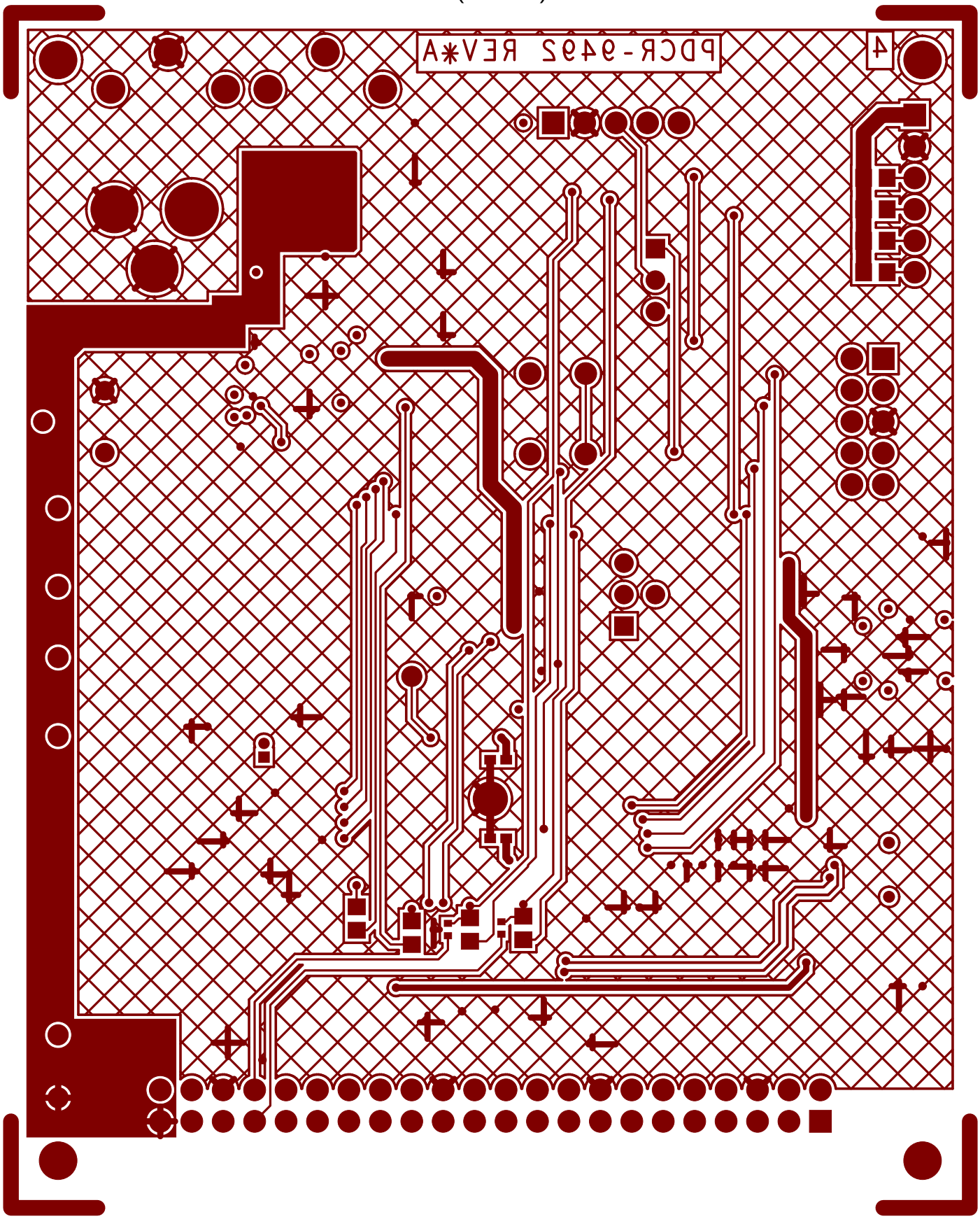
CYPRESS SEMICONDUCTOR
10147 W EMERALD ST.
BOISE, ID 83704
(208) 377-9222

FABRICATION DRAWING
CY3280-20X66 Universal CapSense Controller

SIZE	FSCM NO	DWG NO	REV
C		PDCR-9492	*A

SCALE: 1/1 SHEET:

PDF created with pdfFactory Pro trial version www.pdffactory.com

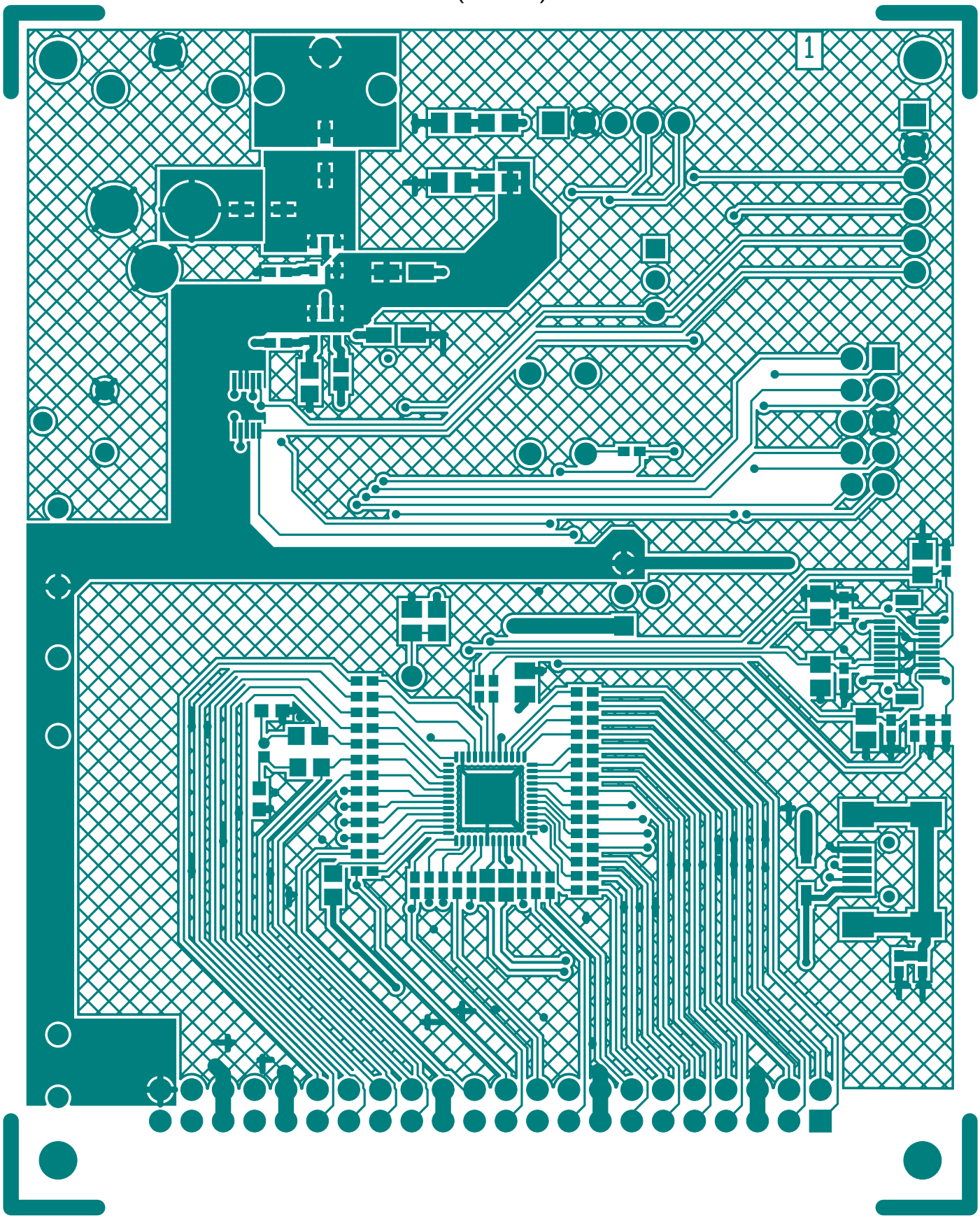


PDCR-9492 REV *A SECONDARY SIDE

PDF created with pdfFactory Pro trial version www.pdffactory.com

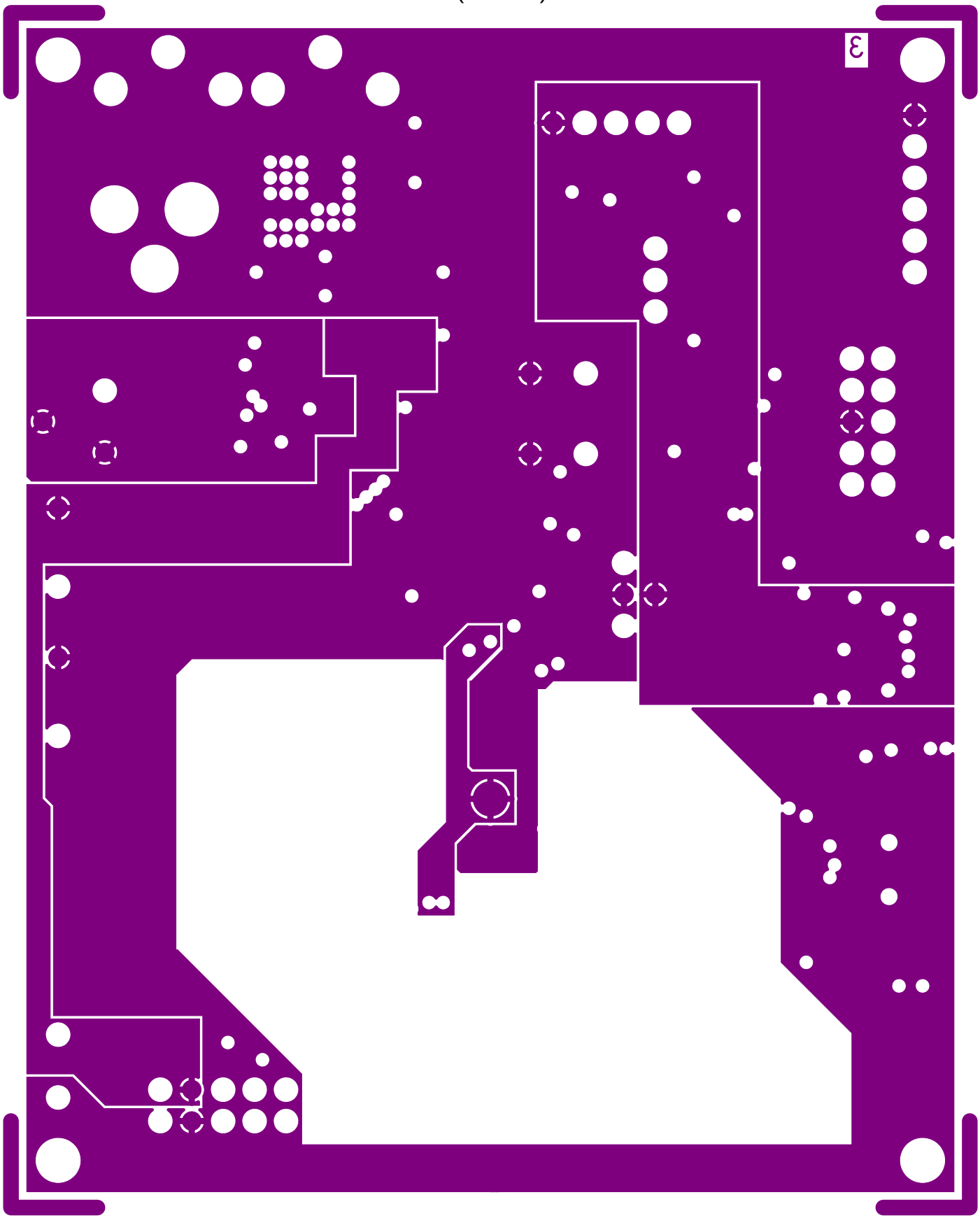
PDCR-9492 REV *A LAYER 2 GND

PDF created with pdfFactory Pro trial version www.pdffactory.com



PDCR-9492 REV *A PRIMARY SIDE

PDF created with pdfFactory Pro trial version www.pdffactory.com



PDCR-9492 REV *A LAYER 3 VCC

PDF created with pdfFactory Pro trial version www.pdffactory.com