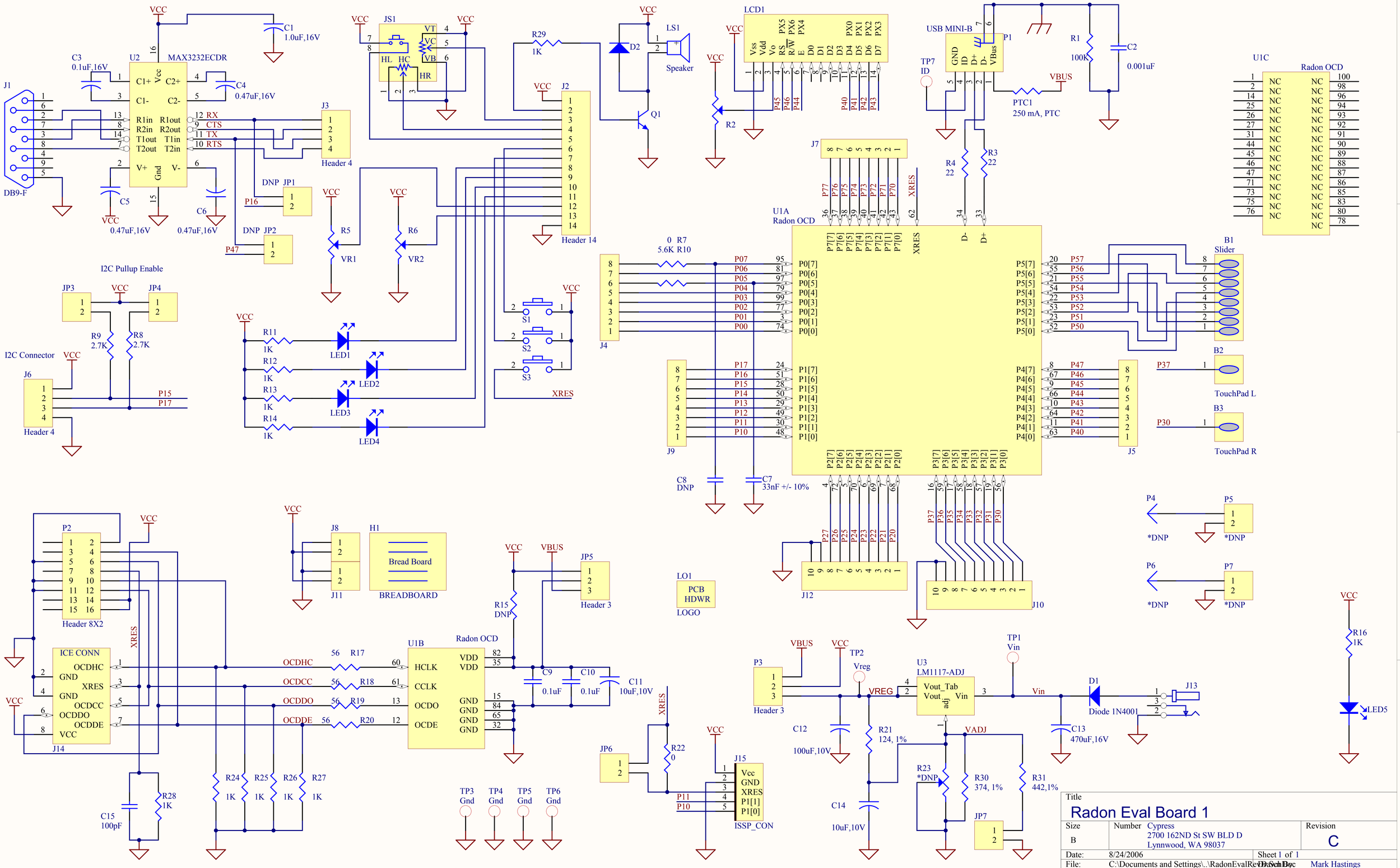

VCC
C1
1.0uF,16V
C3
0.1uF,16V
U2
MAX3232ECDR
C1+
C2+
C1-
C2-
R1in
R2in
T1out
T2out
R1out
R2out
T1in
T2in
V+
V-
Vcc
Gnd
C4
0.47uF,16V
C5
0.47uF,16V
C6
0.47uF,16V
J1
DB9-F
J3
Header 4
12 RX
9 CTS
11 TX
10 RTS
JS1
VT
VC
VB
HL
HC
HR
R29
1K
D2
LS1
Speaker
Q1
J2
Header 14
LCD1
R2
P40
P41
P42
P43
P45
P46
P44
USB MINI-B
P1
GND
ID
D+
D-
VBus
TP7
ID
VBUS
PTC1
250 mA, PTC
R1
100K
C2
0.001uF
R4
22
R3
22
U1C
Radon OCD
DNP JP1
P16
DNP JP2
P47
R5
R6
VR1
VR2
J7
P77
P76
P75
P74
P73
P72
P71
P70
XRES
U1A
Radon OCD
0 R7
5.6K R10
J4
P07
P06
P05
P04
P03
P02
P01
P00
P0[7]
P0[6]
P0[5]
P0[4]
P0[3]
P0[2]
P0[1]
P0[0]
P5[7]
P5[6]
P5[5]
P5[4]
P5[3]
P5[2]
P5[1]
P5[0]
P57
P56
P55
P54
P53
P52
P51
P50
B1
Slider
B2
TouchPad L
B3
TouchPad R
P37
P30
I2C Pullup Enable
JP3
JP4
R9
2.7K
R8
2.7K
I2C Connector
J6
Header 4
P15
P17
R11
R12
R13
R14
1K
LED1
LED2
LED3
LED4
S1
S2
S3
XRES
J9
P17
P16
P15
P14
P13
P12
P11
P10
P1[7]
P1[6]
P1[5]
P1[4]
P1[3]
P1[2]
P1[1]
P1[0]
P4[7]
P4[6]
P4[5]
P4[4]
P4[3]
P4[2]
P4[1]
P4[0]
P47
P46
P45
P44
P43
P42
P41
P40
J5
C8
DNP
C7
33nF +/- 10%
P2[7]
P2[6]
P2[5]
P2[4]
P2[3]
P2[2]
P2[1]
P2[0]
P3[7]
P3[6]
P3[5]
P3[4]
P3[3]
P3[2]
P3[1]
P3[0]
P27
P26
P25
P24
P23
P22
P21
P20
P37
P36
P35
P34
P33
P32
P31
P30
J12
J10
P4
P5
P6
P7
*DNP
LO1
PCB
HDWR
LOGO
P2
Header 8X2
XRES
ICE CONN
OCDHC
GND
XRES
GND
OCDCC
OCDDO
OCDDE
VCC
J14
OCDHC
OCDCC
OCDDO
OCDDE
56 R17
56 R18
56 R19
56 R20
U1B
Radon OCD
HCLK
CCLK
OCDO
OCDE
VDD
GND
C9
0.1uF
C10
0.1uF
C11
10uF,10V
R15
DNP
JP5
Header 3
J8
J11
H1
Bread Board
BREADBOARD
R24
R25
R26
R27
1K
R28
1K
C15
100pF
TP3
TP4
TP5
TP6
Gnd
JP6
R22
0
J15
ISSP_CON
P3
Header 3
VBUS
VCC
TP2
Vreg
U3
LM1117-ADJ
Vout_Tab
Vout
adj
Vin
VREG
VADJ
TP1
Vin
D1
Diode 1N4001
J13
C12
100uF,10V
C13
470uF,16V
R21
124, 1%
R23
*DNP
R30
374, 1%
R31
442,1%
C14
10uF,10V
JP7
LED5
R16
1K
Title
Radon Eval Board 1
Size
B
Number
Cypress
2700 162ND St SW BLD D
Lynnwood, WA 98037
Revision
C
Date:
8/24/2006
Sheet 1 of 1
File:
C:\Documents and Settings\..\RadonEvalRevD.SchDoc
Drawn By:
Mark Hastings