August 3, 2009

Errata for the CY3280-20x66 Universal CapSense Controller Kit

This document describes the errata for the CY3280-20x66 kit. Details include errata trigger conditions, scope of impact, available workarounds, and kit revision applicability.

Contact your local Cypress Sales Representative if you have questions.

Part Numbers Affected

Part Number	Kit Revision Number
CY3280-20x66	**

CY3280-20x66 Qualification Status

Full Release

CY3280-20x66 Errata Summary

The following table defines the errata applicability to the CY3280-20x66 kit.

Items	Applicability	Fix Status
1. CY3240-I2USB Bridge Board Power Detection	Any projects using CY3280-20x66 kit hardware.	No hardware change planned.

1. CY3240-I2USB Bridge Board Power Detection

- **PROBLEM DEFINITION**
  If the CY3240-I2USB bridge board is not configured to supply power to the target device and does not detect power on the target device, then it will report an error when attempting to establish I^2C communication. If no voltage is present on the VCC_PROG net, then the I2C bridge will not detect any power on the CY3280-20x66 board. This can occur if power is supplied from an external source, such as a wall transformer, and J1 is configured to supply power to the PSoC from either the 5V or adjustable regulators.
- **PROJECTS AFFECTED**
  Any projects using CY3280-20x66 kit hardware.
- **TRIGGER CONDITION(S)**
  When the I^2C-USB bridge board is configured to not supply power to the target device, and no voltage is present on the VCC_PROG net.
- **SCOPE OF IMPACT**
  The I^2C-USB bridge board will report an error because no power was detected coming from the CY3280-20x66 board.
- **WORKAROUND**
  Configure the I^2C-USB bridge board to supply power to the target device, or modify the hardware to permanently short the VCC net to the VCC_PROG net.
- **FIX STATUS**
  No hardware change planned.

[+] Feedback

Document History Page

Document Title: CY3280-20x66 Kit Errata		
Rev.	**Orig. of Change**	**Description of Change**
**	GIR	Initial release of spec.

PSoC is a registered trademark of Cypress Semiconductor Corp. "Programmable System-on-Chip," PSoC Designer, and PSoC Express are trademarks of Cypress Semiconductor Corp. All other trademarks or registered trademarks referenced herein are the property of their respective owners.

Cypress Semiconductor
198 Champion Court
San Jose, CA 95134-1709
Phone: 408-943-2600
Fax: 408-943-4730
http://www.cypress.com

© Cypress Semiconductor Corporation, 2009. The information contained herein is subject to change without notice. Cypress Semiconductor Corporation assumes no responsibility for the use of any circuitry other than circuitry embodied in a Cypress product. Nor does it convey or imply any license under patent or other rights. Cypress products are not warranted nor intended to be used for medical, life support, life saving, critical control or safety applications, unless pursuant to an express written agreement with Cypress. Furthermore, Cypress does not authorize its products for use as critical components in life-support systems where a malfunction or failure may reasonably be expected to result in significant injury to the user. The inclusion of Cypress products in life-support systems application implies that the manufacturer assumes all risk of such use and in doing so indemnifies Cypress against all charges.

This Source Code (software and/or firmware) is owned by Cypress Semiconductor Corporation (Cypress) and is protected by and subject to worldwide patent protection (United States and foreign), United States copyright laws and international treaty provisions. Cypress hereby grants to licensee a personal, non-exclusive, non-transferable license to copy, use, modify, create derivative works of, and compile the Cypress Source Code and derivative works for the sole purpose of creating custom software and or firmware in support of licensee product to be used only in conjunction with a Cypress integrated circuit as specified in the applicable agreement. Any reproduction, modification, translation, compilation, or representation of this Source Code except as specified above is prohibited without the express written permission of Cypress.

Disclaimer: CYPRESS MAKES NO WARRANTY OF ANY KIND, EXPRESS OR IMPLIED, WITH REGARD TO THIS MATERIAL, INCLUDING, BUT NOT LIMITED TO, THE IMPLIED WARRANTIES OF MERCHANTABILITY AND FITNESS FOR A PARTICULAR PURPOSE. Cypress reserves the right to make changes without further notice to the materials described herein. Cypress does not assume any liability arising out of the application or use of any product or circuit described herein. Cypress does not authorize its products for use as critical components in life-support systems where a malfunction or failure may reasonably be expected to result in significant injury to the user. The inclusion of Cypress' product in a life-support systems application implies that the manufacturer assumes all risk of such use and in doing so indemnifies Cypress against all charges.

Use may be limited by and subject to the applicable Cypress software license agreement.