Release Notes

CY3274 High Voltage Powerline Communication Development Kit

Release Date, July 23, 2014

Thank you for your interest in the CY3274 High-Voltage Powerline Communication (PLC) Development Kit. This document lists kit contents, installation requirements, limitations, and known issues. It also provides links for updates, support, and additional information.

System Requirements and Recommendations

To evaluate this kit, a second high-voltage CY3274 PLC kit is required. For information on this kit, go to http://www.cypress.com/go/CY3274.

The following are necessary to run the PLC Control Panel GUI for evaluating the PLC kits or to run the PSoC Designer™ development tool for developing your host application on the CY8CPLC20 device. If your computer does not have the minimum versions of .NET Framework and Windows Installer, the CY3274 PLC Kit Installer automatically installs them for you.

Hardware/Operating System Requirements	**Minimum**	**Recommended**
Processor speed	2 GHz	2 GHz Dual Core
RAM	2 GB	3 GB
Free hard drive space	1.3 GB	1.3 GB
Screen resolution	1024x768	1280x1024
USB	Full Speed	2.0 Hi-Speed
Windows XP (SP2 or higher), Vista, Windows 7, or Windows 8	✓	✓
Software Prerequisites	**Minimum**	**Recommended**
Microsoft Internet Explorer	7	
Adobe Reader	6	9+
Windows Installer	3.1	
.NET Framework	2.5	
PSoC Programmer	3.18	
PSoC Designer	5.4	

Kit Contents

The CY3274 High-Voltage Powerline Communication Development Kit includes:

- CY3274 Quick Start Guide
- CY3274 PLC development board
- AC power cable
- MiniProg1 to program CY8CPLC20
- 25 jumper wires
- LCD module
- USB-I^2C bridge

- Retractable USB cable
- Five CY8CPLC20-28PVXI SSOP device samples

Installation

To install the CY3274PLC Kit contents, download the installer package from http://www.cypress.com/go/CY3274 and start the installation. Follow the onscreen procedures to complete the installation.

Updates

Go to http://www.cypress.com/go/CY3274 for the latest software downloads and documents.

Limitations and Known Issues

The following table lists the known issue with the CY3274 Rev. ** board.

	Defect	Workaround
1.	On the I^2C and ISSP header, the silkscreen labels for the data (marked “D”) and clock (marked “C”) pins are swapped. However, the pins have the correct orientation for working with the MiniProg and CY3240 USB-I^2C bridge.	If using these headers for probing, note that the “D” pin has the clock and the “C” pin has the data.

Technical Support

For assistance, go to http://www.cypress.com/go/support or contact our customer support at +1(800) 541-4736 Ext. 2 (in the USA), or +1 (408) 943-2600 Ext. 2 (International).

Additional Information

- For more information on PLC (application notes, software, kits, etc.), visit http://www.cypress.com/go/plc
- For more information about PSoC Designer functionality and releases, review the user guide and release notes on the PSoC Designer web page: http://www.cypress.com/go/psocdesigner
- For more information about PSoC Programmer, supported hardware, and COM layer, visit the PSoC Programmer web page: http://www.cypress.com/go/psocprogrammer
- For a list of trainings on PSoC Designer, visit http://www.cypress.com/?rID=40543

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