CE218964 - PSoC 6 MCU RTC Daily Alarm

Objective

This example demonstrates the RTC Alarm function of the PSoC® 6 MCU Real-Time Clock (RTC).

Overview

This code example demonstrates how to configure RTC registers for a daily alarm using the RTC driver API in the Peripheral Driver Library (PDL). A GPIO output is included for an LED to notify alarm expiration. A UART is used to show the current and alarm times.

Requirements

Tools: PSoC Creator™ 4.2; Peripheral Driver Library (PDL) 3.0.2

Programming Language: C (Arm® GCC 5.4-2016-q2-update, Arm MDK Generic)

Associated Parts: All PSoC 6 MCU parts

Related Hardware: CY8CKIT-062-BLE PSoC 6 BLE Pioneer Kit

Hardware Setups

This example uses the kit's default configuration. Refer to the kit guide to ensure the kit is configured correctly.

Software Setup

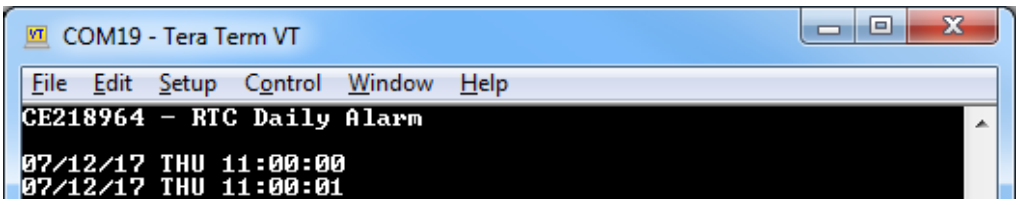
This code example requires a PC terminal software.

Operation

1. Connect the CY8CKIT-062-BLE Pioneer Kit to your computer's USB port.
2. Open a PC terminal using a tool like Tera Term or PuTTY. Configure it for 115,200 baud at data bit 8, no parity and 1 stop bit to match the UART Component.
3. Build the project and program it into the PSoC 6 MCU device. Choose **Debug** > **Program**. For more information on device programming, see PSoC Creator Help. Flash for both CPUs is programmed in a single program operation.

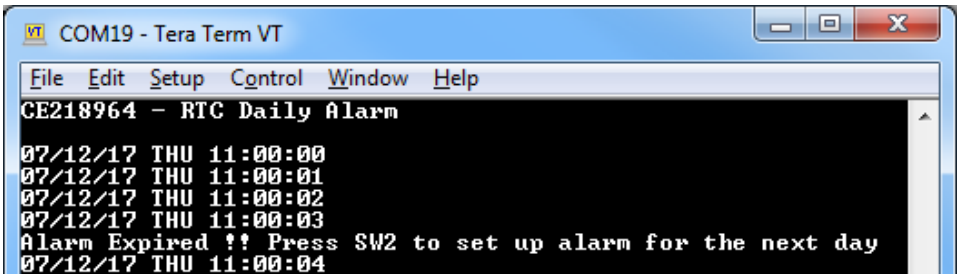
   Note: Do not delete or replace the *stdio_user.h* file, if prompted by PSoC Creator.
4. Confirm that the terminal program is working. It should show a starting message in the terminal window as shown in Figure 1.

Figure 1. UART Display Start Message



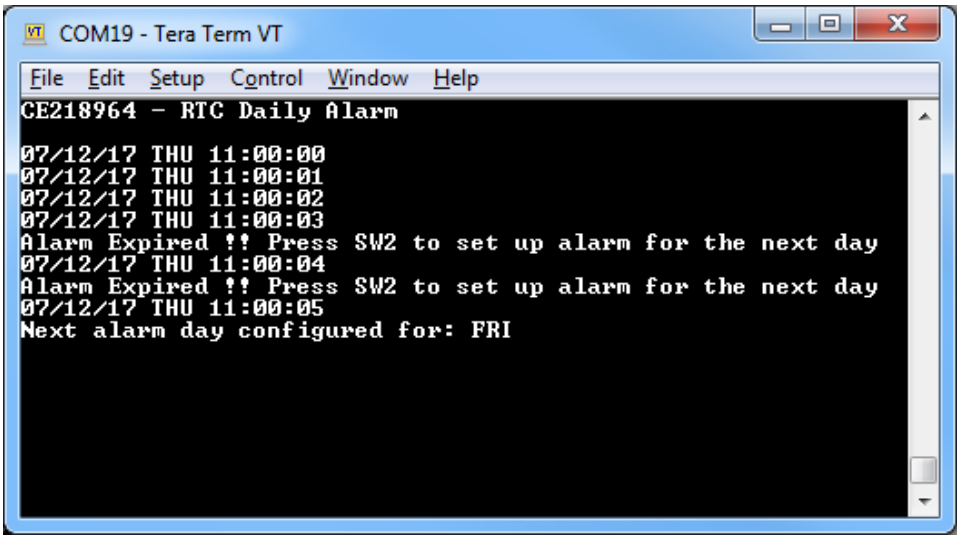
5. Wait three seconds. It should show the message `Alarm Expired !! Press SW2 for to set alarm for next day` after three seconds (Figure 2).

Figure 2. UART Display Message After RTC Alarm



6. Confirm red LED (LED_R) toggles every second.
7. Press switch SW2. It should show the next alarm information as shown in Figure 3.

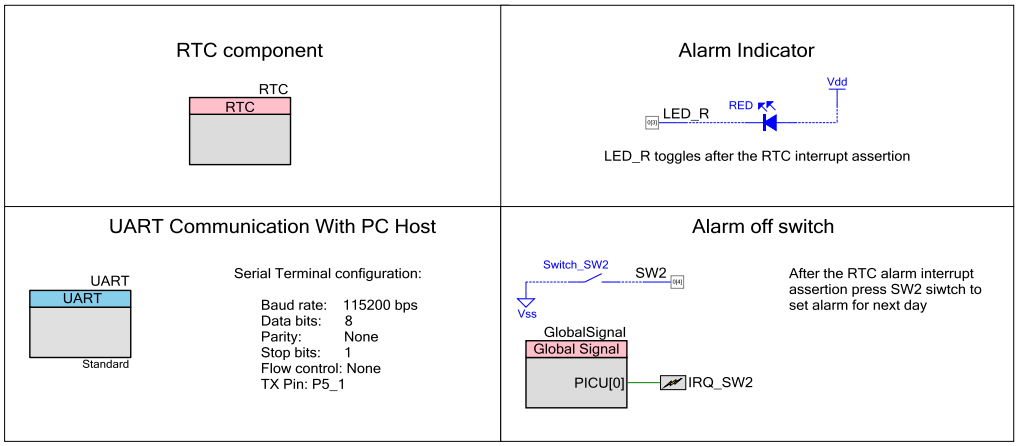
Figure 3. UART Display Message After Switch Is Pressed



Design

As Figure 4 shows, this code example features one RTC, one GPIO for LED alarm indicator, one UART for user interface, and one GPIO for alarm switch SW2.

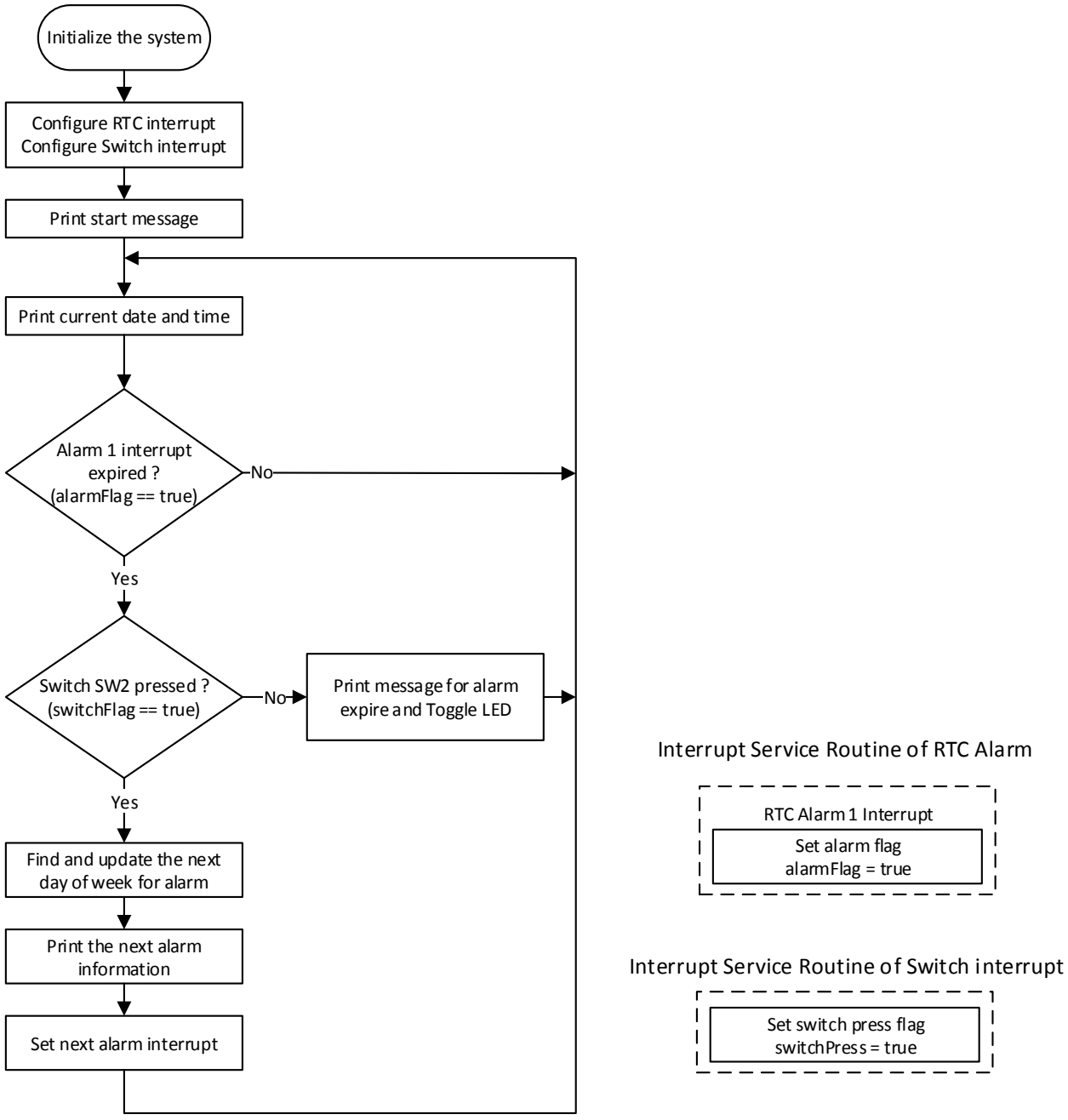
Figure 4. RTC Daily Alarm Schematic



The PSoC 6 RTC is a hardware-based function; the alarm time can be configured by the alarm register fields. The daily alarm needs to enable the hour, minute, and second time fields. Each alarm field is paired with its own enable field. For example, sec (second) field is paired with secEn (second enable) and dayOfWeek field is paired with dayOfWeekEn field. If an enable field is set, the field value will be used for matching the alarm time; otherwise the field value will be ignored. For more information, see PSoC 6 BLE Register Technical Reference Manuals.

As Figure 5 shows, the alarm function uses the RTC alarm 1 interrupt. After an alarm has expired, the code prints the alarm expiration message and toggles the red LED (LED_R, P0[3]) every second until the SW2 button is pressed.

Figure 5. RTC Daily Alarm Flowchart



Components and Settings

Table 1 lists the PSoC Creator Components used in this example, how they are used in the design, and the non-default settings required so they function as intended.

Table 1. List of PSoC Creator Components

Component	Instance name	Propose	Parameter
Real Time Clock	RTC	Provide date and time information	**[General Tab]:** Enable Interrupt: Check
UART (SCB)	UART	used for printing terminal messages	**[General Tab]:** TX/RX Mode: TX only
Digital Output Pin	LED_R	Provide visual feedback	**[General Tab]:** HW connection: Uncheck Drive mode: Strong drive
Digital Input Pin	SW2	Provide user interaction	**[General Tab]:** HW connection: Uncheck Drive mode: Resistive Pull Up
Global Signal Reference	Global Signal	Configure the interrupt	**[General Tab]:** Global signal name: Port interrupt 0 (PICU[0])
Interrupt	IRQ_SW2	Configure the interrupt	Default

Design-Wide / Global Resources

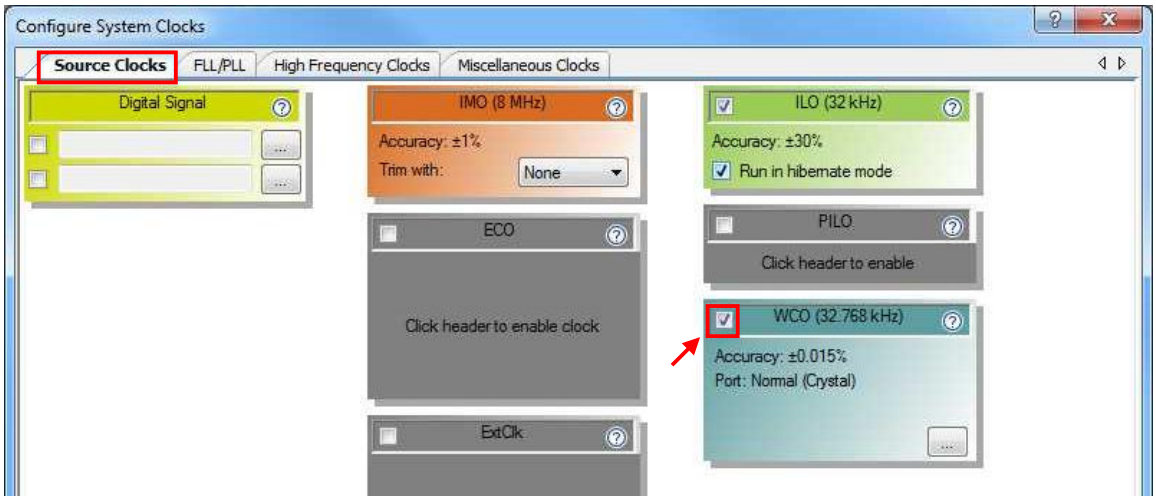
This code example runs on CY8CKIT-062-BLE, which has a PSoC 6 MCU device.

A backup clock is necessary for the RTC to function. For accurate RTC operation, it is recommended that you use a WCO.

Do the following to configure the RTC clock (BakClk) as WCO.

1. Double-click **Clocks** in **Design Wide Resources**.
2. Click **Edit Clock…** and open **Configure System Clocks**.
3. Enable WCO clock for the backup clock source in **Source Clocks**, as Figure 6 shows.

Figure 6. Enable WCO for the RTC Clock



4. Select WCO for BakClk in **Miscellaneous Clocks**, as Figure 7 shows.

Figure 7. Set the Backup Clock Source to WCO

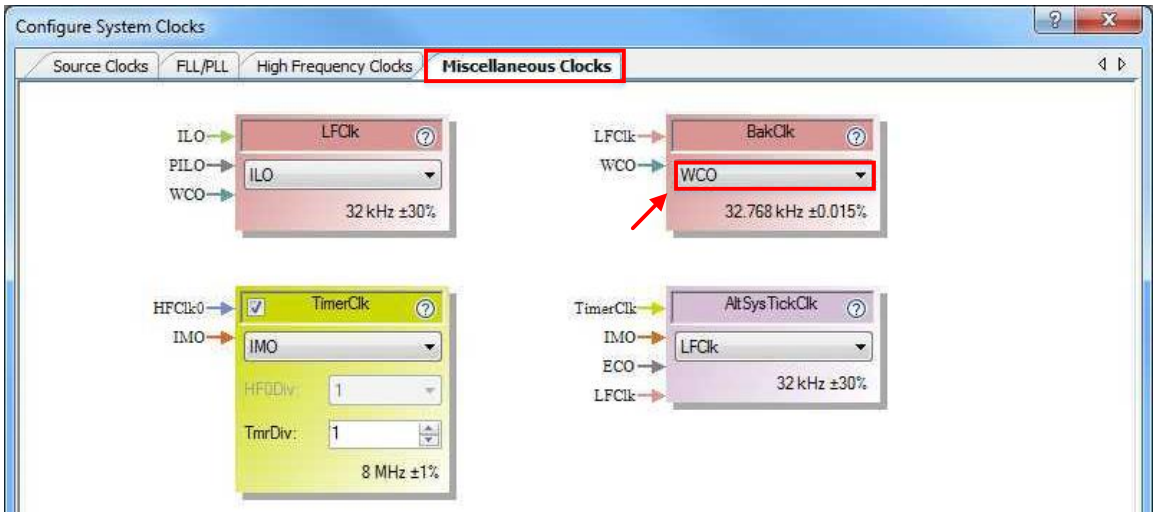


Figure 8 shows the pin assignment for the project done through the **Pins** tab in the **Design Wide Resources** window. These assignments are compatible with CY8CKIT-062-BLE.

Figure 8. Pin Assignments

Name	Port	Pin	Lock
\UART:tx\	P5[1]	K6	☑
LED_R	P0[3]	E3	☑
SW2	P0[4]	F3	☑

Reusing This Example

This example is designed for the CY8CKIT-062-BLE Pioneer Kit. To port the design to a different PSoC 6 MCU device and/or kits, change the target device using the Device Selector and update the pin assignments in the Design Wide Resources Pins settings as needed. For single-CPU PSoC 6 MCU devices, port the code from *main_cm4.c* to *main.c*.

Related Documents

Application Notes	
AN221774 – Getting Started with PSoC 6 MCU	Describes PSoC 6 MCU devices and how to build your first PSoC Creator project
AN210781 – Getting Started with PSoC 6 MCU with BLE Connectivity	Describes PSoC 6 MCU with BLE Connectivity devices and how to build your first PSoC Creator project
Code Examples	
CE216825 PSoC 6 Real-Time Clock Basics	
CE218542 PSoC 6 Customer Tick Timer Using RTC Alarm Interrupt	
CE219339 PSoC 6 MCU - MCWDT and RTC Interrupts (Dual Core)	
PSoC Creator Component Datasheets	
Pins	Supports connection of hardware resources to physical pins
RTC	Component provides an application interface for keeping track of time and date
Device Documentation	
PSoC 6 MCU: PSoC 63 with BLE Datasheet	PSoC 6 MCU: PSoC 63 with BLE Architecture Technical Reference Manual PSoC 6 MCU: PSoC 63 with BLE Registers Technical Reference Manual
Development Kit (DVK) Documentation	
CY8CKIT-062-BLE Pioneer Kit	

Document History

Document Title: CE218964 - PSoC 6 MCU RTC Daily Alarm

Document Number: 002-18964

Revision	ECN	Orig. of Change	Submission Date	Description of Change
*A	5993969	AJYA	12/14/2017	Initial Public Release
*B	6207367	AJYA	06/13/2018	Updated to PSoC Creator 4.2

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