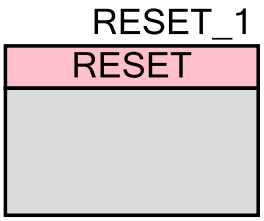
Reset (PDL_RESET) 1.0



Features

- Power-on reset
- INITX input pin
- External power supply/low-voltage detection reset
- Software watchdog reset
- Hardware watchdog reset
- Clock failure detection reset
- Anomalous frequency detection reset
- Software reset
- Deep standby transition reset

General Description

The Peripheral Driver Library (PDL) Reset (PDL_RESET) component determine the cause of a device reset.

This component uses firmware drivers from the PDL_RESET module, which is automatically added to your project after a successful build.

When to Use a PDL_RESET Component

Use the PDL_RESET component when you need to determine the cause of a device reset.

Quick Start

1. Drag a PDL_RESET component from the Component Catalog FMx/System/Reset folder onto your schematic. The placed instance takes the name RESET_1.
2. There is no need to open Configure Dialog. This component doesn't provide any parameters.

3. Build the project to verify the correctness of your design. This will add the required PDL modules to the Workspace Explorer, and generate configuration data for the RESET_1 instance.
4. In the *main.c* file initialize the peripheral and start the application.

```
stc_reset_result_t RESET_1_Result;
Reset_GetCause(RESET_1_Result);
```

5. Build and program the device

Component Usage

After a successful build firmware drivers from the PDL_RESET module, are added to your project in the pdl/drivers/reset folder. Pass the generated data structures to the associated PDL functions in your application initialization code to configure the peripheral.

Generated Data

The PDL_RESET component doesn't generate any peripheral initialization data structure(s).

The application code should use the peripheral functions provided in the referenced PDL files. Refer to the PDL documentation for the list of provided API functions. To access this document, right-click on the component symbol on the schematic and choose "**Open API Documentation…**" option in the drop-down menu.

Code Examples and Application Notes

There are numerous code examples that include schematics and example code available online at the Cypress Code Examples web page.

Cypress also provides a number of application notes describing how FMx devices can be integrated into your design. You can access the Cypress Application Notes search web page at www.cypress.com/appnotes.

Resources

The PDL_RESET component uses the Resets peripheral block.

References

- FM0+ Family of 32-bit ARM® Cortex®-M0+ Microcontrollers Peripheral Manuals
- Cypress FM0+ Family of 32-bit ARM® Cortex®-M0+ Microcontrollers

Component Changes

This section lists the major changes in the component from the previous version.

Version	Description of Changes	Reason for Changes / Impact
1.0	Initial Version	

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